



ALPHA & OMEGA
SEMICONDUCTOR

AO4882

40V Dual N-Channel MOSFET

General Description

The AO4882 uses advanced trench technology to provide excellent $R_{DS(ON)}$ with low gate charge. This is an all purpose device that is suitable for use in a wide range of power conversion applications.

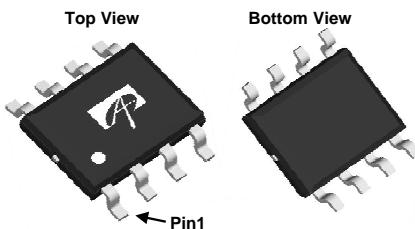
Product Summary

V_{DS}	40V
I_D (at $V_{GS}=10V$)	8A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	< 19mΩ
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	< 27mΩ

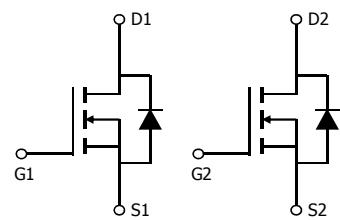
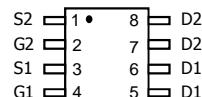
100% UIS Tested
100% R_g Tested



SOIC-8



Top View



Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^A	I_D	8	A
Current ^B		6	
Pulsed Drain Current ^C	I_{DM}	40	
Avalanche Current ^C	I_{AS}	15	A
Avalanche energy $L=0.1mH$ ^C	E_{AS}	11	mJ
Power Dissipation ^B	P_D	2	W
$T_A=70^\circ C$		1.3	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	48	62.5	°C/W
Maximum Junction-to-Ambient ^{A,B}		74	90	°C/W
Maximum Junction-to-Lead	$R_{\theta JL}$	32	40	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	40			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$			1 5	μA
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm20\text{V}$			±100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.4	1.9	2.4	V
$I_{\text{D(ON)}}$	On state drain current	$V_{GS}=10\text{V}, V_{DS}=5\text{V}$	40			A
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=8\text{A}$ $T_J=125^\circ\text{C}$		15.4	19	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=4\text{A}$		22.5	29	
g_{FS}	Forward Transconductance	$V_{DS}=5\text{V}, I_D=8\text{A}$		21	27	$\text{m}\Omega$
V_{SD}	Diode Forward Voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$		0.75	1	V
I_S	Maximum Body-Diode Continuous Current				2.5	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=20\text{V}, f=1\text{MHz}$		415		pF
C_{oss}	Output Capacitance			112		pF
C_{rss}	Reverse Transfer Capacitance			11		pF
R_g	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	1	2.2	3.5	Ω
SWITCHING PARAMETERS						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS}=10\text{V}, V_{DS}=20\text{V}, I_D=8\text{A}$		6.5	12	nC
$Q_g(4.5\text{V})$	Total Gate Charge			3	6	nC
Q_{gs}	Gate Source Charge			1.2		nC
Q_{gd}	Gate Drain Charge			1.1		nC
$t_{\text{D(on)}}$	Turn-On Delay Time	$V_{GS}=10\text{V}, V_{DS}=20\text{V}, R_L=2.5\Omega, R_{\text{GEN}}=3\Omega$		4		ns
t_r	Turn-On Rise Time			3		ns
$t_{\text{D(off)}}$	Turn-Off Delay Time			15		ns
t_f	Turn-Off Fall Time			2		ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=8\text{A}, dI/dt=100\text{A}/\mu\text{s}$		12.5		ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=8\text{A}, dI/dt=100\text{A}/\mu\text{s}$		3.5		nC

A. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design.

B. The power dissipation P_D is based on $T_{J(\text{MAX})}=150^\circ\text{C}$, using $\leqslant 10\text{s}$ junction-to-ambient thermal resistance.

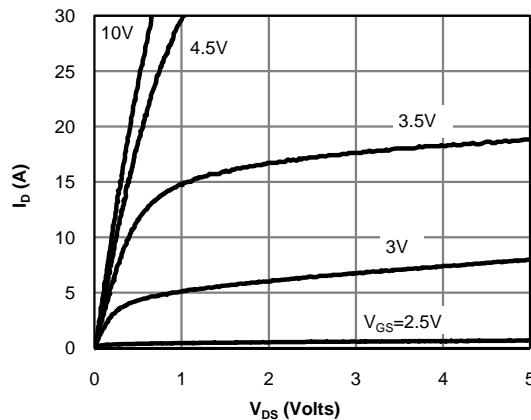
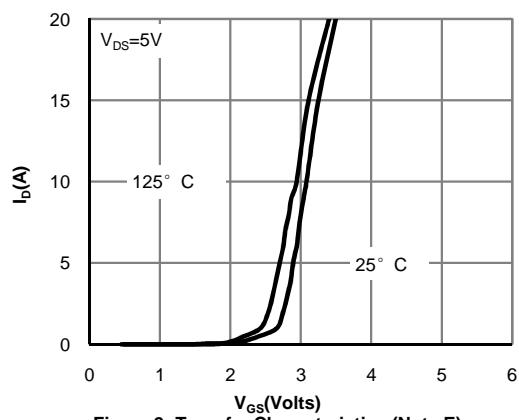
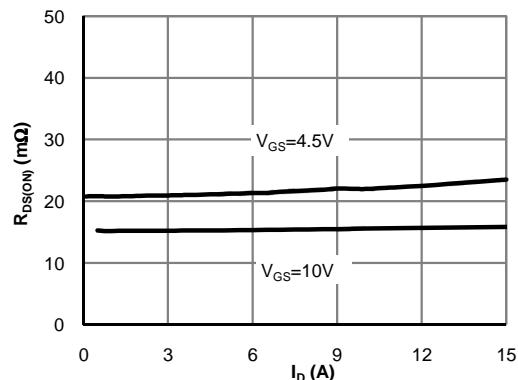
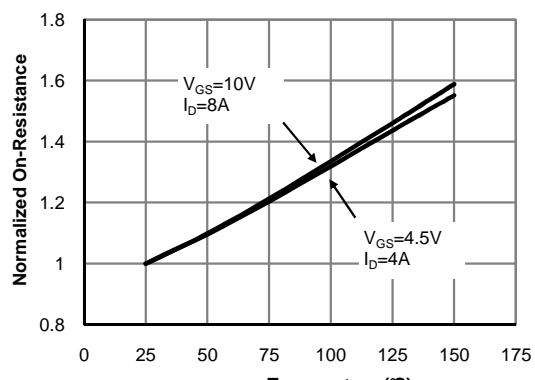
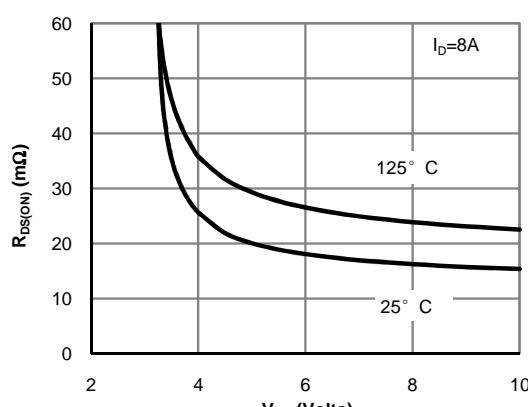
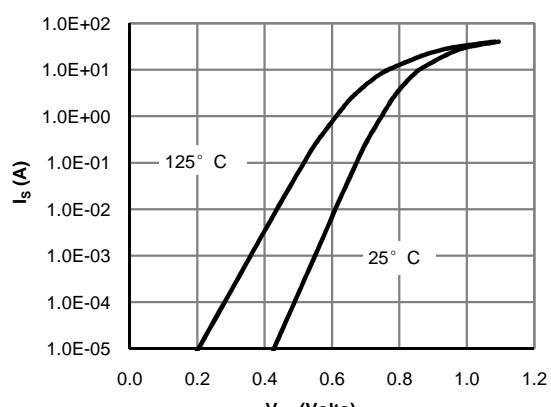
C. Repetitive rating, pulse width limited by junction temperature $T_{J(\text{MAX})}=150^\circ\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^\circ\text{C}$.

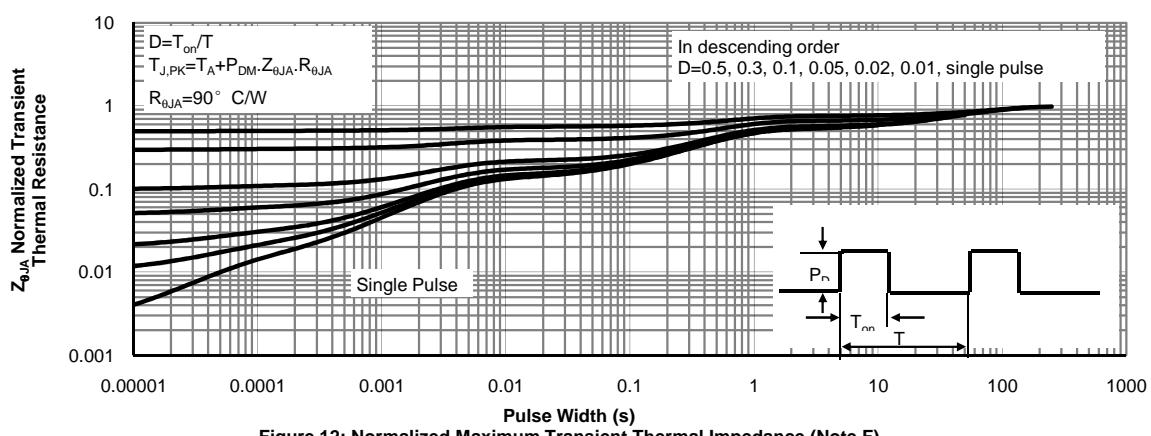
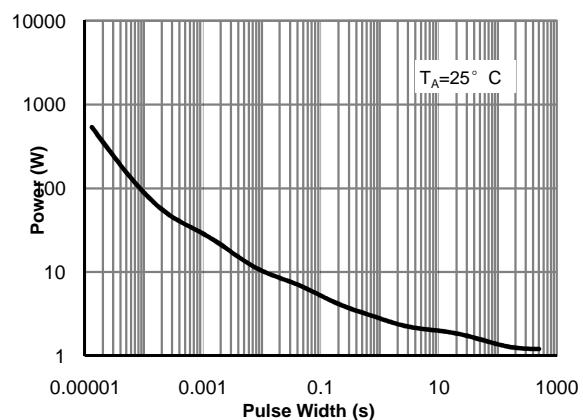
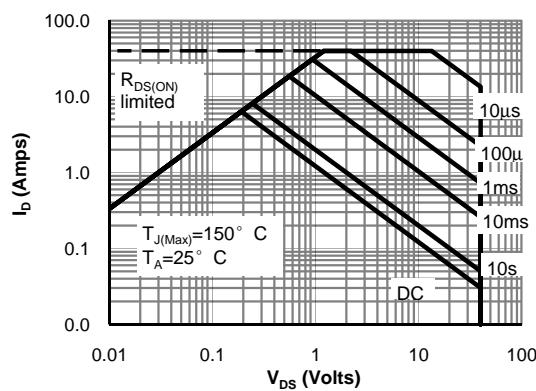
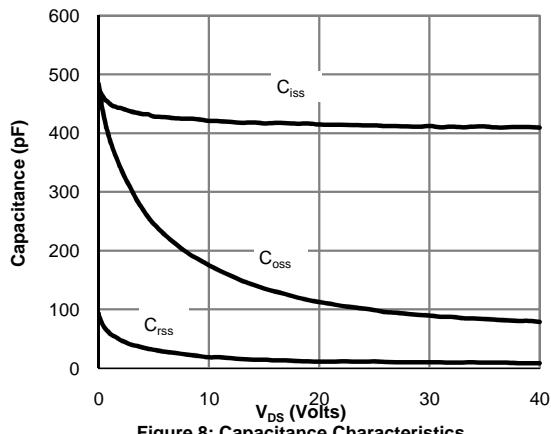
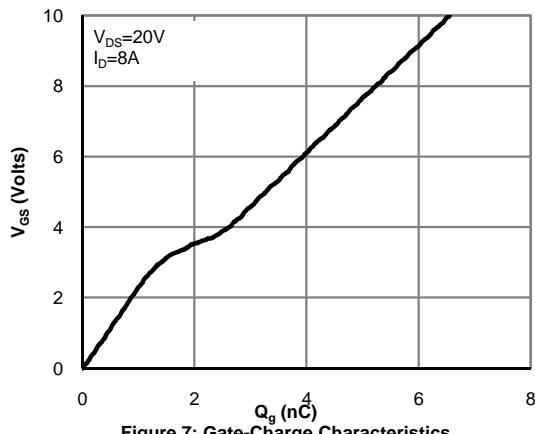
D. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

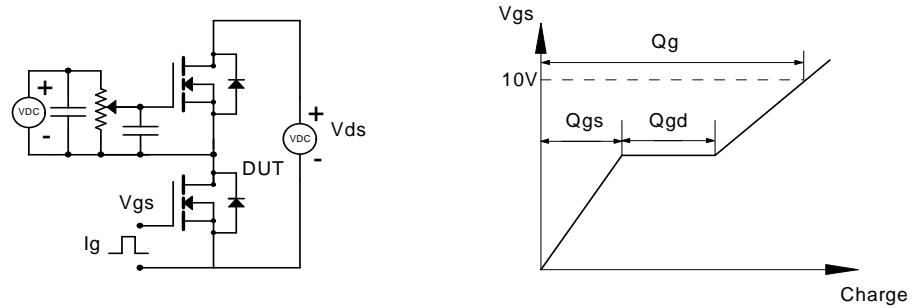
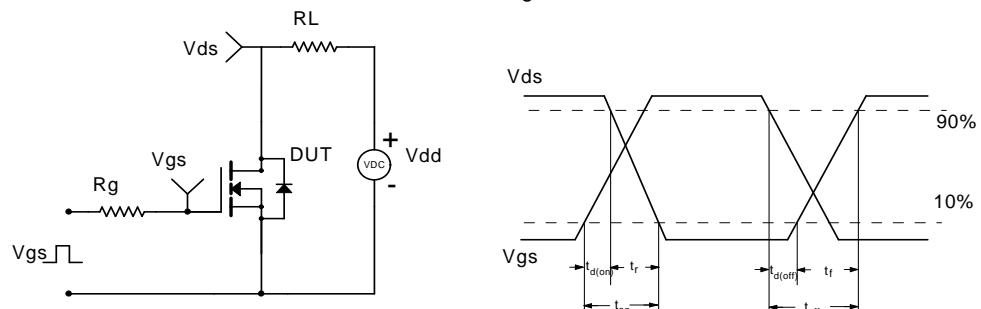
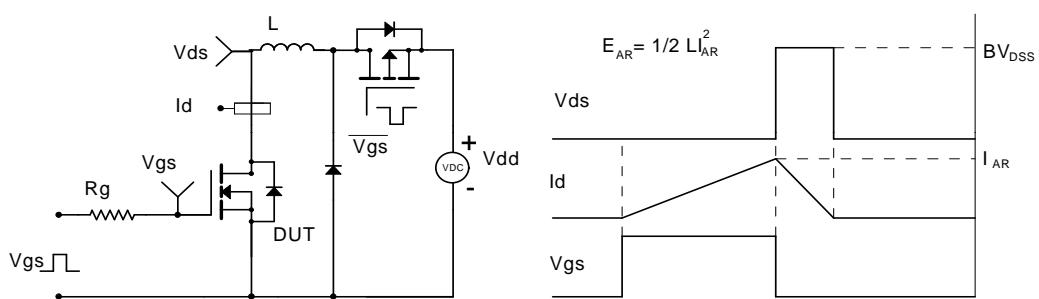
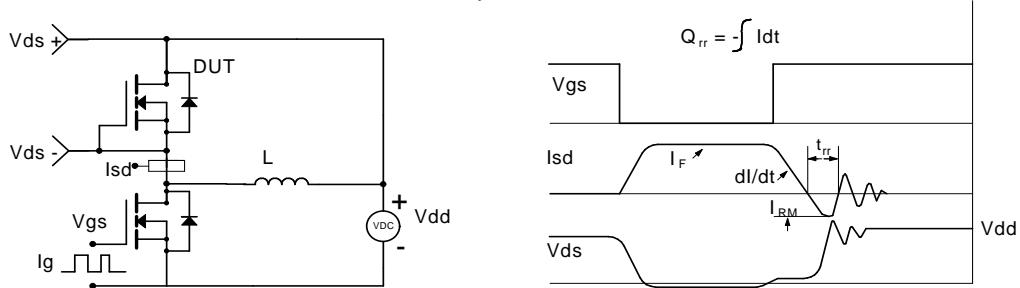
E. The static characteristics in Figures 1 to 6 are obtained using $<300\mu\text{s}$ pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, assuming a maximum junction temperature of $T_{J(\text{MAX})}=150^\circ\text{C}$. The SOA curve provides a single pulse rating.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Fig 1: On-Region Characteristics (Note E)

Figure 2: Transfer Characteristics (Note E)

Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

Figure 4: On-Resistance vs. Junction Temperature (Note E)

Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

Figure 6: Body-Diode Characteristics (Note E)

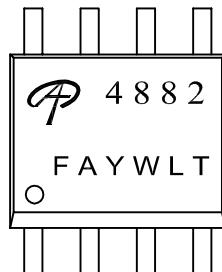
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS


Gate Charge Test Circuit & Waveform

Resistive Switching Test Circuit & Waveforms

Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

Diode Recovery Test Circuit & Waveforms




Document No.	PD-01702
Version	A
Title	AO4882 Marking Description

SO-8 PACKAGE MARKING DESCRIPTION



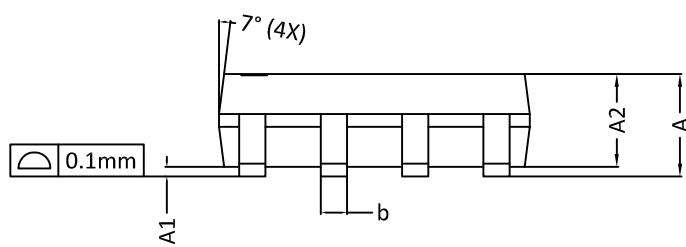
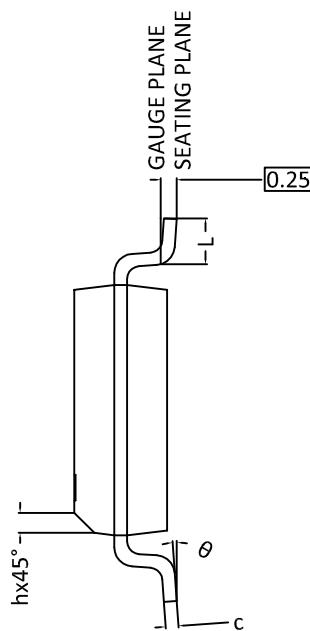
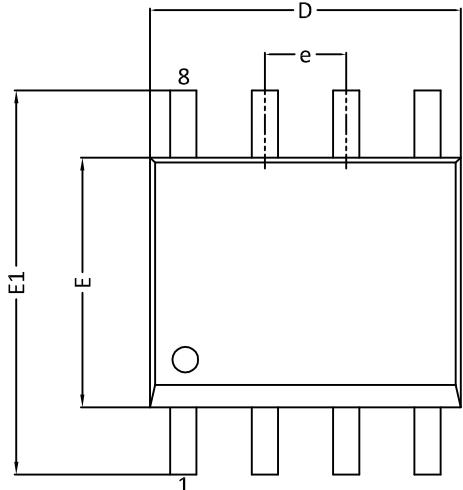
Green product

NOTE:

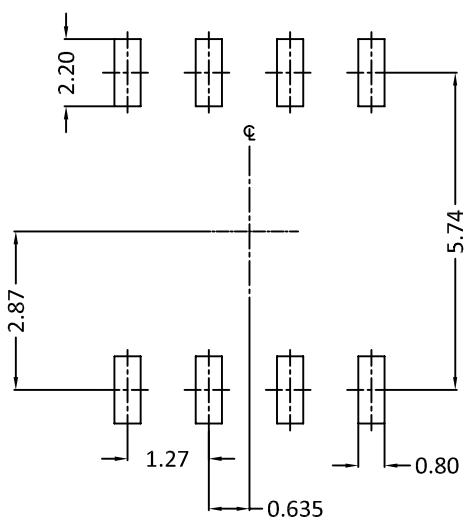
LOGO	- AOS Logo
4882	- Part number code
F	- Fab code
A	- Assembly location code
Y	- Year code
W	- Week code
L&T	- Assembly lot code

PART NO.	DESCRIPTION	CODE
AO4882	Green product	4882
AO4882L	Green product	4882

SO8(SOP-8L) PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

SYMBOLS	DIMENSION IN MM			DIMENSION IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.35	1.65	1.75	0.053	0.065	0.069
A1	0.10	0.15	0.25	0.004	0.006	0.010
A2	1.25	1.50	1.65	0.049	0.059	0.065
b	0.31	0.41	0.51	0.012	0.016	0.020
c	0.17	0.20	0.25	0.007	0.008	0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	3.80	3.90	4.00	0.150	0.154	0.157
E1	5.80	6.00	6.20	0.228	0.236	0.244
e	1.27 BSC			0.050 BSC		
h	0.25	0.30	0.50	0.010	0.012	0.020
L	0.40	0.69	1.27	0.016	0.027	0.050
θ	0°	4°	8°	0°	4°	8°

NOTE

1. ALL DIMENSIONS ARE IN MILLMETERS.
2. DIMENSIONS ARE INCLUSIVE OF PLATING.
3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
4. DIMENSION L IS MEASURED IN GAUGE PLANE.
5. CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



AOS Semiconductor

Product Reliability Report

AO4882, rev B

Plastic Encapsulated Device

ALPHA & OMEGA Semiconductor, Inc

www.aosmd.com

Mar, 2018

This AOS product reliability report summarizes the qualification result for AO4882. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AO4882 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be routine monitored for continuously improving the product quality.

I. Reliability Stress Test Summary and Results

Test Item	Test Condition	Time Point	Total Sample Size	Number of Failures	Reference Standard
HTGB	Temp = 150°C , Vgs=100% of Vgsmx	168 / 500 / 1000 hours	462 pcs	0	JESD22-A108
HTRB	Temp = 150°C , Vds=100% of Vdsmx	168 / 500 / 1000 hours	462 pcs	0	JESD22-A108
Precondition (Note A)	168hr 85°C / 85%RH + 3 cycle reflow@260°C (MSL 1)	-	4620 pcs	0	JESD22-A113
HAST	130°C , 85%RH, 33.3 psia, Vds = 80% of Vdsmx	96 hours	693 pcs	0	JESD22-A110
H3TRB	85°C , 85%RH, Vds = 80% of Vdsmx	1000 hours	693 pcs	0	JESD22-A101
Autoclave	121°C , 29.7psia, RH=100%	96 hours	924 pcs	0	JESD22-A102
Temperature Cycle	-65°C to 150°C , air to air,	1000cycles	924 pcs	0	JESD22-A104
HTSL	Temp = 150°C	1000 hours	693 pcs	0	JESD22-A103
IOL	Δ Tj = 100°C	15000 cycles	693 pcs	0	AEC Q101

Note: The reliability data presents total of available generic data up to the published date.

Note A: MSL (Moisture Sensitivity Level) 1 based on J-STD-020

II. Reliability Evaluation

FIT rate (per billion): 3.82

MTTF = 29919 years

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size. Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

$$\text{Failure Rate} = \text{Chi}^2 \times 10^9 / [2 (N) (H) (Af)] = 3.82$$

$$\text{MTTF} = 10^9 / \text{FIT} = 29919 \text{ years}$$

Chi² = Chi Squared Distribution, determined by the number of failures and confidence interval

N = Total Number of units from burn-in tests

H = Duration of burn-in testing

Af = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

Acceleration Factor [Af] = Exp [Ea / k (1/T_j u - 1/T_j s)]

Acceleration Factor ratio list:

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
Af	259	87	32	13	5.64	2.59	1

T_j s = Stressed junction temperature in degree (Kelvin), K = C+273.16

T_j u =The use junction temperature in degree (Kelvin), K = C+273.16

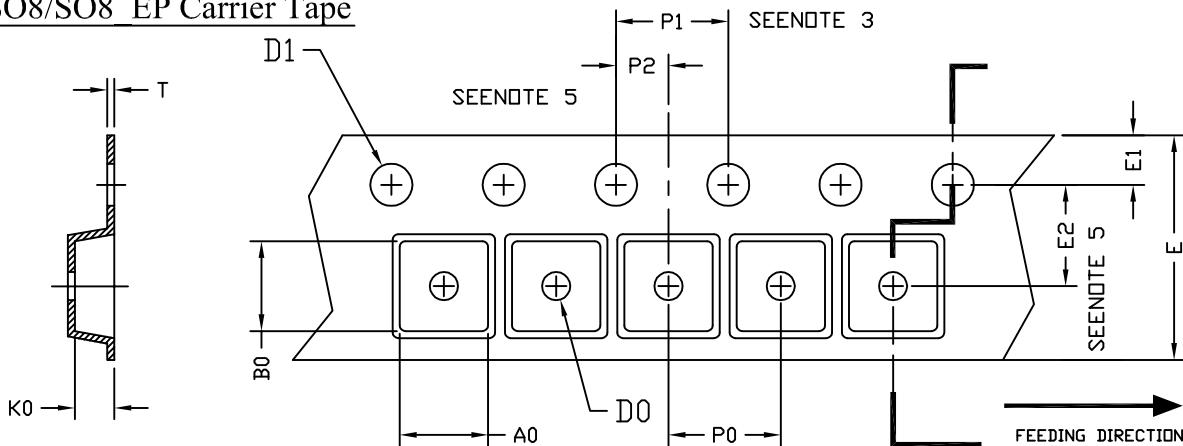
k = Boltzmann's constant, 8.617164 X 10⁻⁵eV / K



ALPHA & OMEGA
SEMICONDUCTOR

SO7/SO8/SO8_—EP Tape and Reel Data

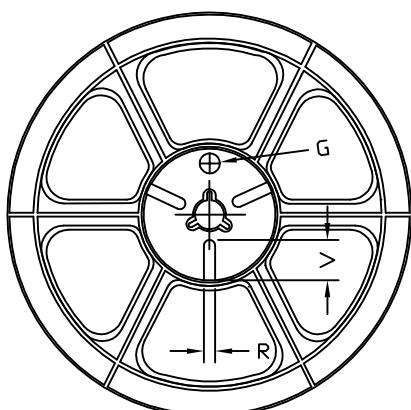
SO7/SO8/SO8 EP Carrier Tape



UNIT: MM

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SO7/SO8-8 (12 mm)	6.40 ± 0.10	5.20 ± 0.10	2.10 ± 0.10	1.60 ± 0.10	1.50 $+0.10$	12.00 ± 0.30	1.75 ± 0.10	5.50 ± 0.05	8.00 ± 0.10	4.00 ± 0.10	2.00 ± 0.05	0.25 ± 0.05

SO7/SO8/SO8 EP Reel



UNIT: MM

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	Ø330	Ø330.00 ± 0.50	Ø97.00 ± 0.10	13.00 ± 0.30	13.00 ± 1.00	Ø13.00 $+0.50$ -0.20	10.60	2.00 ± 0.50	---	---	---

SO7/SO8/SO8 EP Tape

Leader / Trailer
& Orientation

Unit Per Reel:
3000pcs

