

Specification for TFT

AFC8001280A0-8.0INTM-I

Revision A



Α	Orient Display
FC	TFT Type
8001280	Resolution 800 x 1280
A0	Serial A0
8.0	8.0", Module Dimension 184 x 114.5 x 2.57 mm
1	IPS Display
N	Top: -10~+50°C; Tstr: -20~+60°C
Т	Transmissive
М	Medium Brightness, 300 cd/m2
1	MIPI Interface
1	No Touch Panel
1	Controller ILI9881C Or Compatible













Revision History

Date	Rev.	Description	Note	Page
2022-3-2	A	First issue	LCR	

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1. Features

Item	Standard Value
Display Type	800(RGB)*1280 Dots
LCD Type	Color TFT, Transmissive
Screen Size (inch)	8.0
Viewing Direction	Free
Backlight	White
Weight	-
Interface	MIPI
Other (controller/driver IC)	ILI9881C

2. Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	184(L)*114.5(W)*2.57(T)	mm
Active Area	172.224(L)*107.640(W)	mm
Dots Pitch	0.0449*0.1346	mm

3. Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage (Analog)	V _{DDA}	-	-0.3	7.0	٧
Power Supply Voltage (I/O)	V _{DDI}	-	-0.3	3.8	٧
Logic Input Voltage	Vin	-	-0.3	VDDI+0.3	^
Logic Output Voltage	Vo	-	-0.3	VDDI+0.3	
Operating Temperature	Тор	-	-10	50	°C
Storage Temperature	T _{ST}	-	-20	60	°C
Humidity	-	T A≦ 40 ℃	-	90	RH

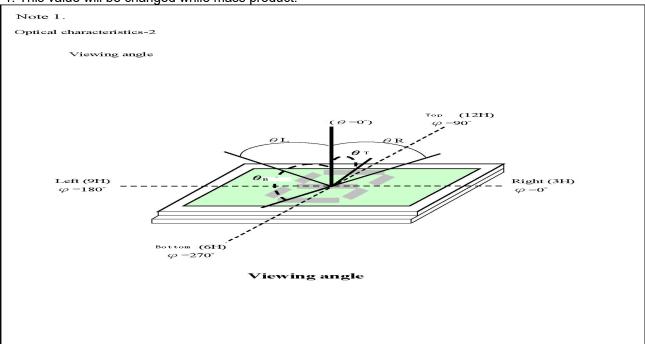
4. DC Electrical Characteristics

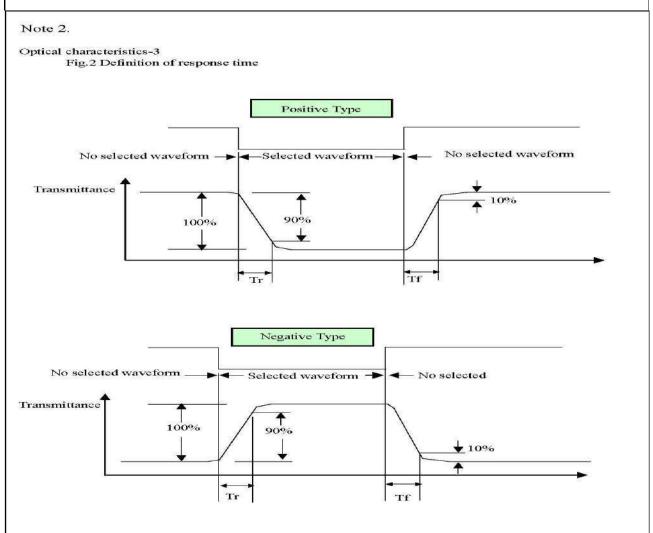
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
System Voltage	V _{DDA}	Operating Voltage	2.5	2.8	6.6	V
Interface Voltage	V_{DDI}	I/O Supply Voltage	1.65	2.8	3.6	V
"H" Input Voltage	ViH	-	0.7 V _{DDI}	-	V _{DDI}	V
"L" Input Voltage	V _{IL}	-	-0.3	-	0.3 V _{DDI}	V
"H" Output Voltage	Vон	IOH = -1.0mA	0.8 V _{DDI}	-	V _{DDI}	V
"L" Output Voltage	Vol	IOL = +1.0mA	0	-	0.2 V _{DDI}	V
Supply Current	1	Normal	-	29	-	mA
Supply Current	I _{DD}	Stand-by	-	0.035	-	mA
TFT gate on voltage	VGH		15.5	16	16.5	V
TFT gate off voltage	VGL		-12.5	-12	-11.5	V
TFT common electrode voltage	Vcom	-	-1.32	-0.82	-0.32	V

5. Optical Characteristics

Ite	m	Symbol	Conditions	Min.	Тур.	Max.	Reference
	Тор	Θу+		75	80	-	
View Angle	Bottom	Θу-	C <u>></u> 10	75	80	-	Notes 1 & 2
View Angle	Left	Өх+	C <u>2</u> 10	75	80	-	Notes I & Z
	Right	Θх-		75	80	•	
	White	Х		-	-	-	
	vviile	Υ		-	-	-	
	Dad	Х		-	-	-	
CIE *1	Red	Y	TA=25°	-	-	-	Notes 5
CIETI	Green	Х	ΘΧ, ΘΥ=0°	-	-	-	Notes 5
		Υ		-	-	-	
	Dive	Х		-	-	-	
	Blue	Υ		-	-	-	
Unifo	rmity	-	-	80%	-	-	Note 4
Contras	st Ratio	Cr	θY = 0°, Ø = 0°	1000	1200	-	Note 3
Surface B	rightness	L	∅ = 0°	-	300cd/m2	-	Note 3 & 4
Color Saturation		NTSC	-	-	56.9%	-	-
Response Time		Tr + Tf	∅ = 0°	-	30ms	35ms	Note 2

*1: This value will be changed while mass product.





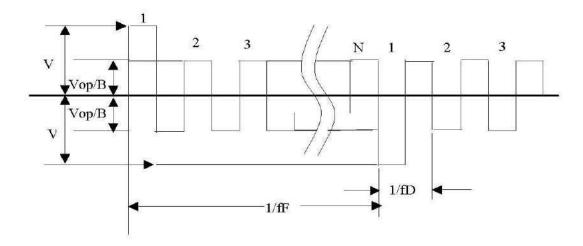
Electrical characteristics-2

※2 Drive waveform

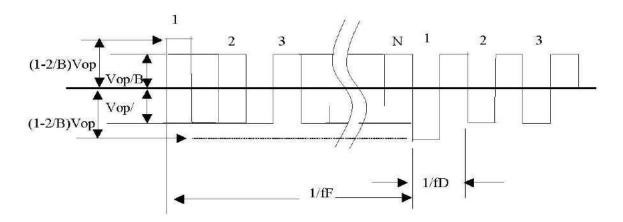
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform

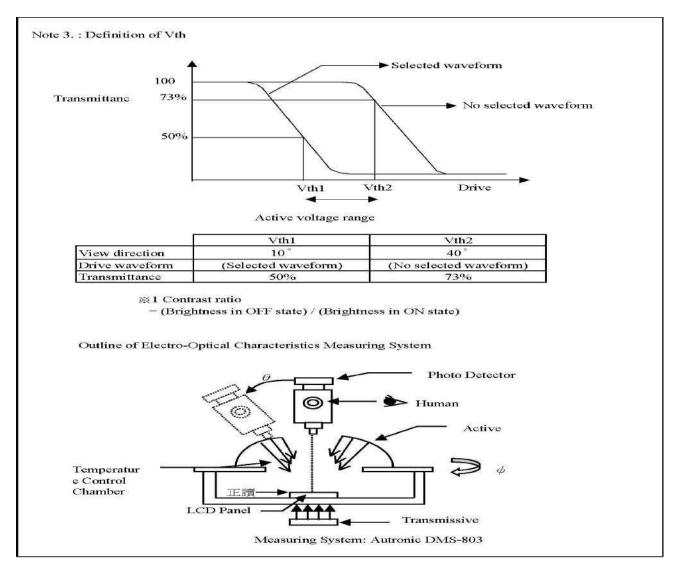


(2) Non- Selected wave form



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period



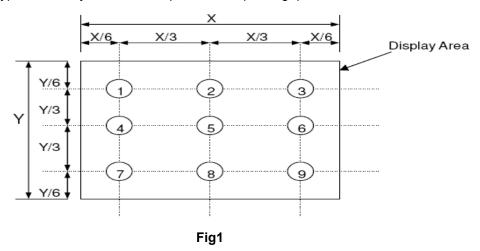
Note 4. Definition of Surface Luminance, Uniformity. (Ref Fig1)

Surface Luminance: LV=average (LP1:LP9)

Uniformity=Minimal (LP1:LP9)/Maximal (LP1:LP9) * 100%

Color Coordinate: The test condition is at IF current of backlight and measured on the surface of LCD module.

Note 5. CIE (x, y) chromaticity is the Center point value. (Ref Fig1)



6. Backlight Characteristics

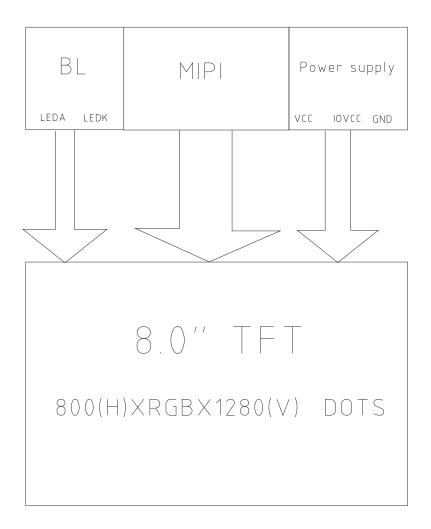
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage (21 LED)	VF	IF=140mA	ı	9.3	-	V
Forward Current	I _F	TA=25°C	-	140	-	mA
LED Life time (50% initial brightness)	Ta=25°C	C, IF=140mA		30000H	minimum	
Color			White			

7. Interface Pin Description

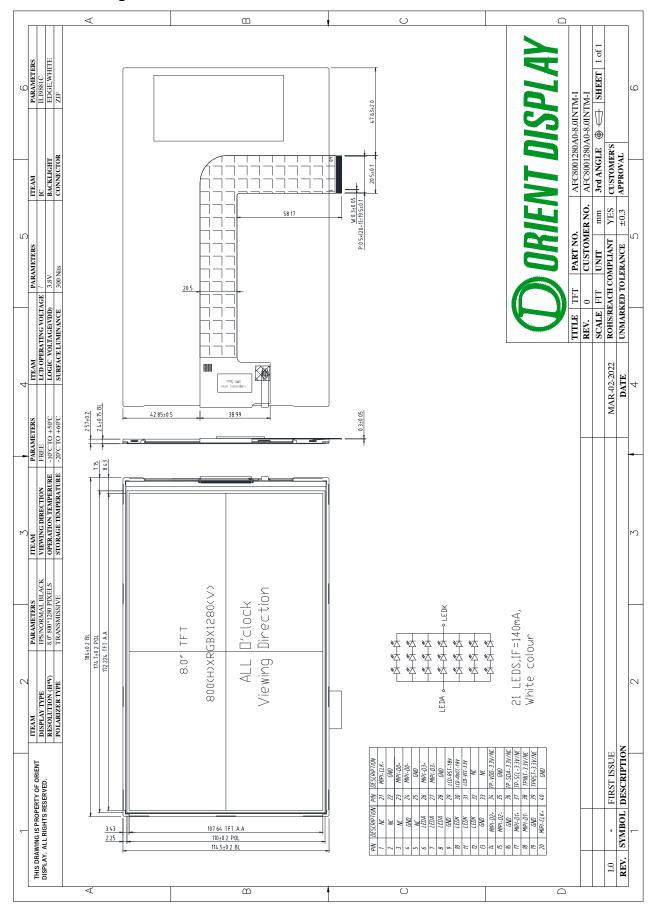
Pin No.	Symbol	Function
1	NC	No Connection
2	NC	No Connection
3	NC	No Connection
4	GND	Ground
5	NC	No Connection
6	LEDA	LED light,anode
7	LEDA	LED light,anode
8	LEDA	LED light,anode
9	GND	Ground
10	LEDK	LED light,cathode
11	LEDK	LED light,cathode
12	LEDK	LED light,cathode
13	GND	Ground
14	MIPI-D2+	MIPI Data differential signal input pins.
15	MIPI-D2-	MIPI Data differential signal input pins.
16	GND	Ground
17	MIPI-D1+	MIPI Data differential signal input pins.
18	DIPI-D1-	MIPI Data differential signal input pins.
19	GND	Ground
20	MIPI-CLK+	MIPI Clock differential signal input pins.
21	MIPI-CLK-	MIPI Clock differential signal input pins.
22	GND	Ground
23	MIPI-D0+	MIPI Data differential signal input pins.
24	MIPI-D0-	MIPI Data differential signal input pins.
25	GND	Ground
26	MIPI-D3+	MIPI Data differential signal input pins.
27	MIPI-D3-	MIPI Data differential signal input pins.
28	GND	Ground
29	LCD-RST-1.8V	Reset signal (1.8V Typ)

30	LCD-IOVCC-1.8V	I/O Power Supply(1.8V Typ)
31	LCD-VCC-3.3V	Power Supply(3.3V Typ)
32	NC	No Connection
33	NC	No Connection
34	TP-VDD-3.3V/NC	CTP Power Supply(3.3V Typ) If not use, please let it open
35	GND	Ground
36	TP-SDA-3.3V/NC	CTP Data signal(3.3V Typ) If not use, please let it open
37	TP-SCL-3.3V/NC	CTP Clock signal(3.3V Typ) If not use, please let it open
38	TPINT-3.3V/NC	CTP Interrupt signal(3.3V Typ) If not use, please let it open
39	TPRST-3.3V/NC	CTP Reset signal(3.3V Typ) If not use, please let it open
40	GND	Ground

8. Block Diagram of Display



9. Count Drawing



10. Timing Characteristics

Please references ILI9881C data sheet.

11. Reliability

NO	Item	Test C	Condition		
1	High Temperature Storage	Storage at 60 ± 2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs			
2	Low Temperature Storage	Storage at -20 ± 2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs			
3	High Temperature Operation	Operation at 50 ± 2°C 240 hrs			
4	Low Temperature Operation	Operation at -10 ± 2°C 240 hrs			
5	High Temperature /Humidity Operating	Operation at 40 ± 2°C, 90%RH 240 hrs surrounding temperature, then storage at normal condition 4hrs.			
6	Thermal shock (non-operation)	-20° C → 25° C → 60° C → 25° C (30mins) (5mins) (5mins) (5mins) 30 Cycle			
7	Mechanical Test	Freq.: 10-55Hz Max. Acceleration: 5G X.Y.Z. each direction For 10 mines. Drop them through			
8	ESD Test	50cm height to strike horizontal plane Air Discharge: Apply +/-6KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15°C~35°C 2. Humidity relative: 30%~60% 3. Energy Storage Capacitance (Cs + Cd): 150pF+/-10% 4. Discharge Resistance (Rd): 330Ω+/-10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: +/-5%)			

Note 1. For humidity test, DI water should be used

Inspection Standard: Inspect after 1-2hrs storage at room temperature, the sample shall be free from the following defects:

Air bubble in the LCD

Seal Leakage

Non-display

Missing Segment

Glass Crack

IDD is greater than twice initial value

Others as per QA Inspection Criteria

Note 2. No defect is allowed after testing.

Note 3. ESD should be applied to LCD glass panel, not other areas (such as on IC and so on)

IDD should be within twice initial value.

In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

Note 4. Only upon request.

12. Specification of Quality Assurance

12.1 Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer by ODNA.

12.2 Standard for Quality Test

ODNA performs the following tests to ensure the quality of product before

shipment. 12.2.1 Sampling Plan:

GB/T2828.1-2003. Single sampling, General Inspection Level II.

Single sampling, normal inspection

12.2.2 Sampling Criteria:

Minor Defect: AQL 0.65%

Major Defect: AQL 0.15%

12.2.3 Defect class defining:

Minor Defect: Not Impact function of product;

Major Defect: Impact the function of product;

Critical Defect: Impact safety, or not conformity with law.

12.2.4 Reliability Test:

Detailed requirement refers to Reliability Test Specification.

- 12.2.5 Nonconforming Analysis & Disposition
- 12.2.6 Nonconforming analysis
 - 12.2.6.1 Customer should provide overall information of non-conforming sample for their complaints.
 - 12.2.6.2 After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.
 - 12.2.6.3 If ODNA can not finish the analysis on time, customer will be notified with the progress status. 12.2.6.4 Disposition of nonconforming:
 - 12.2.6.5 Non-conforming product over ppm level will be replaced.
 - 12.2.6.6 The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

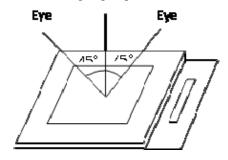
12.3 Agreement Items

ODNA and customer shall negotiate if the following situation occurs

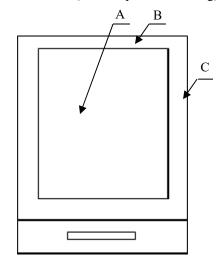
- 12.3.1 There is any discrepancy in standard of quality assurance.
- 12.3.2 Additional requirement to be added in product specification.
- 12.3.3 Any other special problem.

12.4 Standard of the Product Visual Inspection

- 12.4.1 Appearance inspection
 - 12.4.1.1 The normal inspection must be under illumination no less than 800lux, and the distance of view must be between 30cm and 45cm;
 - 12.4.1.2 When inspect the model of transmissive product, back light must be lighted.
 - 12.4.1.3 The visual viewing angle should be 45° from the vertical line without reflection shine or follows customer's viewing angle specifications.



12.4.1.4 Definition of area: (refer to product drawing)



- A. Display Area
- B. Viewing Area
- C. Out of viewing area

12.4.2 Basic principle:

12.4.2.1 According to IPC standards if standard is not described in specification.
12.4.2.2 A set of samples to indicate the limit of acceptable quality level must be discussed by both

ODNA and customer when there is any dispute happened. 12.4.2.3 New item must be added on time when it is necessary.

12.5 Inspection Specification:

No.	Item	Criteria (Unit: mm)						
Functional								
01	LC leakage	LC leakage	Reject	Maj				
02	Non-display	Non-display	Reject	Maj				
03	Missing segment/ Missing character, dot or icon.	OK NG	Reject	Maj				
04	Exceeded dot/line/segment /Distortion	OK NG	Reject	Maj				
05	Dim Display	part of figures display dim than normal obviously	As the samples confirmed each other	Min				
06	Wrong view angle	View angle is different from spec.	Reject	Maj				
07	Not light	1.Backlight not work 2. Some LED not light	Reject	Maj				
08	Backlight Luminance \ uniformity	According to the product specification or limited samples	Reject	Min				

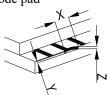
09	Backlight Color	According to the product specification or limited samples		Reject		Min		
10	Connection failure	FPC gold finger damage		Reject		Maj		
Cosn	Cosmetic							
01	Black and white spot, Red, blue and green spot Dent Foreign material (Round type)	$\phi = (a + b)/2$ Distance between 2 defects	$φ \le 0.2$ $0.2 < φ \le 0.3$ $0.30 < φ \le 0.5$ $φ > 0.50$ Total should more than	Zone A B Ignore 3 2 0 5		Min		
02	Black and White line Scratch Foreign material (Line type) (Min)	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		Acc. Q Zone Zone B Ignore 3 2 0 5	ty Zone C NC NC NC NC NC NC NC NC	Min		
		Distance between 2 defects	should more that	Accept Qty				
	Polarizer concave and convex/bubbles	Diameter: Φ(mm)	Zone A	1	ZoneC	-		
		Ф<0.2mm	Ignore					
03		0.2mm<Φ≤0.5mm	4	Ignore Ignore		Min		
		0.5mm<Φ≤0.7mm	2					
		Φ>0.7mm Distance between 2 of	Unacceptal	1	oart.			
04	Bright/Dark dots, lines, Pixel defects	*A dot is defined as a single sub- pixel (either red, green, or blue) within a pixel. Definition of Bright dots: Dots that		Allow		Min		

		can be seen through 6% ND filter.		
		Bright dots	2	
		Adjacent bright dots	0	
		Dark dots	4	
		Adjacent dark dots	0	
		Tiny bright dots	Judge by 6% ND filter, check with 30cm distance, if still can see, NG	
		Mura (50% GERY)	Judge by 6% ND filter, check with 30cm distance, if still can see, NG	
		Dark or Bright Lines	0	
		Blank pixel/ Missing pixel	Not allow	
		Remark: One pixel consists of 3 sub- (Sub-pixel=Dot)	pixels, including R, G and B dot	
		Note 1 Bright dot is defined through 6% trai	nsmission ND filter as following:	
		ND Filter ND Filter	Eye Position 3869 4009 ND Filter Position TFT-LCD Panel iewing area, aren't considered as a	
05	TFT Glass cracked	Z: Chip thickness K T: Glass thickness A: L: Electrode pad length a) General glass chip 1. Chip on panel surface and Z: Chip thickness Y: Ch Z ≤ 1 2T Not exprise viewi	ip width X: Chip length panded in ng area seed 1/3K X≤ 3mm	Min



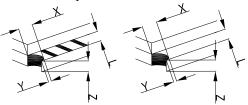
X, Y, Z Spec. same as a.1. If there are 2 or more chips, X is he total length of each chip.

- b) Protrusion over terminal:
 - 1. Chip on electrode pad



Y: Chip width	X: Chip length	Z: Chip thickness
Y≤ 1/4 L	X≤3mm	0 <z≤t< td=""></z≤t<>

2. Non-conductive portion

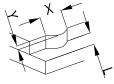


Y: Chip width	X: Chip length	Z: Chip thickness
Y≤ 1/3 L	X≤3mm	0 <z<t< td=""></z<t<>

If the chipped area reaches the ITO terminal, over 2/3 of the ITO must be remained.

If the product will be heat sealed by the customer, the alignment mark must not be damaged.

3. Substrate protuberance and internal crack



Y: Width	X: Length
Y≤1/4L	X≤A

c) LCD with extensive crack line is unacceptable.



06	Discolor/rainbow	Discolor between the LCDs or in the same LCD	Refer to the limit samples signed by customers or ODNA	Min
07	FPC/FFC/TAB/HSC	Connect surface oxidation	Not allow	Maj
		Etching/damage /distortion	Not exceed 1/3 width of wire	Min
		Connect surface contamination /foreign material	Width & length of electric foreign material could not	Min

			exceed the witdth of 2 pins	
		Connect surface scratch	The scratch depth not exceed 1/2 thickness of	Min
			gold layer.	
		Copper/protective film/base board film separates	Not allow	Maj
		Others: FPC follow II	PC-6013A standard.	
		Cold soldering, short soldering	Reject	Min
08	Soldering	Not enough solder paste	Solder paste area < 75% solder pad area Solder paste area < 75% component solder point Solder paste height <1/2 component height	Min
08		FPC Pin deviation	Deviation area > 1/3 Solder pad	Min
		Others: Follow IPC-A-610E standard		
09	COG silicon coating	 a) Not exceed the surface of top polarizer, LCD left/right edges. Cover fully ITO, IC and the juncture of HSC/FPC and LCD. b) No visible non-metal foreign material and metal material in coating c) Entrapped air bubble isn't permissible to exist on the juncture of coating glue and pins of LCD. d) Bubbles or pinhole of silicon coating should Φ≤2mm e) The silicon should cover all around the IC and not gap in between silicon and side of IC, lack of coating on top of IC can 		Min
10	Glass warping	be accepted Product diagonal length (mm) L≤10 10 <l≤30 100<l≤300="" 300<l≤1000<="" 30<l≤100="" td=""><td>Warpage range (mm) 0.05 0.1 0.2 0.4 0.6</td><td>Maj</td></l≤30>	Warpage range (mm) 0.05 0.1 0.2 0.4 0.6	Maj
		1000 <l≤3000< td=""><td>0.8</td><td></td></l≤3000<>	0.8	

11	Color of silk Screen	Refer to limit sample or measurement data base on SPEC			Min
12	Backlight	1. Spots or scratches that appear when light must be judged using LCD glass spot, line and contamination standards.2. Brightness and Chromaticity can't be out of specification.			Min
13	РСВ	13.1 No distortion or contamination on PCB terminals. 13.2 All components on PCB must same as documented on the BOM/component layout. 13.3 Follow IPC-A-600F.			Min
		, ,	on the Bezel.	amination.	Min
		Dent	1 mm > Ø > 0.5 mm, 2↑	Accept	Min
			Ø≤0.5 mm	Ignore	Min
14		material on front	Ø≤0.5 mm ,2↑	Accept	Min
			Dot : Ø≤1.0mm, Line: L≤2.0mm、 W≤0.5mm	Accept	Min
		Scratched	Exposed base metal material	Reject	Min

12.6 ROHS Compliance
The product should RoHS Compliance.

13. Package Specification TBD

14. Precaution for Using LCD Module

14.1 Handing Precaution

- 14.1.1 The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handing especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- 14.1.2 If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- 14.1.3 Do not apply excessive force to the display surface or the adjoining areas since this many cause the color tone to vary. Do not touch the display with bare hands, this will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- 14.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizer with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- 14.1.5 If the display surface becomes contaminated, breathe on the surface and gently wipe it whit a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents, Isotropy alcohol or Ethyl alcohol; do not scrub hard to avoid damaging the display surface.
- 14.1.6 Solvents other than those above-mentioned madly damage the polarizer. Especially, do not use the following: water, Ketone, Aromatic solvents. Wipe off saliva or water drops immediately, contact with water over a ling period of time may cause deformation or color fading. Avoid contact with oil and fats.
- 14.1.7 Exerciser care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or current flow in a high-humidity environment.
- 14.1.8 Install the LCD module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- 14.1.9 Do not attempt to disassemble or process the LCD module.
- 14.1.10 NC terminal should be open. Do not connect anything.
- 14.1.11 If the logic circuit power is off, do not apply the input signals.
- 14.1.12 Electro-Static Discharge control, since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- 14.1.13 Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

14.2 Storage Precaution

- 14.2.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- 14.2.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0° C and 35° C, and keep the relative humidity between 40° RH and 60° RH.
- 14.2.3 The polarizer surface should not come in contact with any other objects (we advise you to store them in anti-static electricity container in which they were shipped).
- 14.2.4 During shipment, please handle with care. The packaging bag can not be broken, step on trap. Packaging Carton layer height can not be over two meters.
- 14.2.5 The transportation process should pay attention to the waterproof and moisture-proof measures. Product can not be watering. Ethylene sealed bags cannot be unsealed.
- 14.2.6 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- 14.2.7 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 14.2.8 To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc.,

14.3 Using LCD Modules

- 14.3.1 The hole in the printed circuit board is used to fix LCM. Attend to the following items when installing the LCM. Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- 14.3.2 When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be +/-0.1mm.
- 14.3.3 Precaution for assemble the module with BTB connector; Please note the position of the male and female connector position.
- 14.3.4 Precaution for soldering the LCM, Manual soldering temperature 300+/-20℃, time is 3~5S.
- 14.3.5 If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- 14.3.6 When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- 14.3.7 When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

14.4 Precautions for Operation

- 14.4.1 Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- 14.4.2 It is an indispensable condition to drive LCD within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD undesirable deterioration, so that the use of direct current drive should be avoid.
- 14.4.3 Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD show dark color in them. However, those phenomena do not mean malfunction or out of order with LCD which will come back in the specified operating temperature.
- 14.4.4 If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and back on.
- 14.4.5 Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
- 14.4.6 Input logic voltage before apply analog high voltage such as LCD driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive & negative voltage becomes stable.
- 14.4.7 Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off many occur with high temperature and high humidity.

14.5 Safety

- 14.5.1 It is recommended to crush damaged or unnecessary LCD into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- 14.5.2 If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

14.6 Limited Warranty

- 14.6.1 Unless otherwise agreed between ODNA display and customer, ODNA display will replace or repair any of its LCD and LCM which ODNA display found to be defective electrically and visually when inspected in accordance with ODNA display quality standards, for a period of one year from date of shipment.
- 14.6.2 The warranty liability of ODNA display is limited to repair and/or replacement. ODNA display will not be responsible for any consequential loss.
- 14.6.3 If possible, we suggest you use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.