# **VCNT2030**



Vishay Semiconductors

# **Reflective Optical Sensor With VCSEL and Transistor Output**



# LINKS TO ADDITIONAL RESOURCES



## FEATURES

- Package type: SMD
- Detector type: phototransistor
- Dimensions (L x W x H in mm): 1.85 x 1.2 x 0.6
- Emitter wavelength: 940 nm
- Moisture sensitivity level (MSL): 3
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

## **APPLICATIONS**

- Position sensor
- Optical switch
- Optical encoder
- Object detection (e.g. paper presence in printer and copy machines)

## DESCRIPTION

The VCNT2030 is a reflective sensor in a miniature SMD package. It has a compact construction where the emitting light source and the detector are arranged in the same plane. The emitter uses a vertical cavity surface emitting laser (VCSEL) chip technology with high radiant intensity, high optical power, and high speed. The operating infrared wavelength is 940 nm. The detector consists of a silicon phototransistor. The sensor's analog output signal at the phototransistor is dependant on the amount of the light emitted by the VCSEL and reflected of an object in the sensor's field of view.





FREE

GREEN

(5-2008)



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### PRODUCT SUMMARY

PRODUCT SUMMART				
PART NUMBER	DISTANCE FOR MAXIMUM CTR <sub>rel</sub> <sup>(1)</sup> (mm)	DISTANCE RANGE FOR I <sub>C</sub> > 0.5 mA (mm)	TYPICAL OUTPUT CURRENT UNDER TEST <sup>(2)</sup> (mA)	DAYLIGHT BLOCKING FILTER INTEGRATED
VCNT2030	0.9	0.3 to 6	2.5	No

#### Notes

 $^{(1)}\,$  CTR: current transfer ratio,  $I_{out}/I_{in}$ 

<sup>(2)</sup> Conditions like in table basic characteristics / sensors

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	VOLUME <sup>(1)</sup>	REMARKS		
VCNT2030	Tape and reel	MOQ: 3000	Drypack, MSL 3		

### Note

<sup>(1)</sup> MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT (VCSEL)					
Reverse voltage		V <sub>R</sub>	5	V	
Forward current		۱ <sub>F</sub>	15	mA	
Power dissipation		P <sub>VCSEL</sub>	38	mW	
Junction temperature		TJ	100	°C	
Thermal resistance junction to ambient	JESD 51	R <sub>thJA</sub>	410	K/W	
OUTPUT (DETECTOR)					
Collector emitter breakdown voltage	I <sub>C</sub> = 0.1 mA, E = 0	V <sub>(BR)CEO</sub>	20	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		Ι <sub>C</sub>	50	mA	
Power dissipation		P <sub>PTR</sub>	100	mW	
Thermal resistance junction to ambient	JESD 51	R <sub>thJA</sub>	380	K/W	
SENSOR					
Total power dissipation		P <sub>tot</sub>	138	mW	
Ambient temperature range		T <sub>amb</sub>	-40 to +85	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +85	°C	
Soldering temperature	In accordance with Fig. 14	T <sub>sd</sub>	260	°C	



Fig. 1 - Power Dissipation vs. Ambient Temperature

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<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT (VCSEL)						
Forward current <sup>(1)</sup>		١ <sub>F</sub>	-	5	-	mA
Converd voltage	I <sub>F</sub> = 8 mA	V <sub>F</sub>	1.7	1.9	2.1	v
Forward voltage	I <sub>F</sub> = 15 mA		-	2.3	-	
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 8 mA	TKV <sub>F</sub>	-	-4	-	mV/K
Angle of half intensity	I <sub>F</sub> = 8 mA	φ	-	17	-	0
Reverse current		I <sub>R</sub>	Not designed for reverse operation			
Peak wavelength	I <sub>F</sub> = 8 mA	λ <sub>P</sub>	-	940	-	nm
OUTPUT (DETECTOR)				•		
Emitter collector voltage	I <sub>E</sub> = 100 μA, E = 0	V <sub>ECO</sub>	7	-	-	V
Collector emitter dark current	$V_{CE} = 5 V, E = 0$	I <sub>CEO</sub>	-	1	100	nA
SENSOR						
Collector current	$V_{CE} = 5 V$ , $I_F = 8 mA$ , $d = 1 mm$	Ι <sub>C</sub>	1.8	2.5	5.4	mA
Current transfer ratio	$I_{\rm C}/I_{\rm F},  {\rm d} = 1  {\rm mm},  {\rm V}_{\rm CE} = 5  {\rm V}$	CTR	-	31	-	%
Rise time	$I_{C}$ = 0.8 mA, $V_{CE}$ = 5 V, $R_{L}$ = 100 $\Omega$	t <sub>r</sub>	-	10	-	μs
Fall time	$I_{C}$ = 0.8 mA, $V_{CE}$ = 5 V, $R_{L}$ = 100 $\Omega$	t <sub>f</sub>	-	15	-	μs

#### Note

<sup>(1)</sup> It is recommended to apply at least 5 mA forward current, to ensure expected device performance



Fig. 2 - Test Circuit



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# BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)



Fig. 3 - Forward Voltage vs. Forward Current



Fig. 4 - Forward Voltage vs. Ambient Temperature



Fig. 5 - Collector Dark Current vs. Ambient Temperature



Fig. 6 - Collector Current vs. Forward Current



Fig. 7 - Relative Collector Current vs. Ambient Temperature





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4 For technical questions, contact: <u>sensorstechsupport@vish</u>

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Fig. 9 - Relative Sensitivity vs. Angular Displacement



Fig. 10 - Relative Spectral Sensitivity vs. Wavelength



Fig. 11 - Relative Collector Current vs. Displacement



Fig. 12 - Rise / Fall Time vs. Collector Current



Fig. 13 - Relative Collector Current vs. Distance

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## **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 3

Floor life: 168 h

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions:

192 h at 40 °C (+ 5 °C), RH < 5 % or 96 h at 60 °C (+ 5 °C), RH < 5 %

## **PRECAUTIONS - EYE SAFETY**

When VCSEL is in operation, looking into laser beam directly by naked eyes, even through a lens, microscope or optical fibers, may cause severe damage to human eyes. For observing laser beams, using safety goggles is recommended.

## LABEL FOR LASER CLASS 1



Note

 Product specification with IEC / EN 60825-1:2014 compliance and above label





Fig. 14 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020



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## **PACKAGE DIMENSIONS** in millimeters





PIN	SIGNAL	
1	Emitter	
2	VCSEL_A	
3	VCSEL_C	
4	Collector	



Not indicated tolerances ± 0.1

Drawing-No.: 6.550-5386.01-4 Issue: 1; 18.07.2022



Technical drawings according to DIN specification

VISHAY, www.vishay.com

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## TAPE AND REEL DIMENSIONS in millimeters



Drawing No.: 9.800-5149.01-4 Issue: 1; 05.12.2019



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