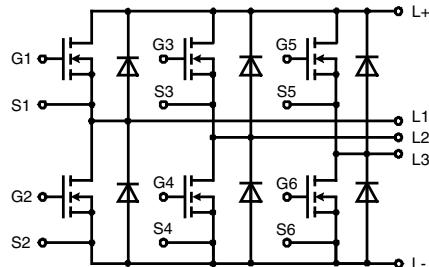


Three phase full Bridge

with Trench MOSFETs
in DCB isolated high current package

V_{DSS} = 100 V
I_{D25} = 90 A
R_{DSon typ.} = 7.5 mΩ



Straight leads

Surface Mount Device

MOSFETs

Symbol	Conditions	Maximum Ratings		
V_{DSS}	T _J = 25°C to 150°C	100	V	
V_{GS}		± 20	V	
I_{D25}	T _C = 25°C	90	A	
I_{D90}	T _C = 90°C	68	A	
I_{F25}	T _C = 25°C (diode)	90	A	
I_{F90}	T _C = 90°C (diode)	68	A	

Symbol Conditions

Symbol	Conditions	Characteristic Values			
		(T _J = 25°C, unless otherwise specified)	min.	typ.	max.
R_{DSon} ¹⁾	on chip level at V _{GS} = 10 V; I _D = 80 A	{ T _J = 25°C T _J = 125°C		7.5 14	8.5 mΩ mΩ
V_{GS(th)}	V _{DS} = 20 V; I _D = 250 μA		2.5		4.5 V
I_{DSS}	V _{DS} = V _{DSS} ; V _{GS} = 0 V	{ T _J = 25°C T _J = 125°C			1 μA mA
I_{GSS}	V _{GS} = ± 20 V; V _{DS} = 0 V				0.2 μA
Q_g Q_{gs} Q_{gd}	{ V _{GS} = 10 V; V _{DS} = 65 V; I _D = 90 A		90 30 30		nC nC nC
t_{d(on)} t_r t_{d(off)} t_f	{ inductive load V _{GS} = 10 V; V _{DS} = 48 V I _D = 70 A; R _G = 33 Ω; T _J = 125°C		130 95 290 55		ns ns ns ns
E_{on} E_{off} E_{recoff}			0.4 0.4 0.007		mJ mJ mJ
R_{thJC} R_{thJH}	with heat transfer paste (IXYS test setup)		1.3	1.0 1.6	K/W K/W

¹⁾ V_{DS} = I_D · (R_{DS(on)} + 2R_{Pin to Chip})

Applications

AC drives

- in automobiles
 - electric power steering
 - starter generator
- in industrial vehicles
 - propulsion drives
 - fork lift drives
- in battery supplied equipment

Features

- MOSFETs in trench technology:
 - low R_{DSon}
 - optimized intrinsic reverse diode
- package:
 - high level of integration
 - high current capability 300 A max.
 - aux. terminals for MOSFET control
 - terminals for soldering or welding connections
 - isolated DCB ceramic base plate with optimized heat transfer
- Space and weight savings

Package options

- 2 lead frames available
 - straight leads (SL)
 - SMD lead version (SMD)

Source-Drain Diode

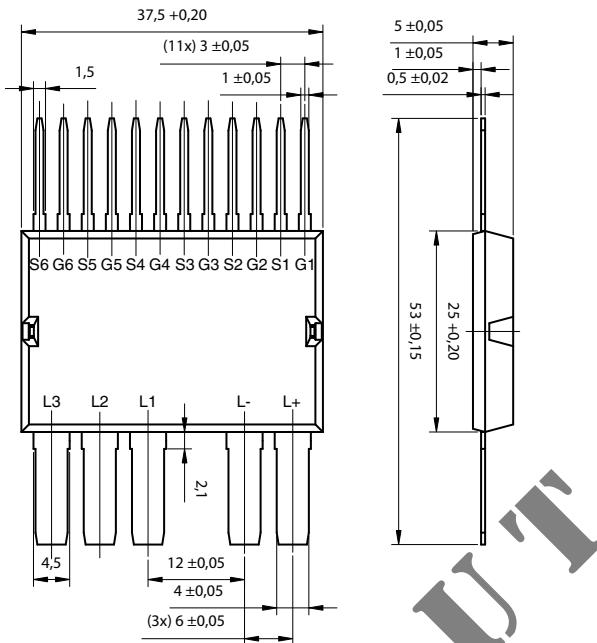
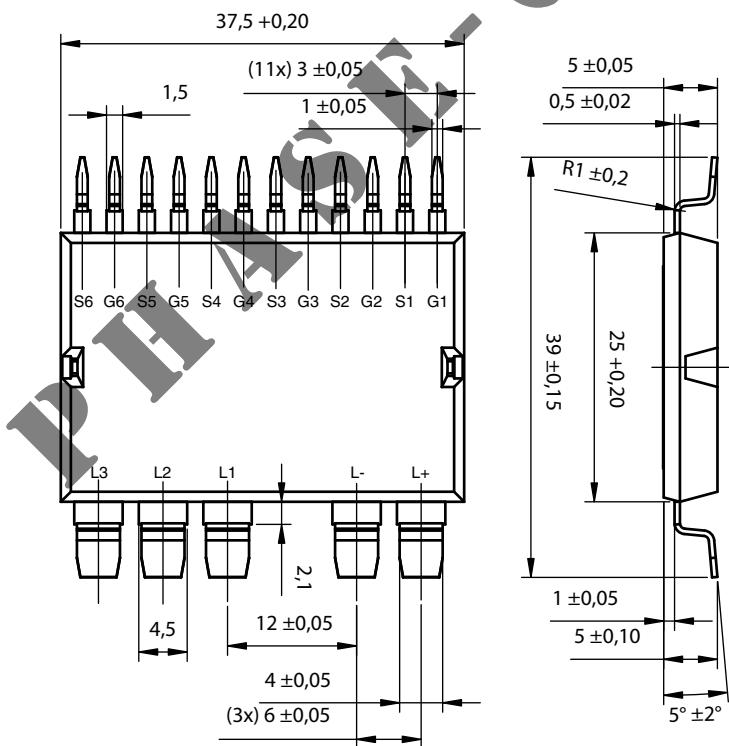
Symbol	Conditions	Characteristic Values		
		(T _J = 25°C, unless otherwise specified)		
		min.	typ.	max.
V _{SD}	(diode) I _F = 70 A; V _{GS} = 0 V	0.9	1.2	V
t _{rr} Q _{RM} I _{RM}	I _F = 70 A; -di _F /dt = 800 A/μs; V _R = 48 V	55 0.95 33		ns μC A

Component

Symbol	Conditions	Maximum Ratings	
I _{RMS}	per pin in main current paths (P+, N-, L1, L2, L3) may be additionally limited by external connections	300	A
T _J		-55...+175	°C
T _{stg}		-55...+125	°C
V _{ISOL}	I _{ISOL} ≤ 1 mA, 50/60 Hz, f = 1 minute	1000	V~
F _c	mounting force with clip	50 - 250	N

Symbol	Conditions	Characteristic Values		
		min.	typ.	max.
R _{pin to chip} ¹⁾			0.6	mΩ
C _P	coupling capacity between shorted pins and mounting tab in the case		160	pF
Weight			25	g

¹⁾ V_{DS} = I_D·(R_{DS(on)} + 2R_{Pin to Chip})

Straight Leads GWM 100-01X1-SL

Surface Mount Device GWM 100-01X1-SMD


Leads	Ordering	Part Name & Packing Unit Marking	Part Marking	Delivering Mode	Base Qty.	Ordering Code
Straight	Standard	GWM 100-01X1 - SL	GWM 100-01X1	Blister	28	505 535
SMD	Standard	GWM 100-01X1 - SMD	GWM 100-01X1	Blister	28	505 542

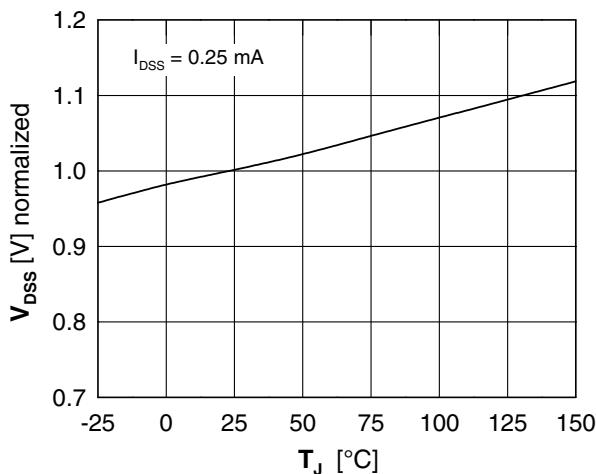


Fig. 1 Drain source breakdown voltage V_{DSS} vs. junction temperature T_J

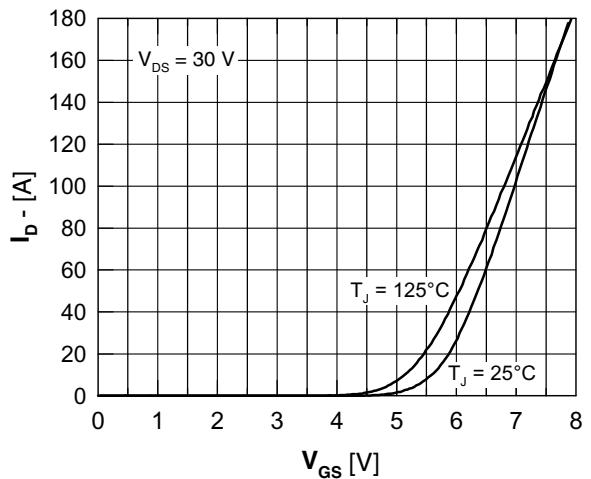


Fig. 2 Typical transfer characteristic

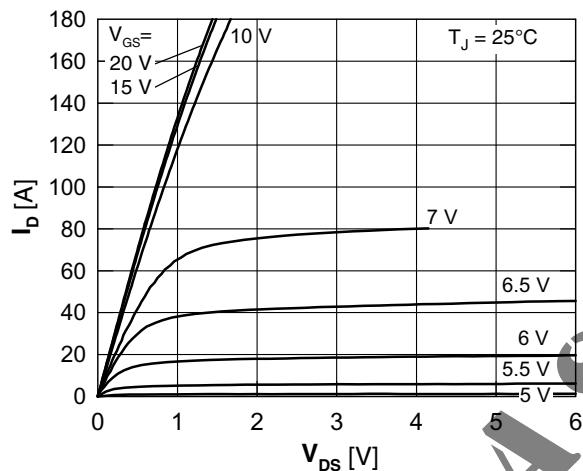


Fig. 3 Typical output characteristic

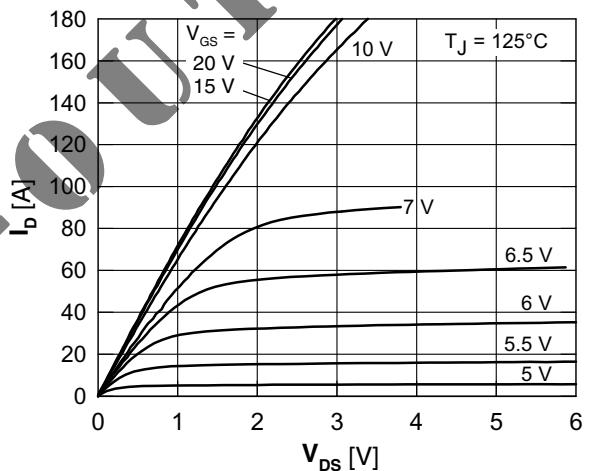


Fig. 4 Typical output characteristic

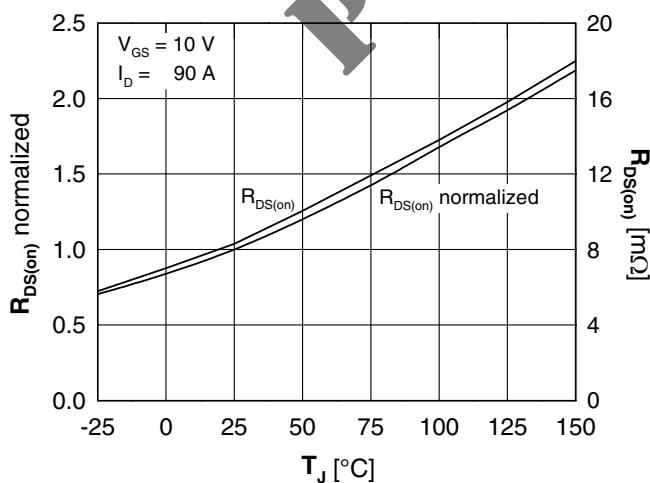


Fig. 5 Drain source on-state resistance $R_{DS(on)}$ versus junction temperature T_J

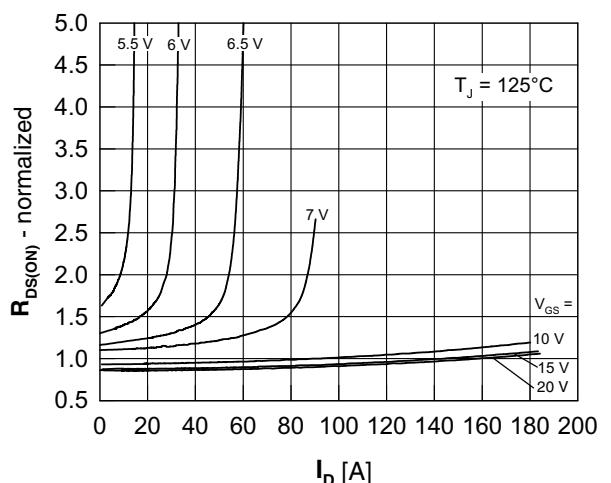


Fig. 6 Drain source on-state resistance $R_{DS(on)}$ versus I_D

IXYS reserves the right to change limits, test conditions and dimensions.

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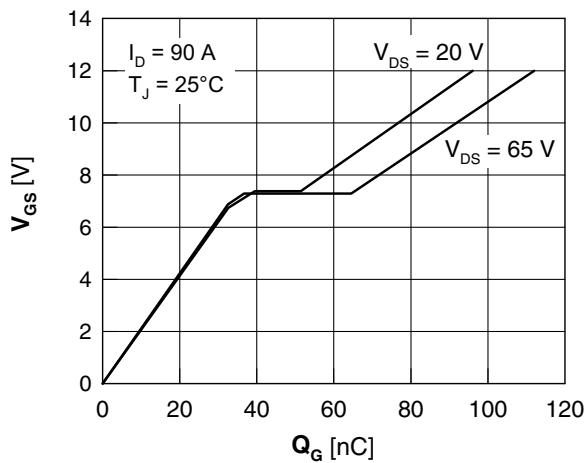


Fig. 7 Gate charge characteristic

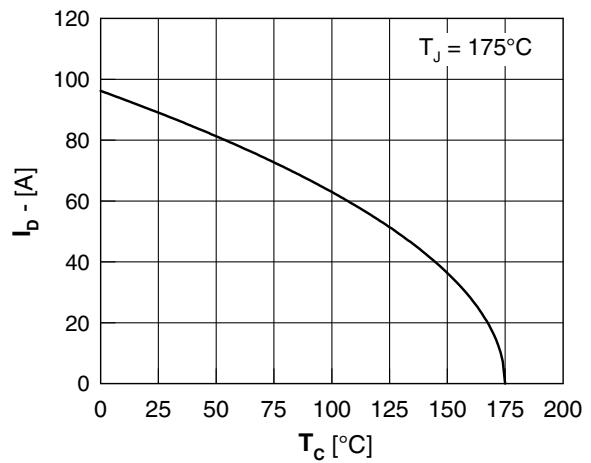
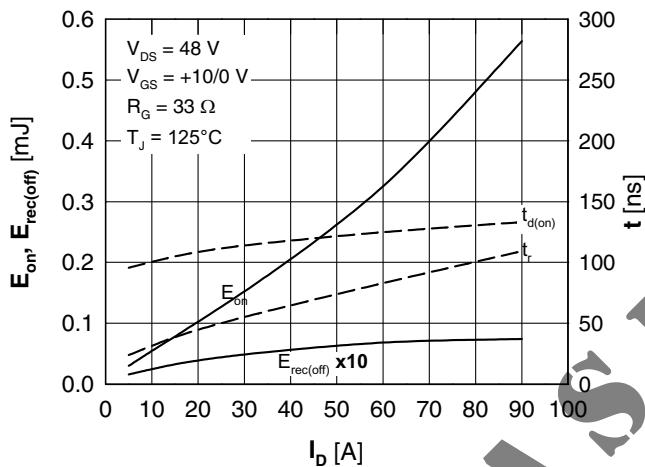
Fig. 8 Drain current I_D vs. case temperature T_c 

Fig. 9 Typ. turn-on energy & switching times vs. collector current, inductive switching

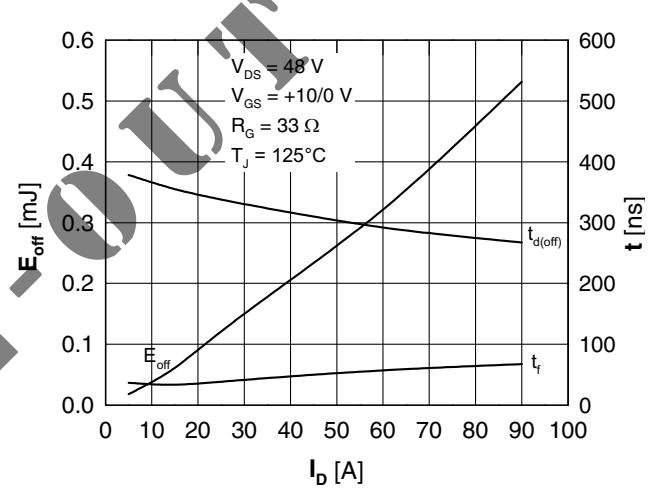


Fig. 10 Typ. turn-off energy & switching times vs. collector current, inductive switching

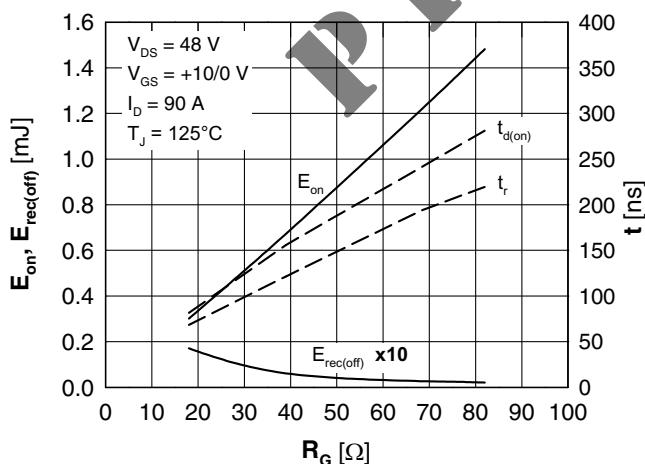


Fig. 11 Typ. turn-on energy & switching times vs. gate resistor, inductive switching

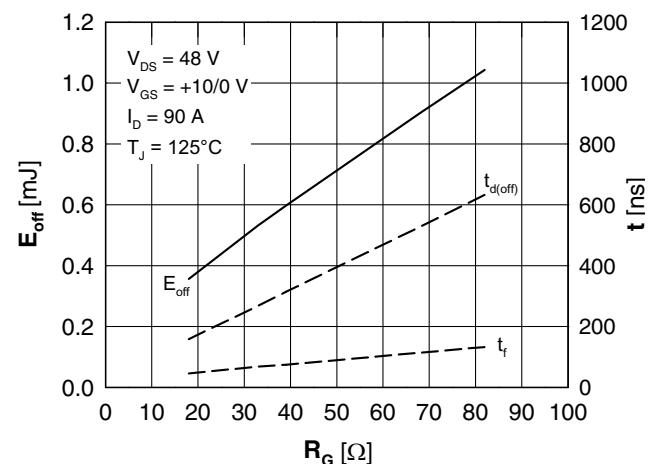


Fig. 12 Typ. turn-off energy & switching times vs. gate resistor, inductive switching

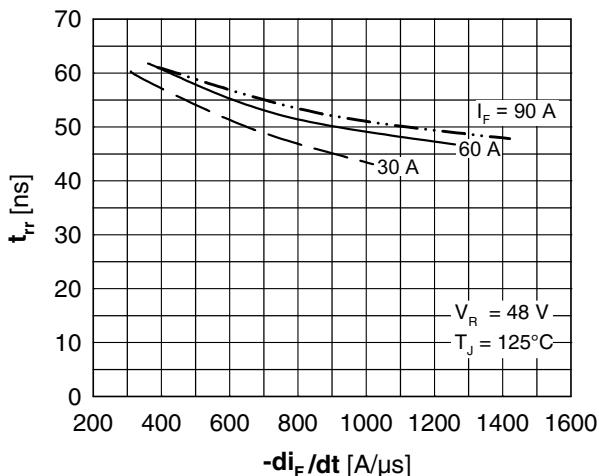


Fig. 13 Reverse recovery time t_{rr} of the body diode vs. di/dt

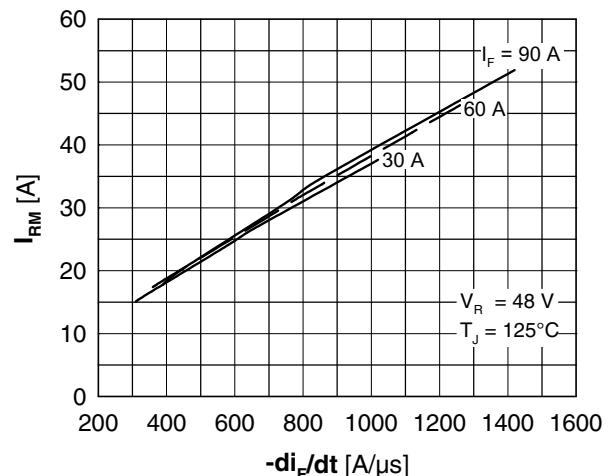


Fig. 14 Reverse recovery current I_{RM} of the body diode vs. di/dt

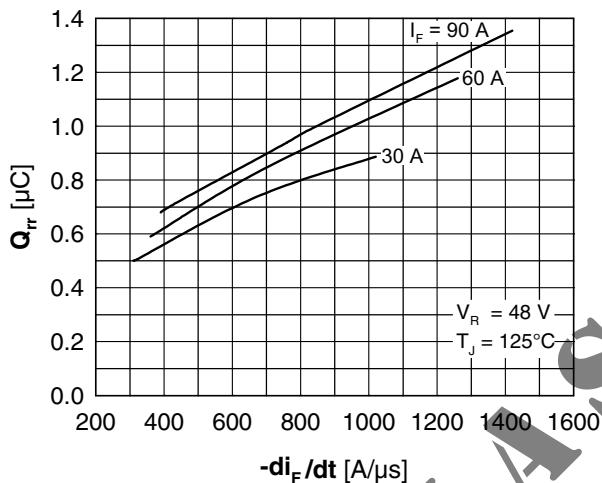


Fig. 15 Reverse recovery charge Q_{rr} of the body diode vs. di/dt

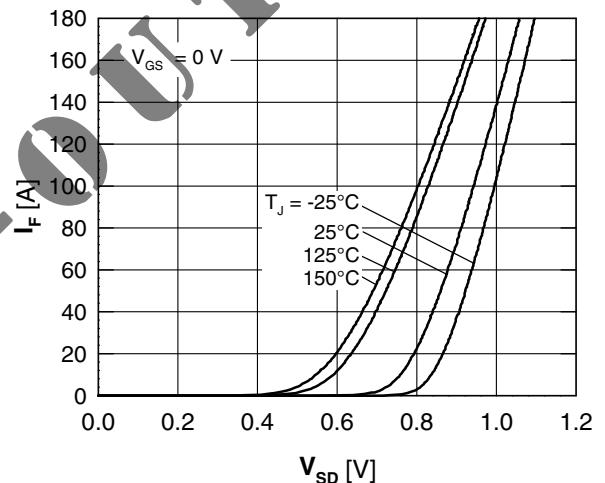


Fig. 16 Source drain diode current I_F vs. source drain voltage V_{SD} (body diode)

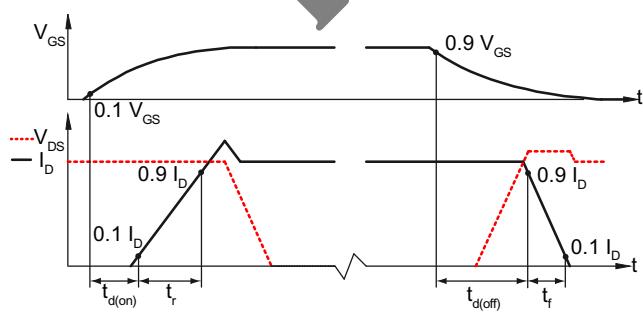


Fig. 17 Definition of switching times

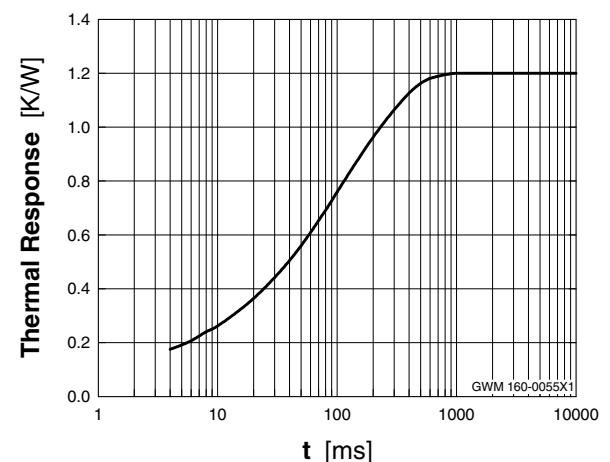


Fig. 18 Typ. thermal impedance junction to heatsink Z_{thJH} with heat transfer paste