

PerFE^T[™] Power Transistor

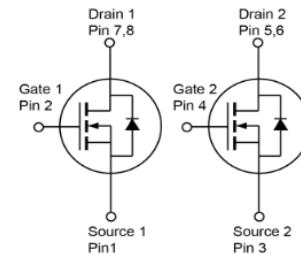
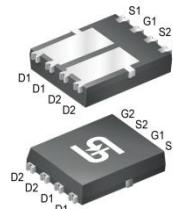
FEATURES

- Excellent FOM
- AEC-Q101 Qualified
- Wettable Flank leads for Enhanced AOI
- 100% UIS and Rg tested
- 175°C Operating Junction Temperature
- RoHS Compliant
- Halogen-Free

APPLICATIONS

- Automotive Applications
- Solenoid and Motor Drivers
- DC-DC Converters

| PRODUCT SUMMARY | | |
|---------------------------|------------------------|------|
| PARAMETER | VALUE | UNIT |
| V _{DS} | 40 | V |
| R _{DS(on)} (max) | V _{GS} = 10V | 7.6 |
| | V _{GS} = 4.5V | 10.6 |
| Q _g | V _{GS} = 4.5V | 10.7 |
| | | nC |


PDFN56U Dual


Note: MSL 1 (Moisture Sensitivity Level) per J-STD-020

| ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted) | | | |
|---|-----------------------------------|----------------|------|
| PARAMETER | SYMBOL | LIMIT | UNIT |
| Drain-Source Voltage | V _{DS} | 40 | V |
| Gate-Source Voltage | V _{GS} | ±16 | V |
| Continuous Drain Current, Silicon limited | I _D | 64 | A |
| Continuous Drain Current (Note 1) | T _C = 25°C | I _D | A |
| | T _C = 100°C | | |
| | T _A = 25°C | | |
| Pulsed Drain Current | I _{DM} | 160 | A |
| Single Pulse Avalanche Current (Note 2) | I _{AS} | 17.3 | A |
| Single Pulse Avalanche Energy (Note 2) | E _{AS} | 45 | mJ |
| Total Power Dissipation | T _C = 25°C | P _D | W |
| | T _C = 125°C | | |
| Operating Junction and Storage Temperature Range | T _J , T _{STG} | -55 to +175 | °C |

| THERMAL RESISTANCE | | | |
|--|------------------|---------|------|
| PARAMETER | SYMBOL | MAXIMUM | UNIT |
| Thermal Resistance – Junction to Case | R _{θJC} | 2.7 | °C/W |
| Thermal Resistance – Junction to Ambient | R _{θJA} | 50 | °C/W |

Thermal Performance Note: R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistances. The case-thermal reference is defined at the solder mounting surface of the drain pins. R_{θJA} is guaranteed by design while R_{θCA} is determined by the user's board design.

| ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted) | | | | | | |
|--|---|---------------------|------------|------------|------------|------------------|
| PARAMETER | CONDITIONS | SYMBOL | MIN | TYP | MAX | UNIT |
| Static | | | | | | |
| Drain-Source Breakdown Voltage | $V_{GS} = 0\text{V}, I_D = 1\text{mA}$ | BV_{DSS} | 40 | -- | -- | V |
| Gate Threshold Voltage | $V_{GS} = V_{DS}, I_D = 250\mu\text{A}$ | $V_{GS(\text{TH})}$ | 1.4 | 1.8 | 2.2 | V |
| Gate-Source Leakage Current | $V_{GS} = \pm 16\text{V}, V_{DS} = 0\text{V}$ | I_{GSS} | -- | -- | ± 100 | nA |
| Drain-Source Leakage Current | $V_{GS} = 0\text{V}, V_{DS} = 40\text{V}$ | I_{DSS} | -- | -- | 1 | μA |
| | $V_{GS} = 0\text{V}, V_{DS} = 40\text{V}$ $T_J = 125^\circ\text{C}$ | | -- | -- | 100 | |
| Drain-Source On-State Resistance (Note 3) | $V_{GS} = 10\text{V}, I_D = 20\text{A}$ | $R_{DS(\text{on})}$ | -- | 6.1 | 7.6 | $\text{m}\Omega$ |
| | $V_{GS} = 4.5\text{V}, I_D = 20\text{A}$ | | -- | 7.7 | 10.6 | |
| Forward Transconductance (Note 3) | $V_{DS} = 10\text{V}, I_D = 5\text{A}$ | g_{fs} | -- | 47 | -- | S |
| Dynamic | | | | | | |
| Total Gate Charge | $V_{GS} = 4.5\text{V}, V_{DS} = 20\text{V}, I_D = 15\text{A}$ | Q_g | -- | 10.7 | 16 | nC |
| Total Gate Charge | $V_{GS} = 10\text{V}, V_{DS} = 20\text{V}, I_D = 15\text{A}$ | Q_g | -- | 22.4 | 34 | |
| Gate-Source Charge | | Q_{gs} | -- | 4.3 | 8.6 | |
| Gate-Drain Charge | | Q_{gd} | -- | 3.3 | 6.6 | |
| Input Capacitance | $V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1.0\text{MHz}$ | C_{iss} | -- | 1337 | 2006 | pF |
| Output Capacitance | | C_{oss} | -- | 242 | 484 | |
| Reverse Transfer Capacitance | | C_{rss} | -- | 40 | 80 | |
| Gate Resistance | $f = 1.0\text{MHz}$ | R_g | -- | 1.7 | -- | Ω |
| Switching (Note 4) | | | | | | |
| Turn-On Delay Time | $V_{GS} = 10\text{V}, V_{DS} = 20\text{V}, I_D = 15\text{A}, R_G = 3.3\Omega$ | $t_{d(on)}$ | -- | 7.3 | -- | ns |
| Rise Time | | t_r | -- | 50.1 | -- | |
| Turn-Off Delay Time | | $t_{d(off)}$ | -- | 23.6 | -- | |
| Fall Time | | t_f | -- | 5.6 | -- | |
| Source-Drain Diode | | | | | | |
| Diode Forward Voltage (Note 3) | $V_{GS} = 0\text{V}, I_S = 20\text{A}$ | V_{SD} | -- | -- | 1.1 | V |
| Reverse Recovery Time | $I_S = 15\text{A}, dI/dt = 100\text{A}/\mu\text{s}$ | t_{rr} | -- | 29.4 | -- | ns |
| Reverse Recovery Charge | | Q_{rr} | -- | 20 | -- | nC |

Notes:

1. Package current limit.
2. $L = 0.3\text{mH}, V_{GS} = 10\text{V}, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$.
3. Pulse test: Pulse Width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
4. Switching time is essentially independent of operating temperature.

ORDERING INFORMATION

| ORDERING CODE | PACKAGE | PACKING |
|----------------------|----------------|---------------------|
| TQM076NH04LDCR RLG | PDFN56U Dual | 2,500pcs / 13" Reel |
| TQM076NH04LDCR-V RLG | PDFN56U Dual | 2,500pcs / 13" Reel |

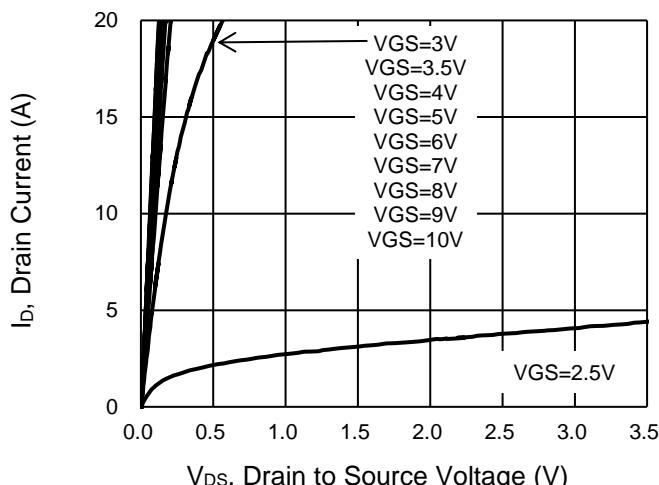
Notes:

V : HOT test

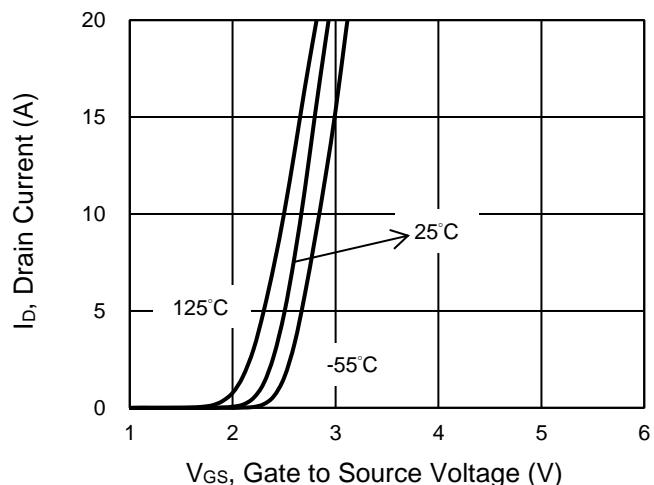
CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)

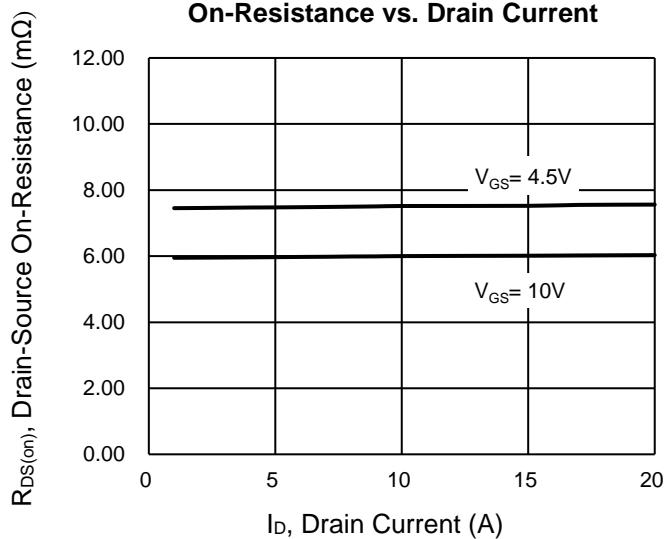
Output Characteristics



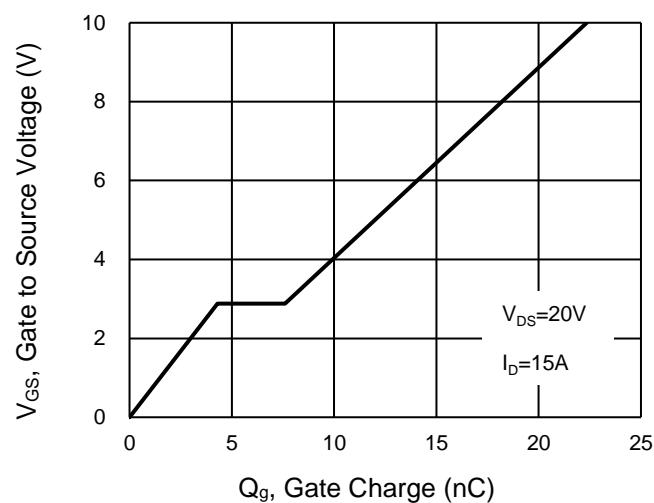
Transfer Characteristics



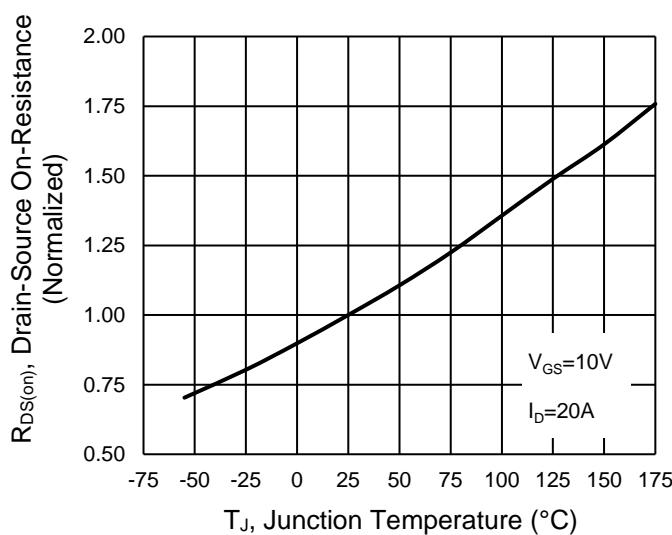
On-Resistance vs. Drain Current



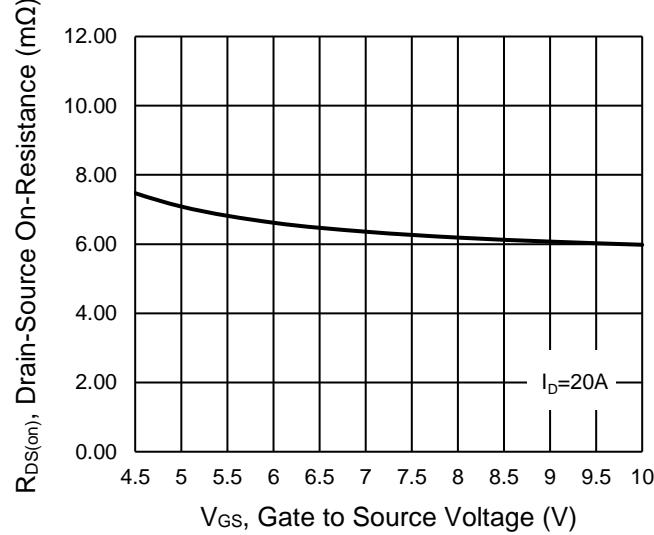
Gate-Source Voltage vs. Gate Charge



On-Resistance vs. Junction Temperature

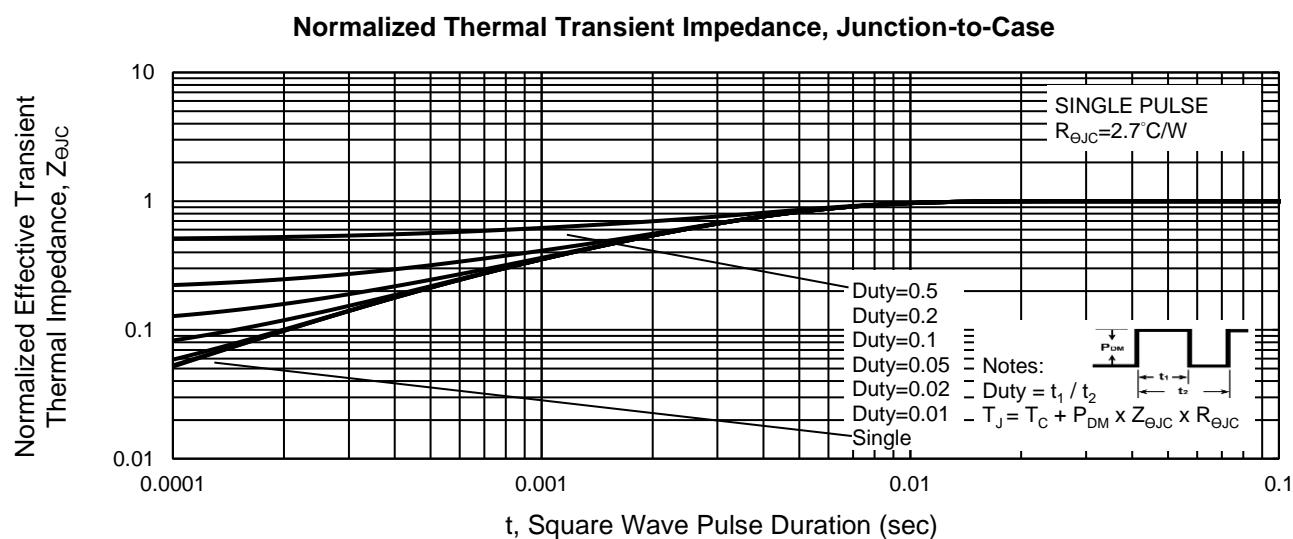
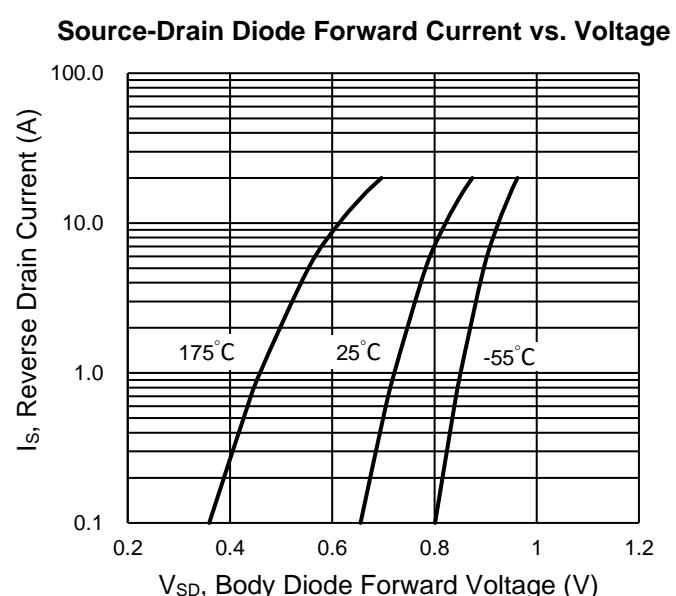
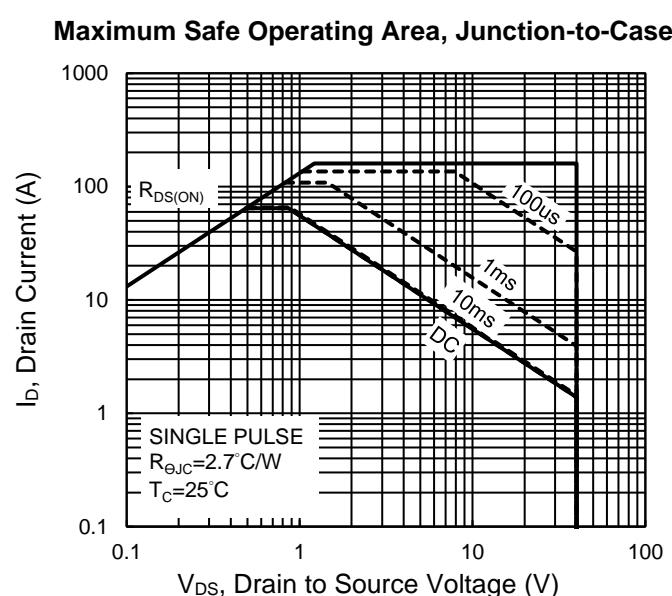
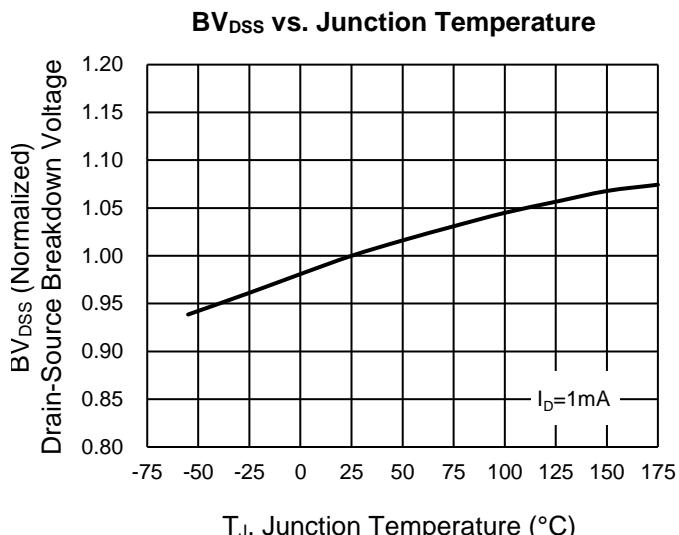
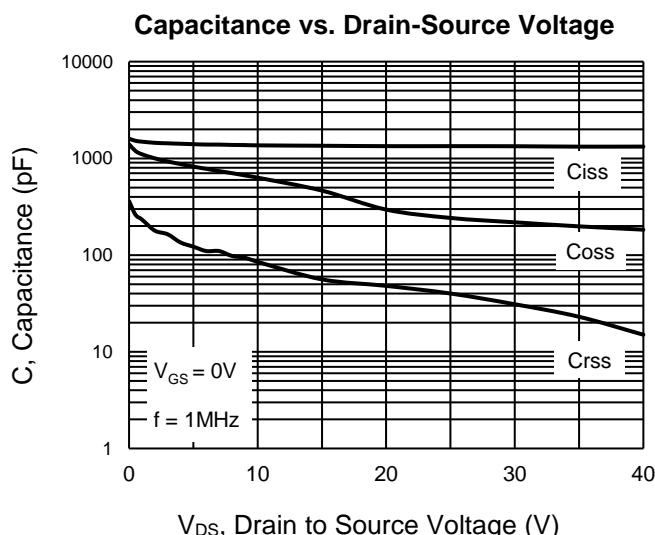


On-Resistance vs. Gate-Source Voltage



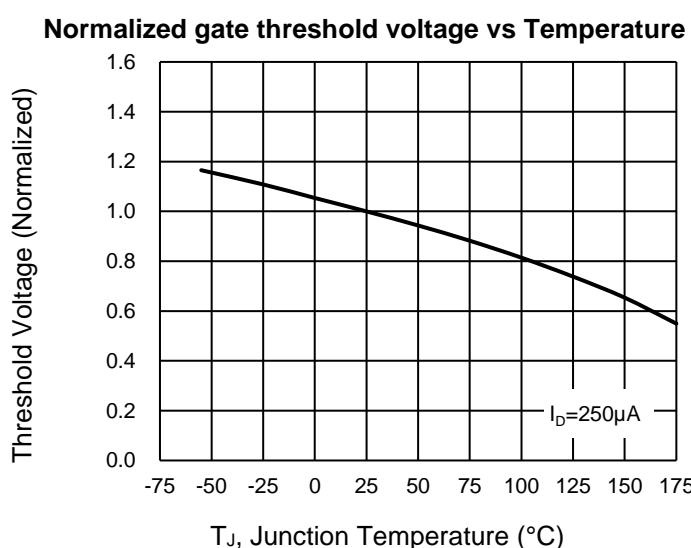
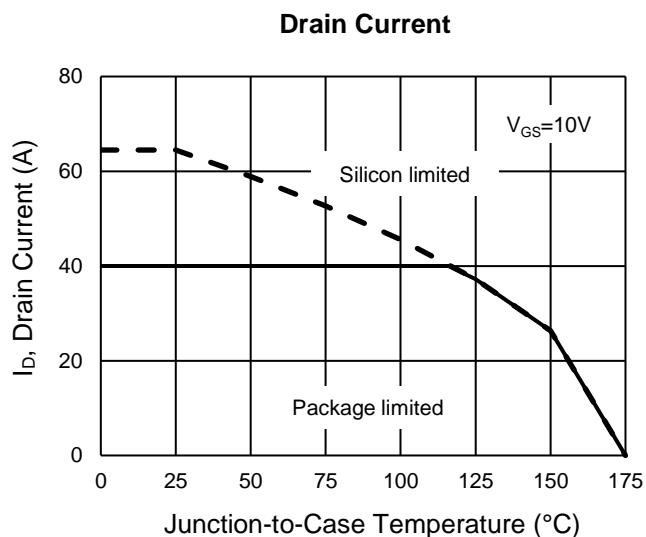
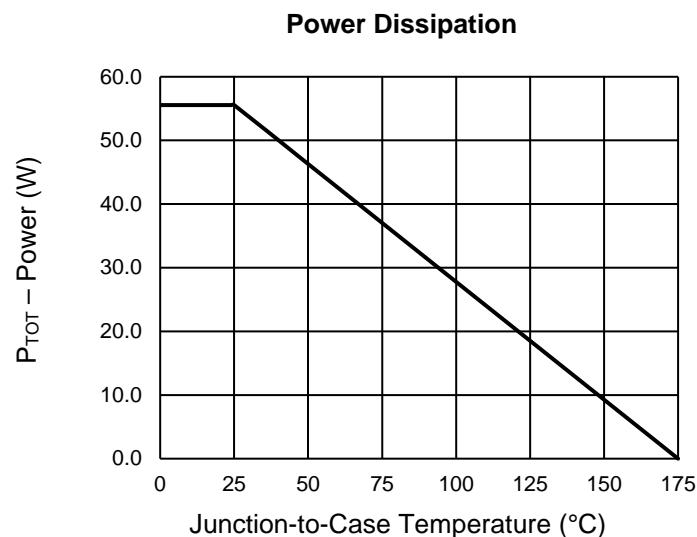
CHARACTERISTICS CURVES

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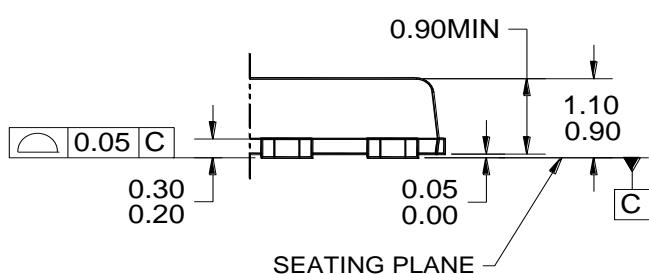
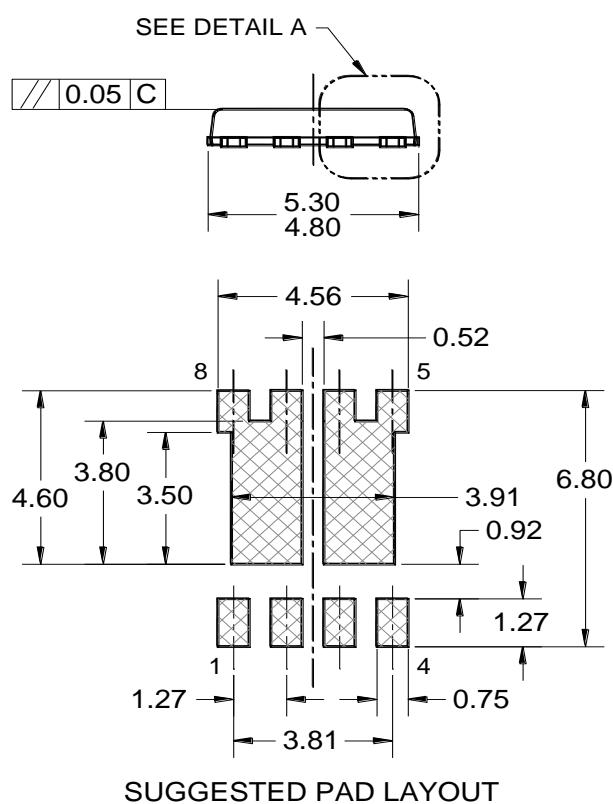
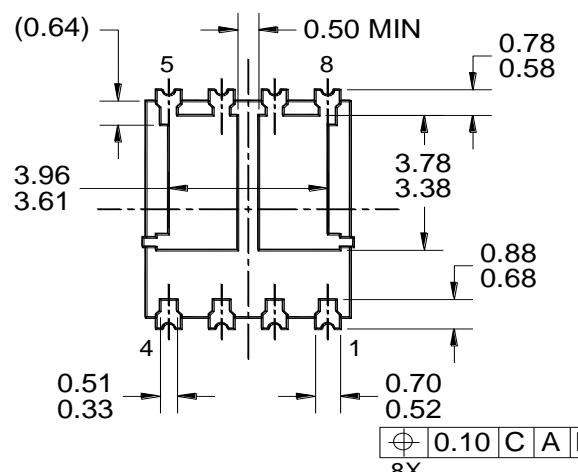
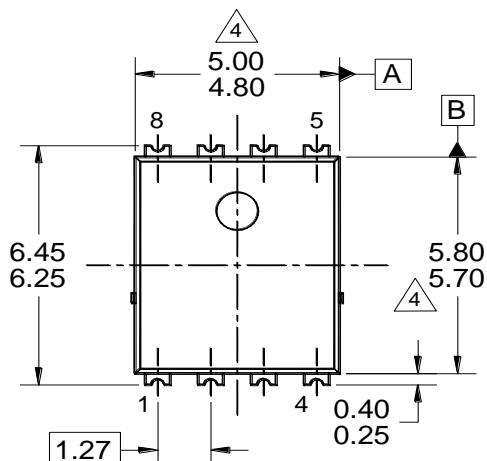
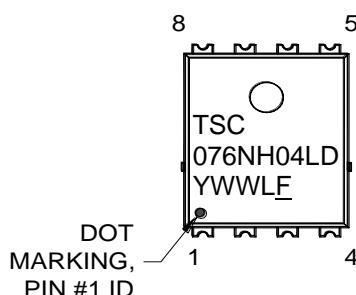


CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)



PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

PDFN56U Dual

**DETAIL A
(SCALE 2:1)**

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. PACKAGE OUTLINE REFERENCE:
JEITA ED-7500B, EIAJ SC-111CB.
-  MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
5. DWG NO. REF: HQ2SD07-PDFN56UD-032 REV A.

| | |
|-----|-----------------------|
| P/N | = MARKING CODE |
| Y | = YEAR CODE |
| WW | = WEEK CODE (01~52) |
| L | = LOT CODE (1~9, A-Z) |
| F | = FACTORY CODE |
| _ | = AEC-Q101 QUALIFIED |

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