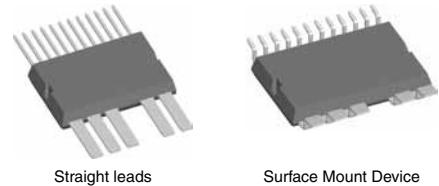
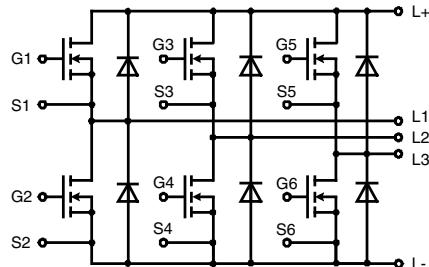


## Three phase full Bridge

with Trench MOSFETs  
in DCB isolated high current package

**V<sub>DSS</sub>** = 40 V  
**I<sub>D25</sub>** = 180 A  
**R<sub>DSon typ.</sub>** = 1.9 mΩ

### Preliminary data



### Applications

- AC drives
  - in automobiles
  - electric power steering
  - starter generator
- in industrial vehicles
  - propulsion drives
  - fork lift drives
- in battery supplied equipment

### Features

- MOSFETs in trench technology:
  - low R<sub>DSon</sub>
  - optimized intrinsic reverse diode
- package:
  - high level of integration
  - high current capability 300 A max.
  - aux. terminals for MOSFET control
  - terminals for soldering or welding connections
  - isolated DCB ceramic base plate with optimized heat transfer
- Space and weight savings

### Package options

- 2 lead forms available
  - straight leads (SL)
  - SMD lead version (SMD)

Symbol	Conditions	Maximum Ratings				
		min.	typ.	max.		
V <sub>DSS</sub>	T <sub>J</sub> = 25°C to 150°C		40		V	
V <sub>GS</sub>			± 20		V	
I <sub>D25</sub>	T <sub>C</sub> = 25°C		180		A	
I <sub>D90</sub>	T <sub>C</sub> = 90°C		136		A	
I <sub>D110</sub>	T <sub>C</sub> = 110°C		120		A	
I <sub>F25</sub>	T <sub>C</sub> = 25°C (diode)		182		A	
I <sub>F90</sub>	T <sub>C</sub> = 90°C (diode)		112		A	
I <sub>F110</sub>	T <sub>C</sub> = 110°C (diode)		88		A	
Symbol	Conditions	Characteristic Values				
		(T <sub>J</sub> = 25°C, unless otherwise specified)	min.	typ.	max.	
R <sub>DSon</sub> <sup>1)</sup>	on chip level at V <sub>GS</sub> = 10 V; I <sub>D</sub> = 100 A	T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C		1.9 2.8	2.5	mΩ mΩ
V <sub>GS(th)</sub>	V <sub>DS</sub> = 20 V; I <sub>D</sub> = 1 mA		2.5		4.5	V
I <sub>DSS</sub>	V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0 V	T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C		50	5	μA μA
I <sub>GSS</sub>	V <sub>GS</sub> = ± 20 V; V <sub>DS</sub> = 0 V				0.2	μA
Q <sub>g</sub> Q <sub>gs</sub> Q <sub>gd</sub>	V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 20 V; I <sub>D</sub> = 100 A			110 33 30		nC nC nC
t <sub>d(on)</sub> t <sub>r</sub> t <sub>d(off)</sub> t <sub>f</sub>	inductive load V <sub>GS</sub> = +10/0 V; V <sub>DS</sub> = 24 V I <sub>D</sub> = 135 A; R <sub>G</sub> = 39 Ω; T <sub>J</sub> = 125°C			150 240 350 170		ns ns ns ns
E <sub>on</sub> E <sub>off</sub> E <sub>recoff</sub>				0.12 0.51 0.003		mJ mJ mJ
R <sub>thJC</sub> R <sub>thJH</sub>	with heat transfer paste (IXYS test setup)			1.3	1.0 1.6	K/W K/W

<sup>1)</sup> V<sub>DS</sub> = I<sub>D</sub> · (R<sub>DS(on)</sub> + R<sub>Pin to Chip</sub>)

Recommended replacement for the SMD type: MTI150W40GC

**Source-Drain Diode**

Symbol	Conditions	Characteristic Values		
		(T <sub>J</sub> = 25°C, unless otherwise specified)		
		min.	typ.	max.
V <sub>SD</sub>	(diode) I <sub>F</sub> = 100 A; V <sub>GS</sub> = 0 V	0.9	1.2	V
t <sub>rr</sub> Q <sub>RM</sub> I <sub>RM</sub>	I <sub>F</sub> = 100 A; -di <sub>F</sub> /dt = 600 A/μs V <sub>R</sub> = 15 V; T <sub>VJ</sub> = 125°C	38 0.31 14		ns μC A

**Component**

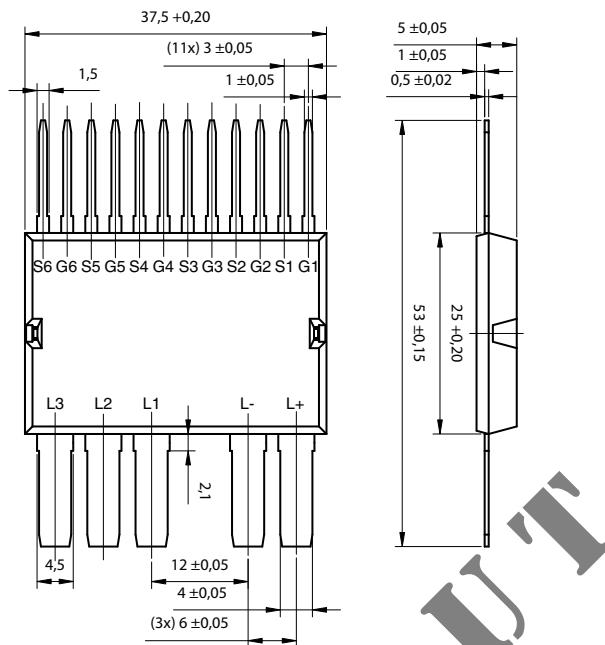
Symbol	Conditions	Maximum Ratings	
I <sub>RMS</sub>	per pin in main current paths (P+, N-, L1, L2, L3) may be additionally limited by external connections	300	A
T <sub>J</sub>		-55...+175	°C
T <sub>stg</sub>		-55...+125	°C
V <sub>ISOL</sub>	I <sub>ISOL</sub> ≤ 1 mA, 50/60 Hz, f = 1 minute	1000	V~
F <sub>c</sub>	mounting force with clip	50 - 250	N

Symbol	Conditions	Characteristic Values		
		min.	typ.	max.
R <sub>pin to chip</sub> <sup>1)</sup>	L+ to L1/L2/L3 or L- to L1/L2/L3		1.0	mΩ
C <sub>P</sub>	coupling capacity between shorted pins and mounting tab in the case		160	pF
Weight			25	g

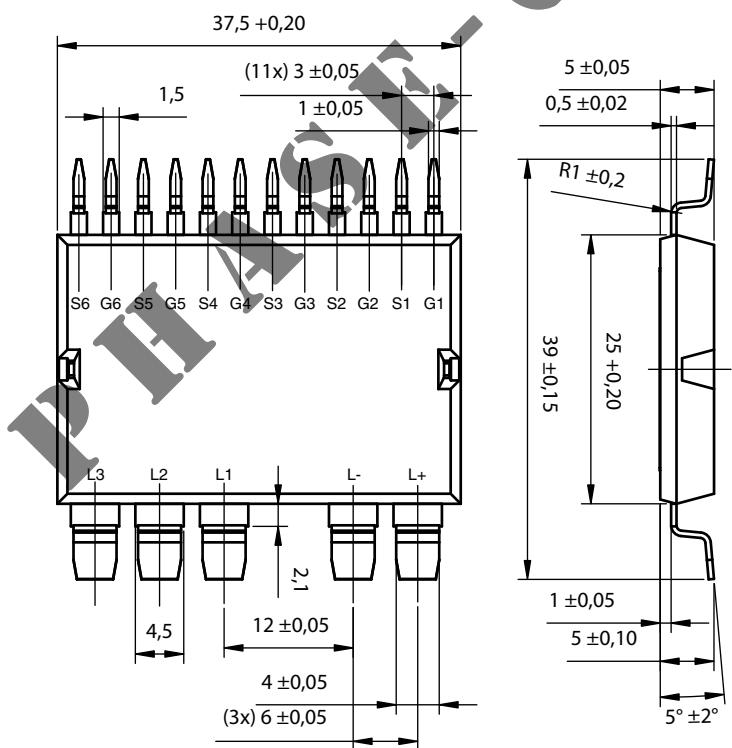
<sup>1)</sup> V<sub>DS</sub> = I<sub>D</sub>·(R<sub>DS(on)</sub> + R<sub>Pin to Chip</sub>)

**Straight Leads**

GWM 180-004X2-SL

**Surface Mount Device**

GWM 180-004X2-SMD



Leads	Ordering	Part Name & Packing Unit Marking	Part Marking	Delivering Mode	Base Qty.	Ordering Code
Straight	Standard	GWM 180-004X2 - SL	GWM 180-004X2	Blister	28	508 772
SMD	Standard	GWM 180-004X2 - SMD	GWM 180-004X2	Blister	28	508 786

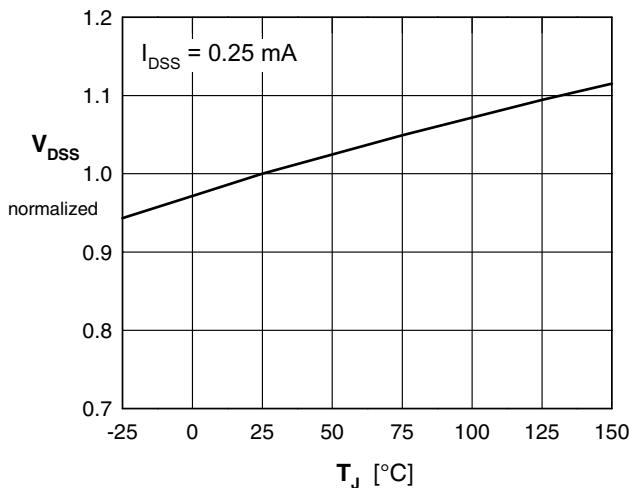


Fig. 1 Drain source breakdown voltage  $V_{DSS}$  versus junction temperature  $T_{VJ}$

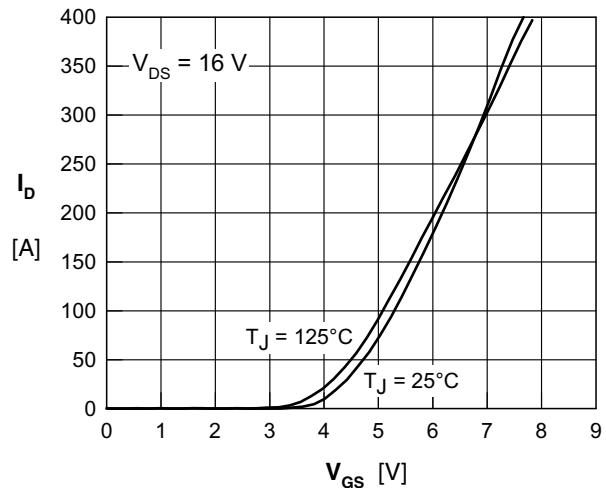


Fig. 2 Typical transfer characteristic

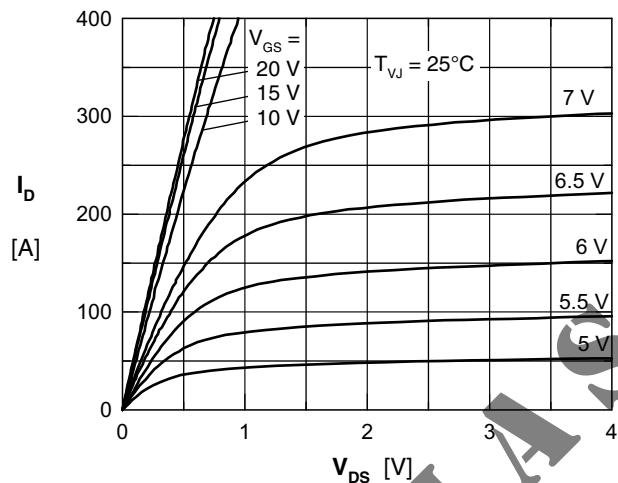


Fig. 3 Typical output characteristic

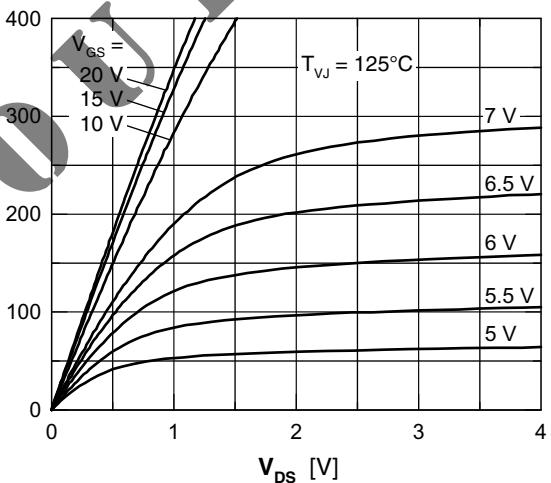


Fig. 4 Typical output characteristic

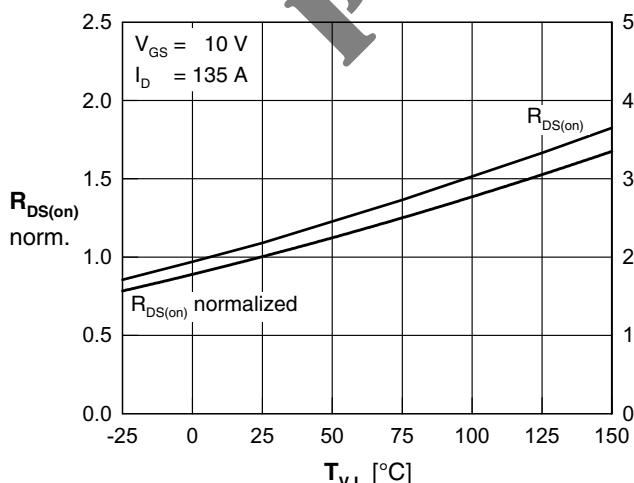


Fig. 5 Typ. drain source on-state resistance  $R_{DS(on)}$  versus junction temperature  $T_J$

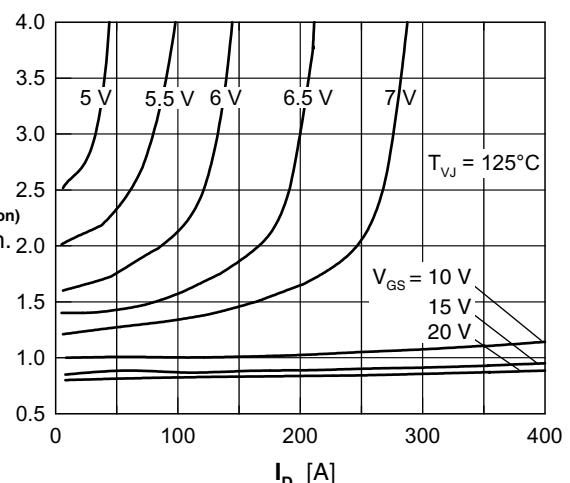


Fig. 6 Typ. drain source on-state resistance  $R_{DS(on)}$  versus  $I_D$

IXYS reserves the right to change limits, test conditions and dimensions.

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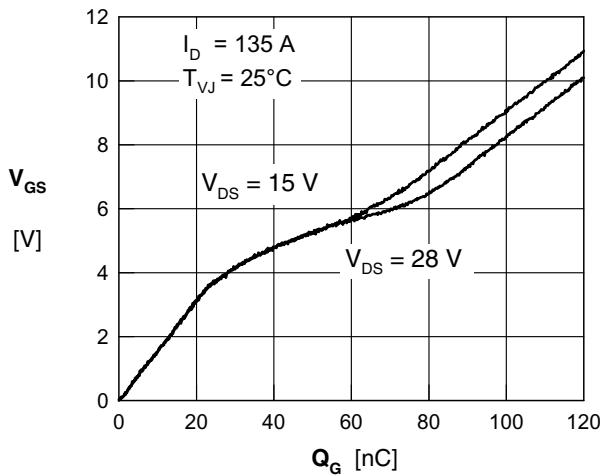


Fig. 7 Gate charge characteristics

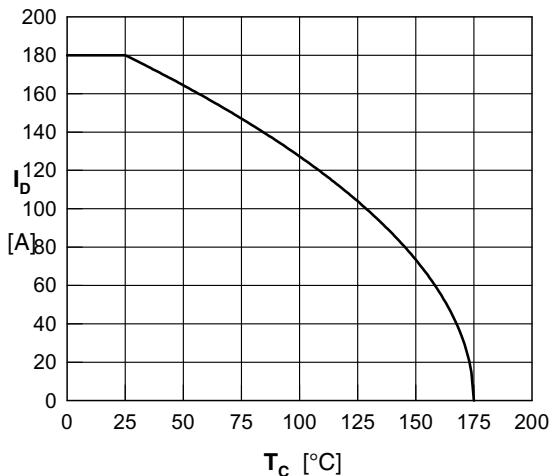


Fig. 8 Drain current  $I_D$  vs. temperature  $T_c$

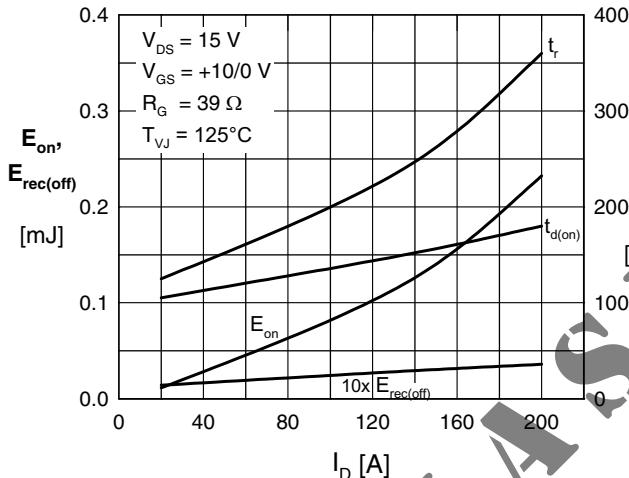


Fig. 9 Typ. turn-on energy & switching times vs. collector current, inductive switching

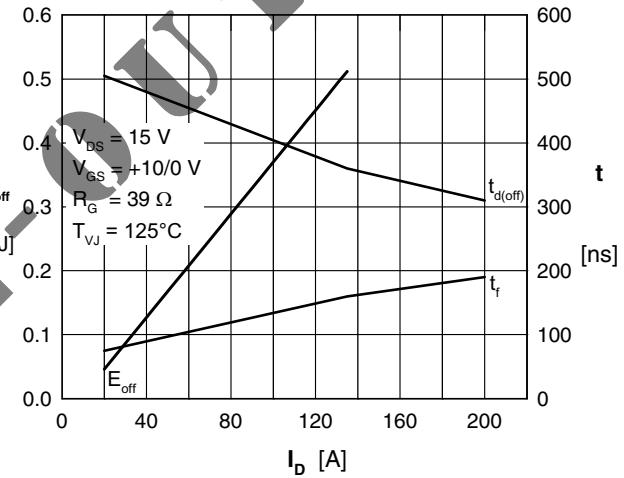


Fig. 10 Typ. turn-off energy & switching times vs. collector current, inductive switching

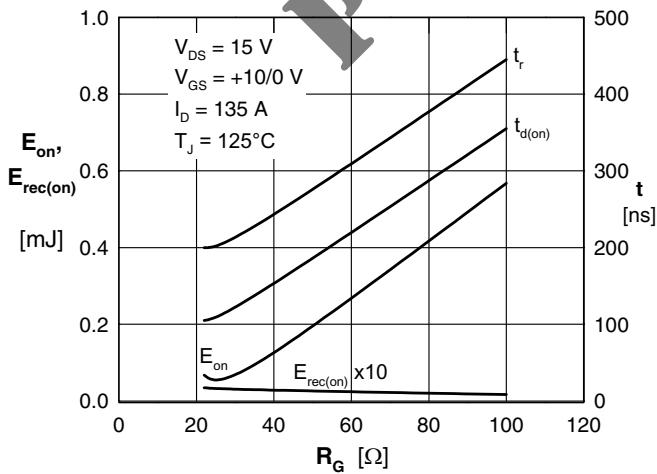


Fig. 11 Typ. turn-on energy & switching times vs. gate resistor, inductive switching

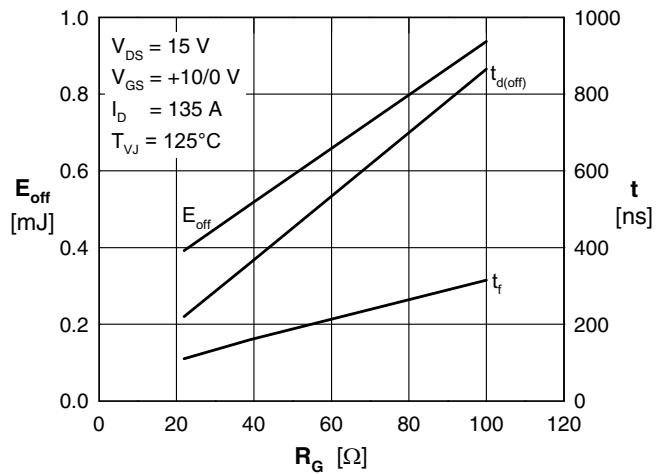


Fig. 12 Typ. turn-off energy & switching times vs. gate resistor, inductive switching

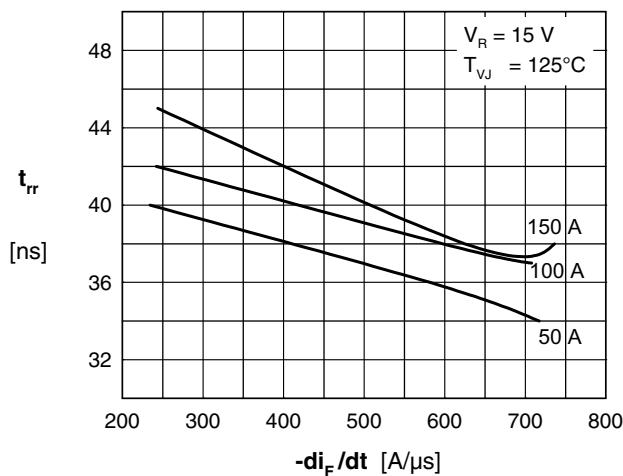


Fig. 13 Reverse recovery time  $t_{rr}$  of the body diodes vs.  $di/dt$

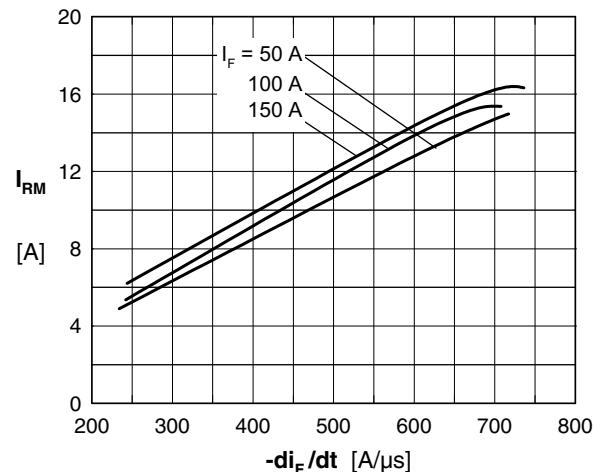


Fig. 14 Reverse recovery current  $I_{RM}$  of the body diodes versus  $di/dt$

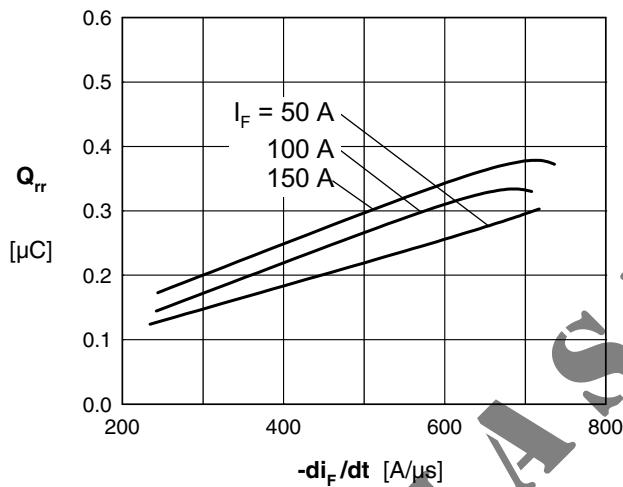


Fig. 15 Reverse recovery charge  $Q_{rr}$  of the body diodes versus  $di/dt$

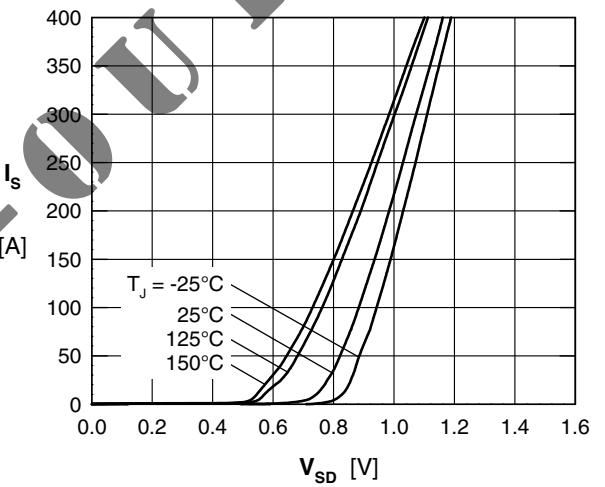


Fig. 16 Source current  $I_s$  versus source drain voltage  $V_{SD}$  (body diode)

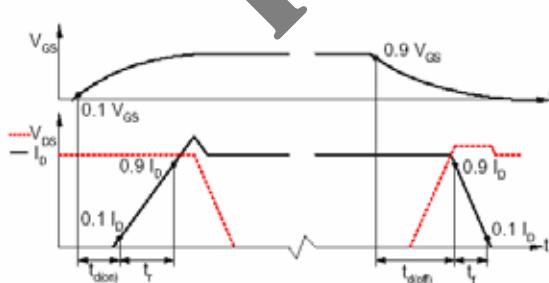


Fig. 17 Definition of switching times