

# **Advance Product Change Notification**

Issue Date: 03-Oct-2015

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# 201509016A



### **Management Summary**

Microcontroller products LPC18(S)x0 and LPC43(S)x0 will be sourced from the new product version Rev "D".

Currently released version is Rev. "C".

Change Category			
[] Wafer Fab process	[] Assembly Process	[] Product Marking	[X] Design
[] Wafer Fab	[] Assembly	[] Electrical spec./Test	[] Mechanical Specification
materials	Materials	coverage	
[] Wafer Fab location	[] Assembly Location	[] Test Location	[] Packing/Shipping/Labeling

LPC18(S)x0 and LPC43(S)x0 change from Rev "C" to Rev "D"

# **Details of this Planned Change**

Microcontroller products LPC18(S)x0 and LPC43(S)x0 will be sourced from the new product version Rev

Currently released version is Rev "C".

- No wafer fab location or process change.
- These changes fix the following known functional deviations as documented in the device errata:

OTP.1 - OTP ROM driver may not program boot source

OTP.2 - Repeated power cycling of the device may cause erroneous programing of the OTP banks

USBROM.2 - Nested NAK handling of EP0 OUT endpoint

USBROM.3 - Isochronous transfers

ISP.1 - Part ID format incorrect

ISP.2 - "J" command in ISP mode swaps last two items

- device functionality has only changed to fix the errata items identified above.

# Why do we Plan this Change

To fix known functional deviations as documented in the errata sheets.

### **Identification of Affected Products**

Top side marking

### Sample Information

Sample planning follows with the final PCN

#### **Impact**

Although the new product revision is expected to be electrically identical it is recommended that customers fully characterize the change in their applications.

# **Data Sheet Revision**

No impact to existing datasheet

### **Disposition of Old Products**

Existing inventory will be shipped until depleted

# **Timing and Logistics**

The Self Qualification Report will be ready on 23-Nov-2015.

The Final PCN is planned to be issued on: 23-Nov-2015.

Your acknowledgement of this change, conform JEDEC JESD46 D, is expected till 02-Nov-2015.

# **Contact and Support**

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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- LPC43S50FET180E
- LPC1850FET256,551
- LPC18S10FET100E
- LPC18S30FET256E
- LPC43S20FBD144E
- LPC18S10FBD144E
- LPC43S30FET256E
- LPC4370FET100E
- LPC4370FET256E
- LPC4320FBD144,551
- LI C+3201 DD1++,331
- LPC4330FBD144,551
- LPC4330FET180,551 LPC4330FET100,551
- LPC43S50FET256,551
- LPC4350FET256,551
- LF C43301 L1230,331
- LPC4330FET256,551
- LPC4320FET100,551
- LPC4310FET100,551
- LPC1850FET256,551
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- LPC43S50FET180E
- LPC18S50FET180E
- LPC18S10FET100E
- LPC18S30FET256E
- LPC43S20FBD144E
- LPC18S10FBD144E
- LPC43S30FET256E
- LPC4370FET100E
- LPC4370FET256E
- LPC4320FBD144,551
- LPC4330FBD144,551
- LPC4330FET180,551
- LPC4330FET100,551
- LPC43S50FET256,551
- LPC4350FET256,551
- LPC18S50FET180E