

DATA SHEET

SKY65371-11: 880 to 915 MHz High Linearity, Active Bias Low-Noise Variable Gain Amplifier

Applications

- LTE, WCDMA, GSM wireless infrastructure
- Low noise, high linearity systems
- Macro base stations
- Small cells

Features

- Fully integrated low-noise front end
- High gain: 39 dB
- Excellent return loss: >20 dB
- High linearity gain control > 35 dB
- Low NF: 0.82 dB
- Switchable high/low gain state modes
- Temperature and process stable active bias
- Small MCM (16-pin, 8 x 8 mm) package (MSL3 @ 260 °C per JEDEC J-STD-020)



Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.

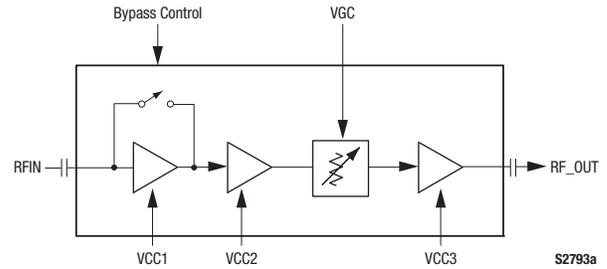


Figure 1. SKY65371-11 Block Diagram

Description

The SKY65371-11 is variable gain Low-Noise Amplifier (LNA) with an active bias and high linearity performance. The first stage of the device is comprised of an LNA with a bypass switch that is followed by a high linearity driver amplifier, a variable voltage attenuator, and a high linearity power amplifier. This module architecture provides excellent return loss, low noise, and high linearity performance.

The internal active bias circuitry ensures repeatable performance over temperature. The device is fully integrated and requires minimal external components.

The SKY65371-11 is manufactured in a compact, 8 x 8 mm, 16-pin Multi-Chip Module (MCM) package. A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

The SKY65371-11 is part of a family of LNAs that cover the frequency range of 814 MHz to 2570 MHz:

- **SKY65369-11**, 832 to 862 MHz (Data Sheet # 201767)
- **SKY65370-11**, 814 to 849 MHz (Data Sheet # 201965)
- **SKY65371-11**, 880 to 915 MHz (this Data Sheet)
- **SKY65373-11**, 1710 to 1785 MHz (Data Sheet # 201764)
- **SKY65374-11**, 1850 to 1915 MHz (Data Sheet #201966)
- **SKY65375-11**, 1920 to 1980 MHz (Data Sheet # 201755)
- **SKY65376-11**, 2500 to 2570 MHz (Data Sheet # 202458)

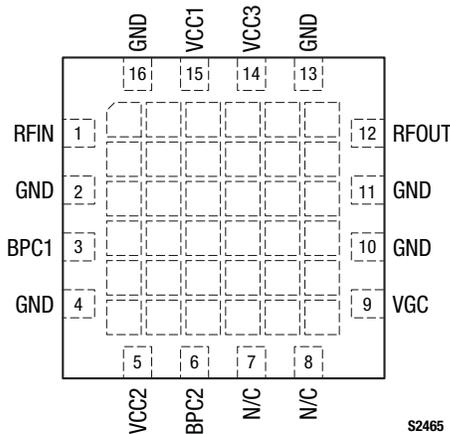


Figure 2. SKY65371-11 Pinout – 16-Pin MCM (Top View)

Table 1. SKY65371-11 Signal Descriptions

Pin	Name	Description	Pin	Name	Description
1	RFIN	RF input	9	VGC	Gain control, 0 V (maximum gain) to +3.3 V (minimum gain)
2	GND	Ground	10	GND	Ground
3	BPC1	Bypass switch for high/low gain state, terminal 1. See Table 8.	11	GND	Ground
4	GND	Ground	12	RFOUT	RF output
5	VCC2	Second state amplifier bias. Connect to +5 V, 120 mA minimum DC supply.	13	GND	Ground
6	BPC2	Bypass switch for high/low gain state, terminal 2. See Table 8.	14	VCC3	Third stage amplifier bias. Connect to +5 V, 120 mA minimum DC supply.
7	N/C	No connection. Can be left open or grounded.	15	VCC1	First stage amplifier bias. Connect to +5 V, 120 mA minimum DC supply.
8	N/C	No connection. Can be left open or grounded.	16	GND	Ground

Table 2. SKY65371-11 Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Minimum	Maximum	Units
Supply voltage	V _{CC}		5.5	V
RF input power	P _{IN}		+5	dBm
Storage temperature	T _{STG}	-55	+150	°C
Operating temperature	T _C	-40	+100	°C
Junction temperature	T _J		+150	°C
Thermal resistance	R _{TH}		21	C/W

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY65371-11 are provided in Table 2. The recommended operating conditions are specified

in Table 3. Electrical specifications are provided in Tables 4 through 7. The gain control logic is shown in Table 8.

Table 3. SKY65371-11 Recommended Operating Conditions

(@ Maximum Gain [V_{Gc} = V_{Bpc1} = 0 V, V_{Bpc2} = 3.3 V], P_{IN} = -25 dBm, Characteristic Impedance [Z₀] = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Minimum	Typical	Maximum	Units
RF input power	P _{IN}		-25	-12	dBm
Frequency range	f	880		915	MHz
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Gain control voltage	V _{Gc}	0		+3.3	V
Bypass control voltage: Logic high	V _{Bpc1} , V _{Bpc2}	2.7	3.0	3.3	V
Logic low		0		0.6	V
Operating case temperature	T _C	-40		+85	°C

Table 4. SKY65371-11 Electrical Specifications (1 of 2) (Note 1)

(V_{DD} = 5.0 V @ Maximum Gain [V_{Gc} = V_{Bpc1} = 0 V, V_{Bpc2} = 3.3 V], T_C = +25 °C, P_{IN} = -25 dBm, f = 900 MHz, Characteristic Impedance [Z₀] = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
RF Specifications						
Frequency range	f		880		915	MHz
Quiescent current	I _{CQ}			380	440	mA
Noise Figure (Note 2)	NF	Gain = +35 dB		0.82	1.00	dB
Gain variation over frequency		P _{IN} = -25 dBm: Gain = 3 and 17 dB (low gain) Gain = 18 and 35 dB (high gain)		0.60	0.80	dB
Part-to-part gain variation		Gain = 32, 29, 26, 22, 17, 13, 10, and 6 dB	-1		+1	
Absolute gain, high gain mode	G _{MAX_HIGHGAIN}	P _{IN} = -25 dBm, BPC1 = 0, BPC2 = 1, V _{Gc} = 0 V	37.5	39.0		dB
Absolute gain, high gain mode	G _{MIN_HIGHGAIN}	P _{IN} = -25 dBm, BPC1 = 0, BPC2 = 1, V _{Gc} = 3.3 V		16	17	dB
Absolute gain, low gain mode	G _{MAX_LOWGAIN}	P _{IN} = -25 dBm, BPC1 = 1, BPC2 = 0, V _{Gc} = 0 V	18.5	21.5		dB
Absolute gain, low gain mode	G _{MIN_LOWGAIN}	P _{IN} = -25 dBm, BPC1 = 1, BPC2 = 0, V _{Gc} = 3.3 V		-6	+1	dB
Input return loss	S ₁₁	P _{IN} = -25 dBm: Gain = 3 dB (low gain) Gain = 17 dB (low gain) Gain = 18 dB (high gain) Gain = 35 dB (high gain)	18.5 18.5 18.5 20.5	24.0 25.0 22.0 23.0		dB dB dB dB
Output return loss	S ₂₂	P _{IN} = -25 dBm: Gain = 3 dB (low gain) Gain = 17 dB (low gain) Gain = 18 dB (high gain) Gain = 35 dB (high gain)	16 16 16 16	21 22 21 22		dB dB dB dB

Table 4. SKY65371-11 Electrical Specifications (2 of 2) (Note 1)
(VDD = 5.0 V @ Maximum Gain [VGC = VBPC1 = 0 V, VBPC2 = 3.3 V], Tc = +25 °C, PIN = -25 dBm, f = 900 MHz, Characteristic Impedance [Zo] = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
RF Specifications (continued)						
Reverse isolation	S12	PIN = -25 dBm, gain = 35 dB (high gain)	60.5	70		dB
3 rd Order Input Intercept Point (Note 3)	IIP3	Δf = 1 MHz, PIN = -25 dBm/tone:				
		Gain = 11 dB	+18.5	+21.0		dBm
		Gain = 17 dB	+15.0	+18.5		dBm
		Gain = 29 dB	+5.5	+8.0		dBm
		Gain = 35 dB	+2.5	+5.0		dBm
1 dB Input Compression Point (Note 4)	IP1dB	Gain = 11 dB	+9.0	+16.0		dBm
		Gain = 17 dB	+5.0	+12.0		dBm
		Gain = 29 dB	-8.0	-4.5		dBm
		Gain = 35 dB	-12.0	-7.5		dBm

Note 1: Performance is guaranteed only under the conditions listed in this Table.

Note 2: Loss from the input SMA connector and Evaluation Board up to pin 1 has been de-embedded from the NF measurement. See Table 5 for full specification. The Evaluation Board input and output trace loss (typical) is 0.07 dB.

Note 3: See Table 6 for full specification.

Note 4: See Table 7 for full specification.

Table 5. SKY65371-11 Electrical Specifications: Noise Figure (Note 1)
(VDD = 4.75 to 5.25 V, Tc = -40 °C to +85 °C, PIN = -25 dBm, f = 880 to 915 MHz, Characteristic Impedance [Zo] = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Noise Figure	NF	Gain = 3 dB			23.70	dB
		Gain = 4 to 6 dB			22.70	dB
		Gain = 7 to 8 dB			20.70	dB
		Gain = 9 to 10 dB			18.70	dB
		Gain = 11 to 12 dB			16.70	dB
		Gain = 13 to 14 dB			14.70	dB
		Gain = 15 to 16 dB			12.80	dB
		Gain = 17 to 18 dB			10.80	dB
		Gain = 19 to 20 dB			8.80	dB
		Gain = 21 to 22 dB			6.80	dB
		Gain = 23 to 24 dB			5.30	dB
		Gain = 25 to 26 dB			3.50	dB
		Gain = 27 dB			2.60	dB
		Gain = 28 dB			2.40	dB
		Gain = 29 to 30 dB			2.10	dB
		Gain = 31 dB			1.70	dB
Gain = 32 dB			1.60	dB		
Gain = 33 dB			1.40	dB		
Gain = 34 dB			1.35	Db		
Gain = 35 dB			1.15	dB		

Note 1: Verified by characterization.

Table 6. SKY65371-11 Electrical Specifications: IIP3 (Note 1)

(V_{DD} = 4.75 to 5.25 V, T_c = -40 °C to +85 °C, P_{IN} = -25 dBm, f = 880 to 915 MHz, Characteristic Impedance [Z₀] = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
3 rd Order Input Intercept Point	IIP3	Gain = 3 to 11 dB	+18			dBm
		Gain = 12 to 17 dB	+14			dBm
		Gain = 18 to 29 dB	+3			dBm
		Gain = 30 to 35 dB	+1			dBm

Note 1: Verified by characterization.

Table 7. SKY65371-11 Electrical Specifications: IP1dB (Note 1)

(V_{DD} = 4.75 to 5.25 V, T_c = -40 °C to +85 °C, P_{IN} = -25 dBm, f = 880 to 915 MHz, Characteristic Impedance [Z₀] = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
1 dB Input Compression Point	IP1dB	Gain = 3 to 5 dB	+10.5			dBm
		Gain = 6 to 11 dB	+8.0			dBm
		Gain = 12 to 17 dB	+4.0			dBm
		Gain = 18 to 28 dB	-7.0			dBm
		Gain = 29 dB	-10.0			dBm
		Gain = 30 to 35 dB	-14.0			dBm

Note 1: Verified by characterization.

Table 8. Gain Control Logic

	BPC1 (Pin 3)	BPC2 (Pin 6)
High gain	0	1
Low gain	1	0

Note: "1" = 3.0 V, "0" = 0 V.

Evaluation Board Description

The SKY65371-11 Evaluation Board is used to test the performance of the SKY65371-11 LNA. An assembly drawing for the Evaluation Board is shown in Figure 3 and the layer detail is provided in Figure 4. The layer detail physical characteristics are noted in Figure 5.

An Evaluation Board schematic diagram is provided in Figure 6. Table 9 provides the Bill of Materials (BOM) list for Evaluation Board components.

Package Dimensions

The PCB layout footprint for the SKY65371-11 is provided in Figure 7. Package dimensions for the 16-pin MCM are shown in Figure 8, and tape and reel dimensions are provided in Figure 9.

Package and Handling Information

Since the device package is sensitive to moisture absorption, it is baked and vacuum packed before shipping. Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY65371-11 is rated to Moisture Sensitivity Level 3 (MSL3) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to Skyworks Application Note, *PCB Design and SMT Assembly/Rework Guidelines for MCM-L Packages*, document number 101752.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

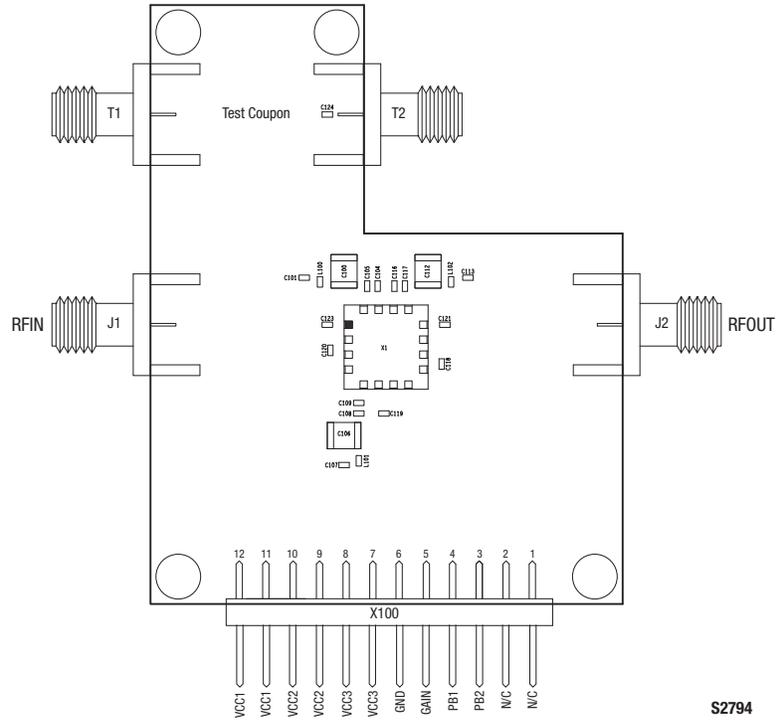
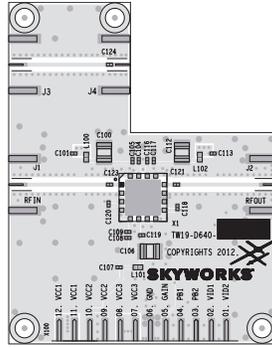
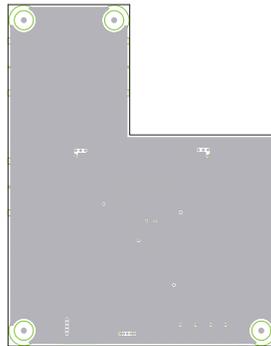


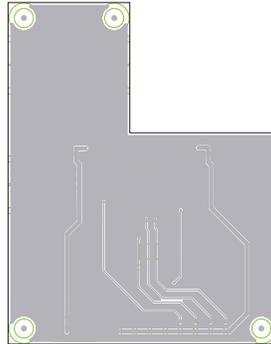
Figure 3. SKY65371-11 Evaluation Board Assembly Diagram



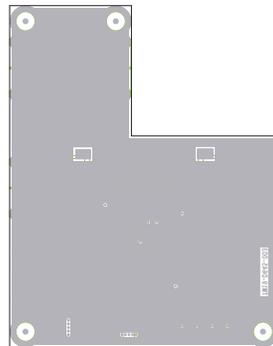
Layer 1: Top – Metal



Layer 2: Ground



Layer 3: Signal



Layer 4: Bottom Ground

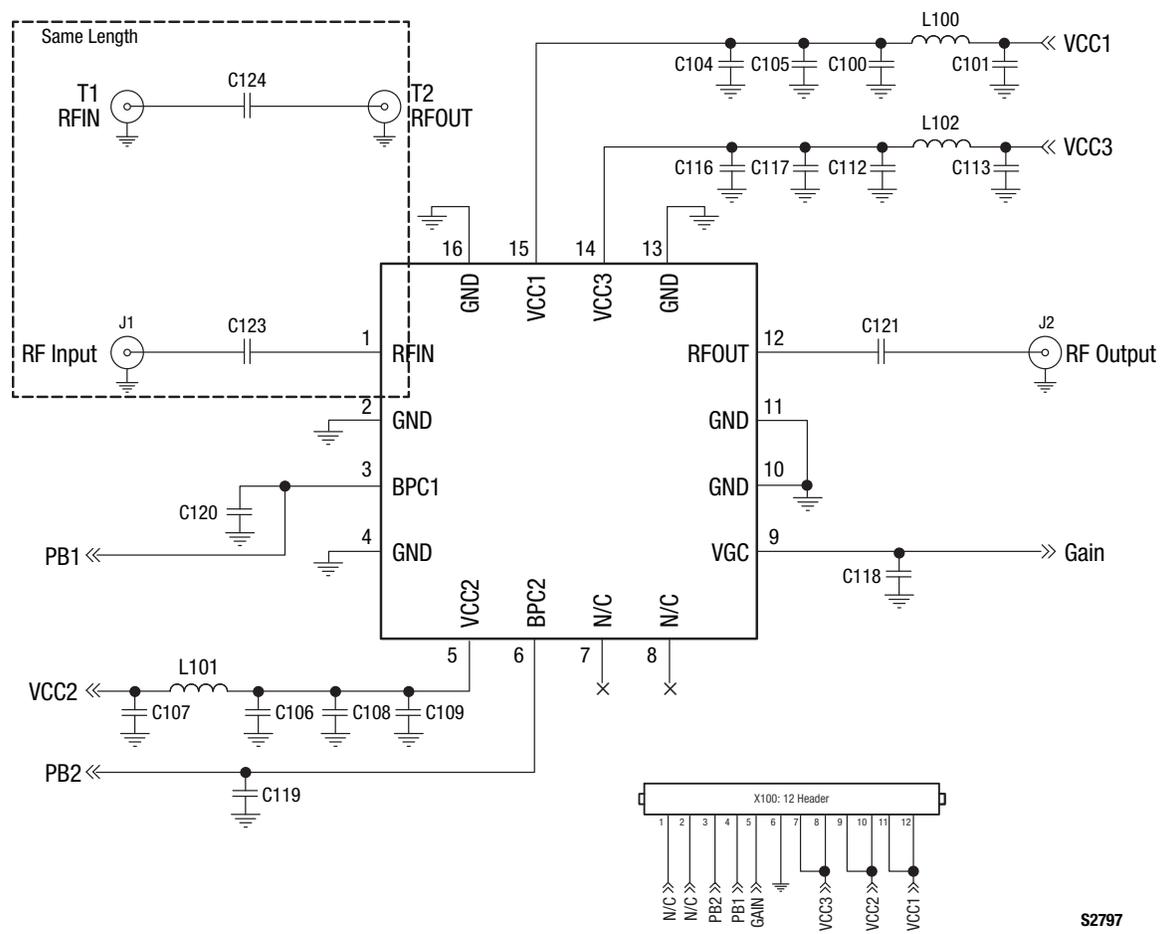
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Figure 4. SKY65371-11 Evaluation Board Layer Detail

Cross Section	Name	Thickness (mm)	Material
	Tmask	0.010	Solder Resist
	L1	0.035	-
	Dielectric	0.305	R04003C
	L2	0.018	-
	Dielectric	4 x 0.101	R04350B RF
	L3	0.018	-
	Dielectric	0.305	R04003C
	L4	0.035	-
	Bmask	0.010	Solder Resist

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Figure 5. Layer Detail Physical Characteristics

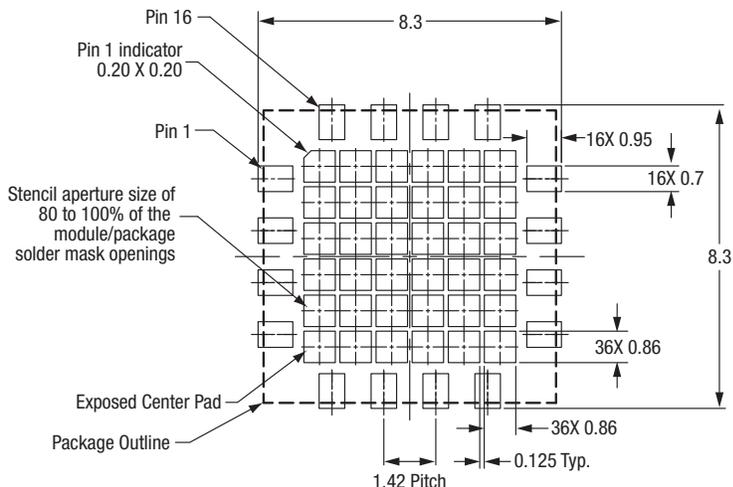


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Figure 6. SKY65371-11 Evaluation Board Schematic

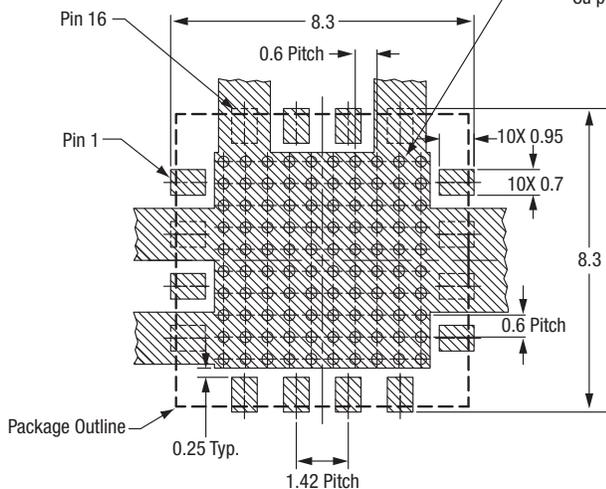
Table 9. SKY65371-11 Evaluation Board Bill of Materials

Component	Size	Value	Tolerance (%)
C100, C106, C112	1210	10 μ F	10
C101, C107, C113	0402	10 pF	5
C104, C109, C116, C124	0402	1000 pF	5
C105, C108, C117	0402	100 nF	10
C118	0402	DNI	–
C119, C120	0402	1000 pF	5
C121, C123	0402	150 pF	5
L100, L101, L102	0603	68 nH	2

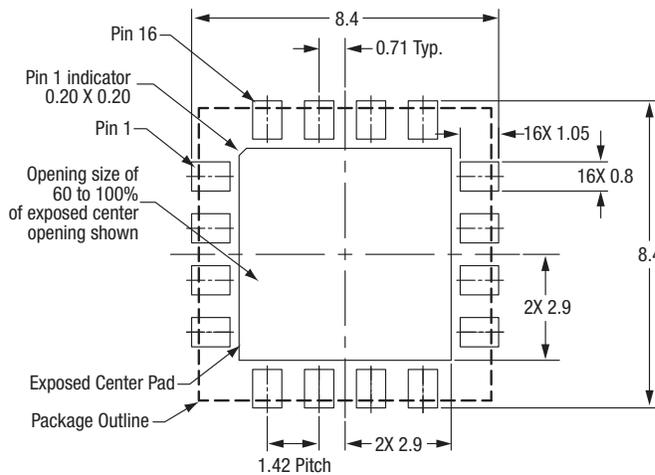


Stencil Aperture Top View

Thermal via array $\varnothing 0.3$ mm on 0.6 mm pitch will improve thermal performance.
 NOTE: thermal vias should be tented with solder mask, 30-35 μ m Cu plating recommended.



Metallization Top View

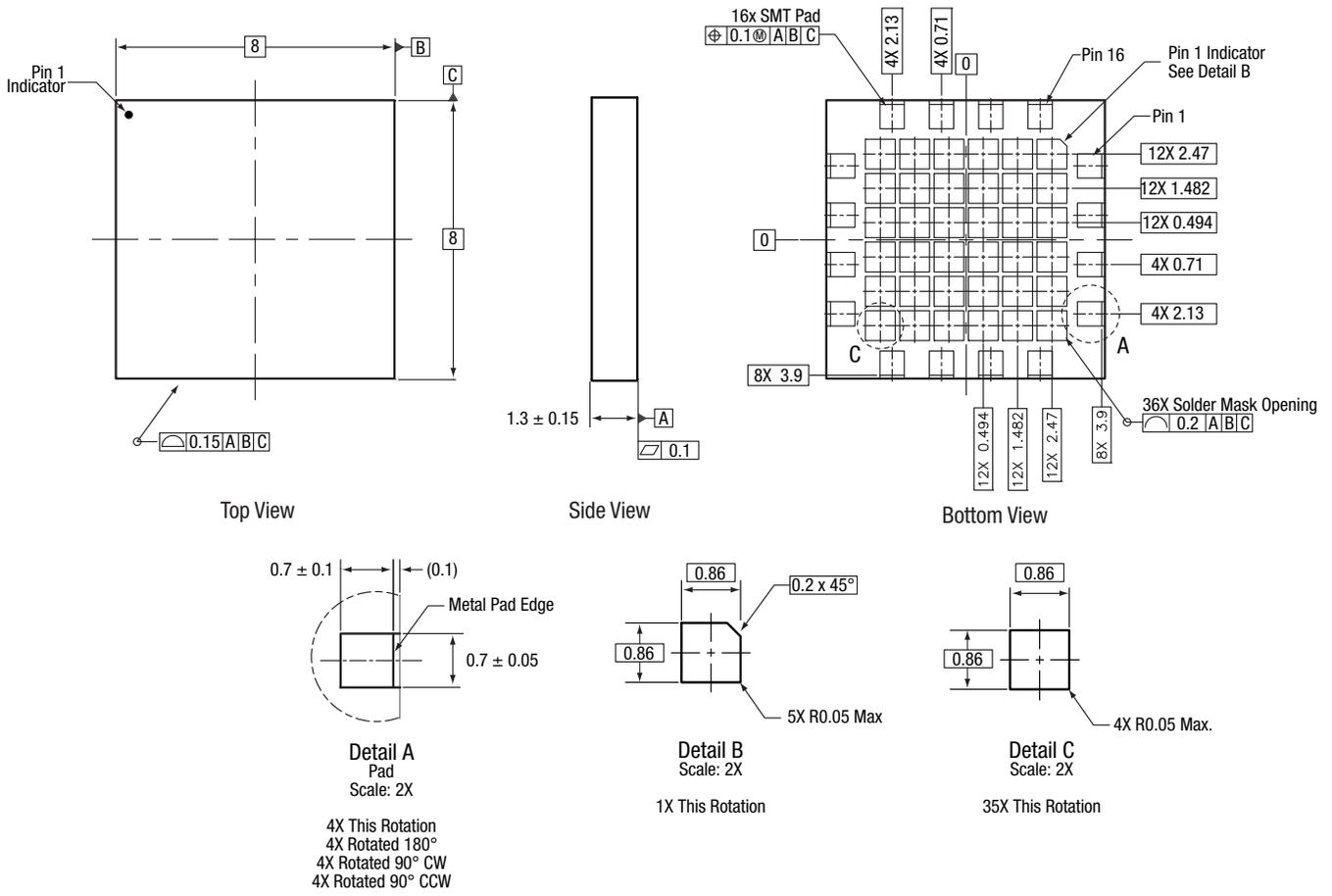


Solder Mask Opening Top View

All dimensions are in millimeters

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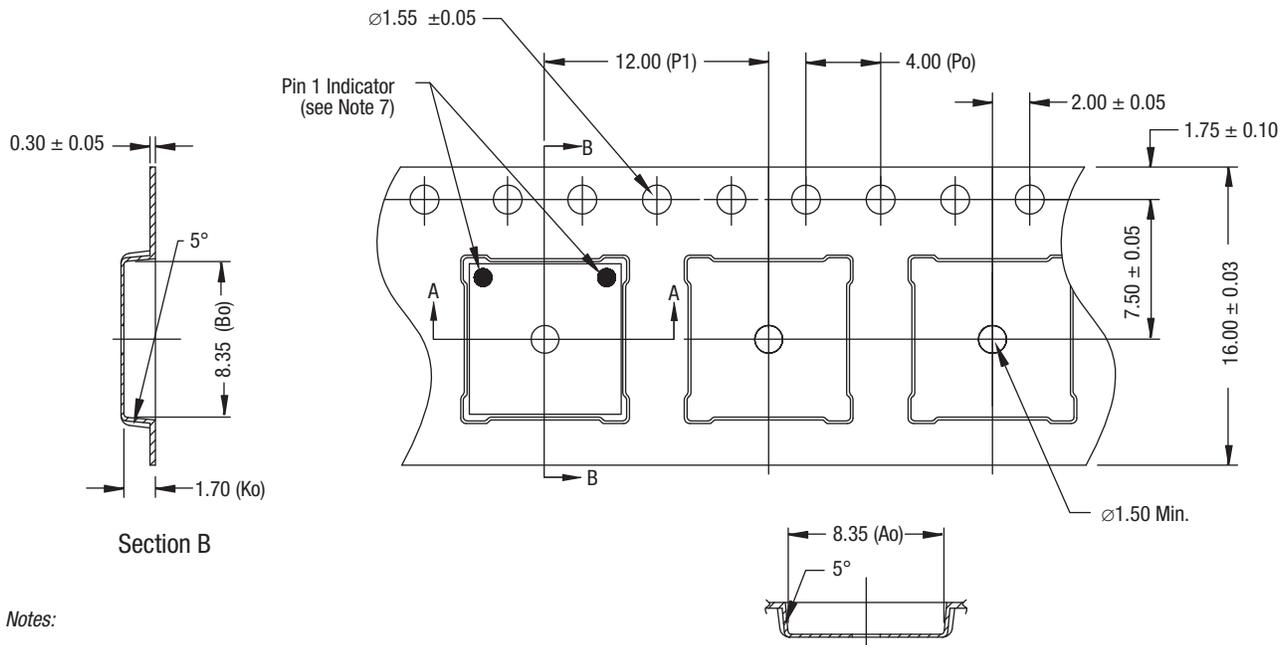
Figure 7. SKY65371-11 PCB Layout Footprint (Top View)



Dimensioning and tolerancing according to ASME Y14.5M-1994.
 All measurements are in millimeters

S2473

Figure 8. SKY65371-11 16-Pin MCM Package Dimensions



Notes:

1. Carrier tape: black conductive polystyrene.
2. Cover tape material: transparent conductive.
3. ESD-surface resistivity is $\leq 1 \times 10^{10}$ Ohms/square per EIA, JEDEC TNR Specification.
4. Po/P1 10 pitches cumulative tolerance on tape: ± 0.20 mm.
5. Ao and Bo measurement point to be 0.30 mm from bottom pocket.
6. All dimensions are in millimeters.
7. Pin 1 orientation is in top left corner for the following Skyworks products:

SKY77503-XX	SKY65369-11	SKY65374-11
SKY77506-XX	SKY65370-11	SKY65375-11
SKY77512-XX	SKY65371-11	SKY65376-11
SKY77526-XX	SKY65372-11	SKY65379-11
SKY77343-XX	SKY65373-11	

For all other 8 x 8 mm MCM/RFLGA products, pin 1 orientation is in top right corner.

Y0491

Figure 9. SKY65371-11 Tape and Reel Dimensions

Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number
SKY65371-11 LNA	SKY65371-11	TW19-D640

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