

ROHS

HALOGEN

FREE

Hyperfast Rectifier, 6 A FRED Pt®





DPAK (TO-252AA)

PRIMARY CHARACTERISTICS						
I _{F(AV)} 6 A						
V_{R}	600 V					
V _F at I _F	1.65 V					
t _{rr} (typ.)	14 ns					
T _J max.	175 °C					
Package	DPAK (TO-252AA)					
Circuit configuration	Single					

FEATURES

- Hyperfast recovery time, extremely low Q_{rr}
- 175 °C maximum operating junction temperature
- For PFC CCM operation
- Low forward voltage drop
- Low leakage current
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V_{RRM}		600	V			
Average rectified forward current	I _{F(AV)}	T _C = 136 °C	6				
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	50	Α			
Peak repetitive forward current	I _{FM}	$T_C = 136 ^{\circ}\text{C}, f = 20 \text{kHz}, d = 50 \%$	12				
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	W	
E	V _F	I _F = 6 A	-	2.50	3.1	V	
Forward voltage	VF	I _F = 6 A, T _J = 150 °C	-	1.65	1.9		
Dayawaa laakaga ayuwant	_	$V_R = V_R$ rated	-	-	20		
Reverse leakage current	IR	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	250	μA	
Junction capacitance	C _T	V _R = 600 V	-	3.5	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH	



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 A, dI_F/dt = 10$	00 A/μs, V _R = 30 V	=	14	21			
Reverse recovery time		$I_F = 1 A, dI_F/dt = 50$	$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		16	-			
neverse recovery time	t _{rr}	T _J = 25 °C	I _F = 6 A dI _F /dt = 200 A/μs V _R = 390 V	=	19	-	ns - A		
		T _J = 125 °C		=	27	-			
Dook recovery ourrent	I _{RRM}	T _J = 25 °C		-	3.0	-			
Peak recovery current		T _J = 125 °C		-	4.0	-			
Reverse recovery charge	Q _{rr}	T _J = 25 °C		=	28	-	nC		
		T _J = 125 °C		-	57	-			

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	-	3	°C/W	
Approximate weight				0.3		g	
Approximate weight				0.01		OZ.	
Marking device		Case style DPAK (TO-252AA)	6EWX06FNH				

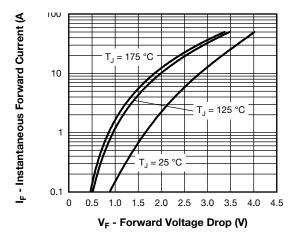


Fig. 1 - Typical Forward Voltage Drop Characteristics

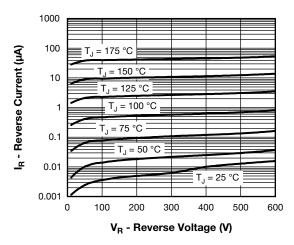


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

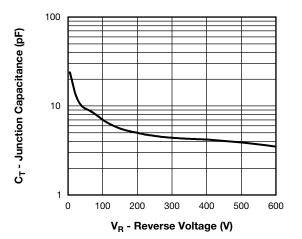


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

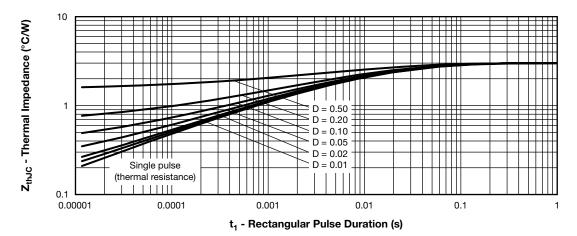


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

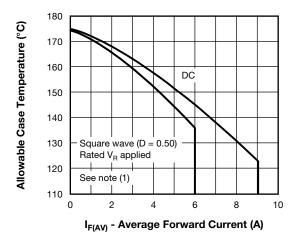


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

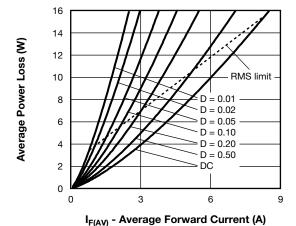


Fig. 6 - Forward Power Loss Characteristics

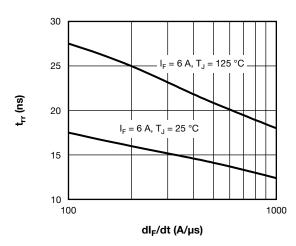


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

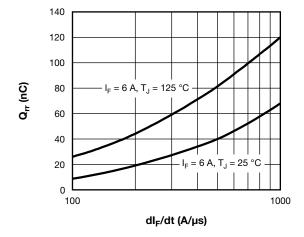


Fig. 8 - Typical Stored Charge vs. dI_F/dt

Note

⁽¹⁾ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R

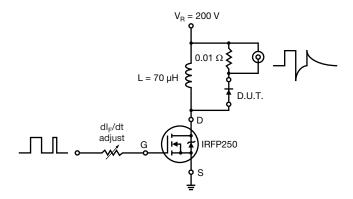
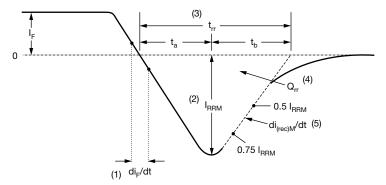


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RBM} and 0.50 I_{RBM} extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

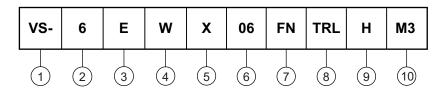
(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (6 = 6 A)

3 - Circuit configuration:

E = single diode

4 - Package identifier:

W = D-PAK

5 - X = hyperfast recovery time

Voltage rating (06 = 600 V)

7 - FN = TO-252AA

8 - • None = tube

TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

- H = AEC-Q101 qualified

Environmental digit:

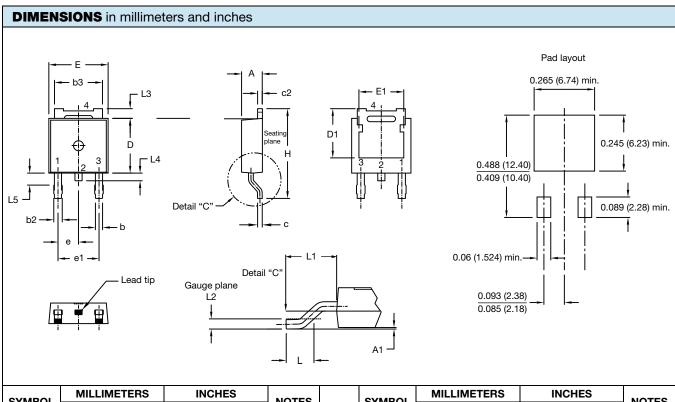
M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-6EWX06FNHM3	75	Antistatic plastic tube				
VS-6EWX06FNTRHM3	2000	13" diameter reel				
VS-6EWX06FNTRRHM3	3000	13" diameter reel				
VS-6EWX06FNTRLHM3	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				



DPAK (TO-252AA)



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	1	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	4.93	-	0.194	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MROI MILLIMETERS INCHES		NOTES		
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74	2.74 BSC		REF.	
L2	0.51	0.51 BSC		BSC	
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
	•	•		•	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Outline conforms to JEDEC® outline TO-252AA, except for D1 dimension



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Vishay

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