



100V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D T _A = +25°C
-100V	350mΩ @ V _{GS} = -10V	-2.4A
-1000	450mΩ @ V _{GS} = -6V	-2.1A

Description and Applications

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Motor controls
- DC-DC converters
- Power management functions
- · Relay and solenoid driving

Features and Benefits

- Fast Switching Speed
- Low Input Capacitance
- Low Gate Drive
- Lead-Free Finish; RoHS Compliant (Notes 1& 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
 A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

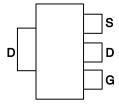
Mechanical Data

- Package: SOT223 (Type DN)
- Package Material: Molded Plastic; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208³
- Weight: 0.112 grams (Approximate)

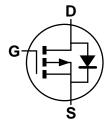
SOT223 (Type DN)



Top View



Pin Out - Top



Equivalent Circuit

Ordering Information (Note 4)

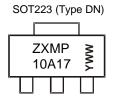
Part Number	Package	Packing		
Part Number	rackage	Qty.	Carrier	
ZXMP10A17GTA	SOT223 (Type DN)	1,000	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information



ZXMP 10A17 = Product Type Marking Code YWW = Date Code Marking Y or \overline{Y} = Last Digit of Year (ex: 2 = 2022) WW or \overline{WW} = Week Code (01~53)

Maximum Ratings (@ $T_A = +25$ °C unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	-100	V	
Gate-Source Voltage			V_{GS}	±20	V
		(Note 6)		-2.4	
Continuous Drain Current	$V_{GS} = 10V$	$T_A = +70^{\circ}C \text{ (Note 6)}$	I _D	-1.9	Α
		(Note 5)		-1.7	
Pulsed Drain Current	V _{GS} = 10V	(Note 7)	I _{DM}	-9.4	Α
Continuous Source Current (Body Diode) (Note 6)		(Note 6)	I _S	-2.4	Α
Pulsed Source Current (Body Diode) (Note 7)		I _{SM}	-9.4	Α	

Thermal Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 5)	D	2.0 16		
Linear Derating Factor	(Note 6)	P _D	3.9 31	mW/°C	
Thermal Resistance, Junction to Ambient	(Note 5)	В	62.5	°C/W	
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	32.0		
Thermal Resistance, Junction to Case (Note 8)		$R_{ hetaJL}$	9.8		
Operating and storage temperature range	T _J , T _{STG}	-55 to 150	°C		

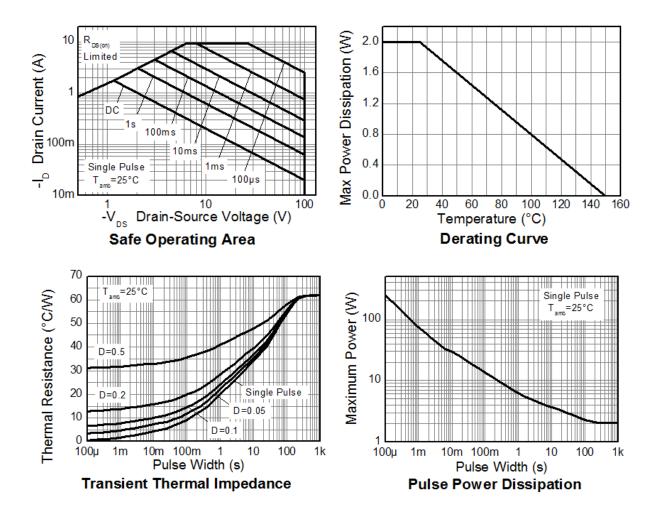
- 5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 6. Same as Note 5, except the device is measured at t ≤ 10 seconds.

 7. Same as Note 5, except the device is pulsed with D= 0.02 and pulse width 300 μs. The pulse current is limited by the maximum junction temperature.

 8. Thermal resistance from junction to solder-point (at the end of the drain lead).



Thermal Characteristics





Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

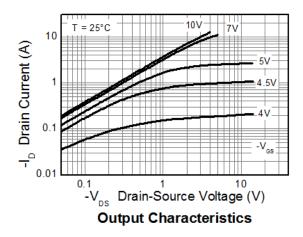
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	-100	_	_	V	$I_D = -250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-0.5	μA	V _{DS} = -100V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	V _{GS} = ±20V, V _{DS} = 0V	
ON CHARACTERISTICS							
Gate Threshold Voltage	$V_{GS(th)}$	-2.0	_	-4.0	V	$I_D=-250\mu A,\ V_{DS}=V_{GS}$	
Static Drain-Source On-Resistance (Note 9)	Б			0.350	Ω	V _{GS} = -10V, I _D = -1.4A	
Static Dialii-Source Off-Resistance (Note 9)	R _{DS(on)}	_	_	0.450	12	V _{GS} = -6V, I _D = -1.2A	
Forward Transconductance (Notes 9 & 10)	g fs	_	2.8	_	S	V _{DS} = -15V, I _D = -1.4A	
Diode Forward Voltage (Note 9)	V_{SD}	_	-0.85	-0.95	V	I _S = -1.7A, V _{GS} = 0V	
Reverse Recovery Time (Note 10)	t _{rr}	_	33	_	ns	I _F = -1.5A, di/dt= 100A/μs	
Reverse Recovery Charge (Note 10)	Q_{rr}	_	48	_	nC		
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C _{iss}	_	424	_	pF		
Output Capacitance	Coss	_	36.6	_	pF	V _{DS} = -50V, V _{GS} = 0V -f= 1MHz	
Reverse Transfer Capacitance	C _{rss}	_	29.8	_	pF		
Total Gate Charge (Note 11)	Q_g	_	7.1	_	nC	V _{GS} = -6.0V	
Total Gate Charge (Note 11)	Q_g	_	10.7	_	nC	V _{DS} = -50V	
Gate-Source Charge (Note 11)	Qgs	_	1.7	_	nC	V _{GS} = -10V I _D = -1.4A	
Gate-Drain Charge (Note 11)	Q_{gd}	_	3.8	_	nC	1	
Turn-On Delay Time (Note 11)	t _{D(on)}	_	3.0	_	ns	·	
Turn-On Rise Time (Note 11)	t _r	_	3.5	_	ns	V_{DD} = -15V, V_{GS} = -10V I_{D} = -1A, $R_{G} \cong 6.0\Omega$	
Turn-Off Delay Time (Note 11)	t _{D(off)}	_	13.4	—	ns		
Turn-Off Fall Time (Note 11)	t _f	_	7.2	_	ns		

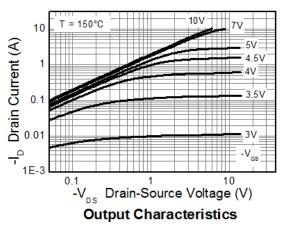
Notes:

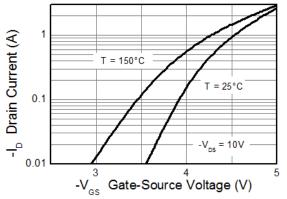
Measured under pulsed conditions. Pulse width ≤ 300μs; duty cycle ≤ 2%.
 For design aid only, not subject to production testing.
 Switching characteristics are independent of operating junction temperatures.

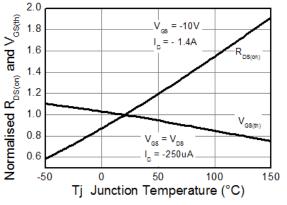


Typical Characteristics



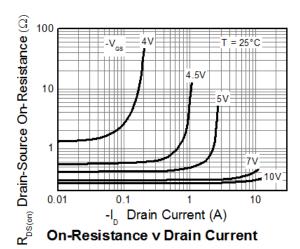


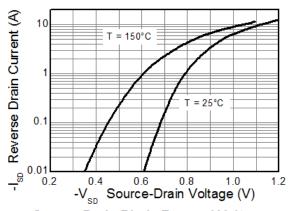




Typical Transfer Characteristics

Normalised Curves v Temperature

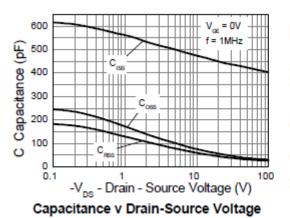


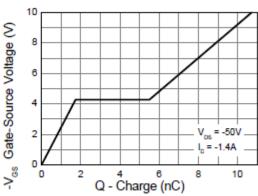


Source-Drain Diode Forward Voltage



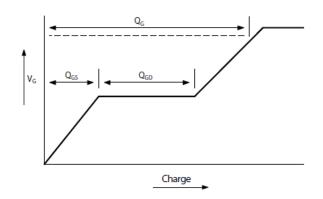
Typical Characteristics (continued)





Gate-Source Voltage v Gate Charge

Test Circuits

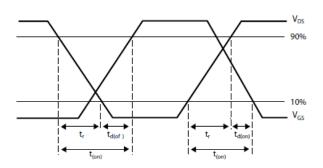


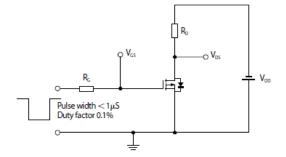
Same as D.U.T

Current regulator

Basic gate charge waveform

Gate charge test circuit





Switching time waveforms

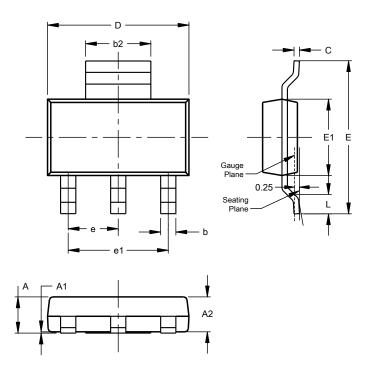
Switching time test circuit



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

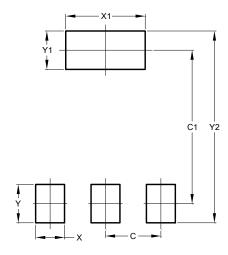
SOT223 (Type DN)



SOT223 (Type DN)				
Dim	Min	Max	Тур	
Α		1.70		
A1	0.01	0.15		
A2	1.50	1.68	1.60	
b	0.60	0.80	0.70	
b2	2.90	3.10		
С	0.20	0.32		
D	6.30	6.70		
Е	6.70	7.30		
E1	3.30	3.70		
е			2.30	
e1			4.60	
L	0.85			
All Dimensions in mm				

Suggested Pad Layout

SOT223 (Type DN)



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00



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