# **AN5279**

## Mono channel SEPP audio power amplifier IC

#### Overview

The AN5279 is a monolithic integrated circuit designed for 5.0 W (19 V, 8  $\Omega$ ) output audio power amplifier. It is a mono channel SEPP IC suitable for TV application.

#### ■ Features

- Few external components:
  - No Boucherot cells(output C, R)
  - No Bootstrap Capacitors
  - No Negative Feeback Capacitors
- Built-in muting circuit
- Built-in stand-by circuit
- Built-in various protection circuits (Load-short, thermal, over-voltage and current)
- High ripple rejection(55 dB)
- Operating voltage range 10 V to 24 V(19 V typ.)

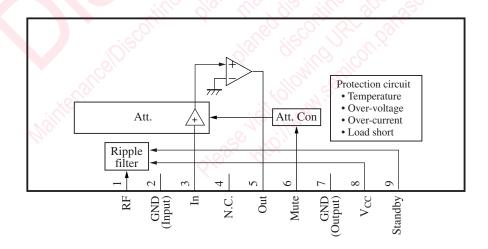
#### Applications

• TV

### ■ Package

• HSIP009-P-0000E

## Block Diagram



## ■ Pin Descriptions

| Pin No. | Descriptions  | Pin No. | Descriptions    |
|---------|---------------|---------|-----------------|
| 1       | Ripple filter | 6       | Mute            |
| 2       | Input GND     | 7       | Output GND      |
| 3       | Input         | 8       | V <sub>CC</sub> |
| 4       | Not connected | 9       | Standby         |
| 5       | Output        |         |                 |

## ■ Absolute Maximum Ratings

| Parameter                        | Symbol           | Rating      | Unit |
|----------------------------------|------------------|-------------|------|
| Supply voltage                   | V <sub>CC</sub>  | 26.0        | V    |
| Supply current                   | I <sub>CC</sub>  | 1.6         | A    |
| Power dissipation *2             | $P_{\mathrm{D}}$ | 6.2         | W    |
| Operating ambient temperature *1 | T <sub>opr</sub> | -25 to +75  | °C   |
| Storage temperature *1           | $T_{stg}$        | -55 to +150 | °C   |

Note) \*1: Except these items, all other measurements are taken at  $T_a = 25$  °C.

## ■ Recommended Operating Range

| Parameter      | Symbol          | Range        | Unit |
|----------------|-----------------|--------------|------|
| Supply voltage | V <sub>CC</sub> | 10.0 to 24.0 | V    |

<sup>\*2:</sup>  $T_a = 75$  °C with infinite heat sink.

## $\blacksquare$ Electrical Characteristics at $V_{CC}$ = 19 V, f = 1 kHz, $R_L$ = 8 $\Omega,\,T_a$ = 25 $^{\circ}C$

| Parameter                   | Symbol               | Conditions   | Min | Тур   | Max       | Unit |
|-----------------------------|----------------------|--|-----|-------|-----------|------|
| Quiescent current           | $I_{CQ}$             | $V_{IN} = 0 \text{ mV}$                            | _   | 25    | 50        | mA   |
| Output end noise voltage *1 | V <sub>NO</sub>      | No input, $R_g = 10 \text{ k}\Omega$               | _   | 0.22  | 0.4       | mV   |
| Voltage gain                | $G_{V}$              | $V_{IN} = 57 \text{ mV}$                           | 32  | 34    | 36        | dB   |
| Total harmonic distortion   | THD                  | $V_{IN} = 57 \text{ mV}$                           | _   | 0.2   | 0.4       | %    |
| Maximum Output Power        | P <sub>O1</sub>      | $V_{CC} = 19 \text{ V}, \text{THD} = 10 \%$        | 4.0 | 5.0   | _         | W    |
| Maximum Output power        | P <sub>O2</sub>      | V <sub>CC</sub> = 22 V, THD = 10 %                 | 5.6 | 7.0   | _         | W    |
| Ripple rejection ratio *1   | RR                   | $V_r = 1 V_{rms}$                                  | 45  | 55    | _         | dB   |
|                             |                      | $f_r = 120 \text{ Hz}, R_g = 10 \text{ k}\Omega$   |     |       |           |      |
| Muting Ratio                | MR                   | $V_{IN} = 57 \text{ mV}, V_{MUTE} > 3.0 \text{ V}$ | 70  | 80    | <u>6.</u> | dB   |
| Muting control voltage      | V <sub>MUTE</sub>    | $V_{IN} = 57 \text{ mV}, MR > 70 \text{ dB}$       | 3.0 | _6    | <u></u>   | V    |
| Standby on voltage          | V <sub>STD-ON</sub>  | No input, $I_{CC} \le 0.1 \text{ mA}$              |     | 10,00 | 5.0       | V    |
| Standby off voltage         | V <sub>STD-OFF</sub> | No input, $I_{CC} \ge 9.5 \text{ mA}$              | 8.5 | ) ]   | _         | V    |

Note) \*1: For this measurement, use the 20 Hz to 20 kHz (12 dB/OCT) filter.



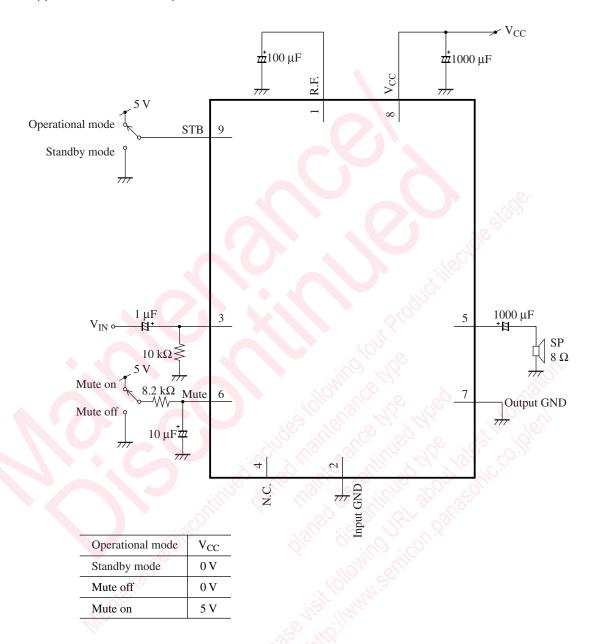
## ■ Terminal Equivalent Circuits

| Pin No. | Equivalent circuit   | Description  | DC voltage (V)                       |
|---------|--|--|--------------------------------------|
| 1       | 30 kΩ 1 Cy 251 | Ripple Filter This is the pin to connect the positive terminal of a ripple filter capacitor. | V <sub>CC</sub> - 1.5V <sub>BE</sub> |
| 2       |  | Input GND Input ground pin   | 0                                    |
| 3       | 3 200 Ω 400 Ω 30 kΩ 30 kΩ 2  | Input This is the amplifier input pin.   | Soilbeil                             |
| 4       | - Little bloom   | Not connected  | <u> </u>                             |
| 5       | Pre amp. Driver Cct $\sqrt{5}$ $\sqrt{V_{CC/2}}$ $\sqrt{30 \text{ k}\Omega}$   | Output Output pin  | V <sub>CC</sub> /2                   |

## ■ Terminal Equivalent Circuits (continued)

| Pin No. | Equivalent circuit  | Description   | DC voltage (V) |
|---------|---|---|----------------|
| 6       | $\begin{array}{c} 3 \text{ k}\Omega \\ \hline \\ 4 \text{ k}\Omega \\ \hline \\ 4 \text{ k}\Omega \\ \hline \\ 5 \text{ k}\Omega \\ \hline \\ 6 \text{ k}\Omega \\ \hline \\ 200 \Omega \\ \hline \\ 2 \text{ k}\Omega \\ \hline \\ 2 \text{ k}\Omega \\ \hline \\ 4 \text{ k}\Omega \\ \\ 4 \text{ k}\Omega \\ \hline \\ 4 $ | Mute input pin. Mute 'on' = 5 V Mute 'off' = 0 V                                      | <u>-</u>       |
| 7       |   | Output GND Output ground.   | 0              |
| 8       | -   | V <sub>CC</sub> This is the power supply pin.   | 19 V(typ.)     |
| 9       | 8<br>9<br>5 kΩ<br>1 5 kΩ<br>2<br>2  | Standby Standby control pin. Standby mode = $0 \text{ V}$ Operational mode = $V_{CC}$ | ioillein       |

### ■ Application Circuit Example



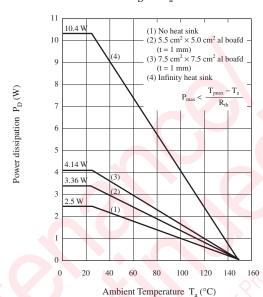
#### ■ Usage notes

- 1) External heatsink is needed when used. External heatsink should be fixed to the chassis.
- 2) Fin of the IC can be connected to GND.
- 3) Please prevent "Output to  $V_{CC}$  short", "Output to GND short" and "Reverse Insertion" to avoid damaging the IC.
- 4) The temperature protection circuit will operate at T<sub>j</sub> around 150 °C. However, if temperature decreases, the protection circuit would automatically be deactivated and resume normal operation.

#### ■ Technical Information

•  $P_D - T_a$  curves of HSI P009-P-0000E

$$P_D - T_a$$



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