



PJD14P10A

100V P-Channel Enhancement Mode MOSFET

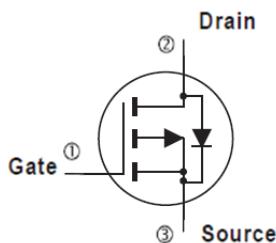
Voltage **-100 V** **Current** **-14 A**

Features

- $R_{DS(ON)}$, $V_{GS} @ -10V, I_D @ -7A < 140m\Omega$
- $R_{DS(ON)}$, $V_{GS} @ -4.5V, I_D @ -3A < 170m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case: TO-252AA Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0104 ounces, 0.297grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	-100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current $T_C=25^\circ C$	I_D	-14	A
$T_C=100^\circ C$	I_D	-9	
Pulsed Drain Current ^(Note 1)	I_{DM}	-40	
Power Dissipation $T_C=25^\circ C$	P_D	60	W
$T_C=100^\circ C$	P_D	24	
Continuous Drain Current $T_A=25^\circ C$	I_D	-2.5	A
$T_A=70^\circ C$	I_D	-2.0	
Power Dissipation	P_D	2.0	W
Power Dissipation	P_D	1.3	
Single Pulse Avalanche Energy ^(Note 6)	E_{AS}	20	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	°C
Typical Thermal Resistance ^(Note 4,5)	Junction to Case	$R_{\theta JC}$	°C/W
Junction to Ambient	$R_{\theta JA}$	62.5	

- Limited only By Maximum Junction Temperature



PJD14P10A

Electrical Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	-2.0	-3.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-7A$	-	115	140	$m\Omega$
		$V_{GS}=-4.5V, I_D=-3A$	-	130	170	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-100V, V_{GS}=0V$	-	-	-1.0	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Dynamic <small>(Note 5)</small>						
Total Gate Charge	Q_g	$V_{DS}=-50V, I_D=-7A,$ $V_{GS}=-10V$ <small>(Note 1,2)</small>	-	40.7	-	nC
Gate-Source Charge	Q_{gs}		-	7.8	-	
Gate-Drain Charge	Q_{gd}		-	6.4	-	
Input Capacitance	C_{iss}	$V_{DS}=-30V, V_{GS}=0V,$ $f=1.0MHz$	-	2298	-	pF
Output Capacitance	C_{oss}		-	136	-	
Reverse Transfer Capacitance	C_{rss}		-	92	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=-30V, I_D=-1A,$ $V_{GS}=-10V, R_G=6.2\Omega$ <small>(Note 1,2)</small>	-	28	-	ns
Turn-On Rise Time	t_r		-	12	-	
Turn-Off Delay Time	$t_{d(off)}$		-	151	-	
Turn-Off Fall Time	t_f		-	46	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	-14	A
Reverse Recovery Time	V_{SD}	$I_S=-1A, V_{GS}=0V$	-	-0.8	-1.2	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics
3. Repetitive rating, pulse width limited by junction temperature $T_J(MAX)=150^\circ C$. Ratings are based on low frequency and duty cycles to keep initial $T_J = 25^\circ C$.
4. The maximum current rating is package limited
5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper
6. $L=0.1mH, I_{AS}=-20A, V_{GS}=-10V, V_{DS}=-25V, R_G=25\text{ ohm}$, Starting $T_J=25^\circ C$
7. Guaranteed by design, not subject to production testing.



PJD14P10A

TYPICAL CHARACTERISTIC CURVES

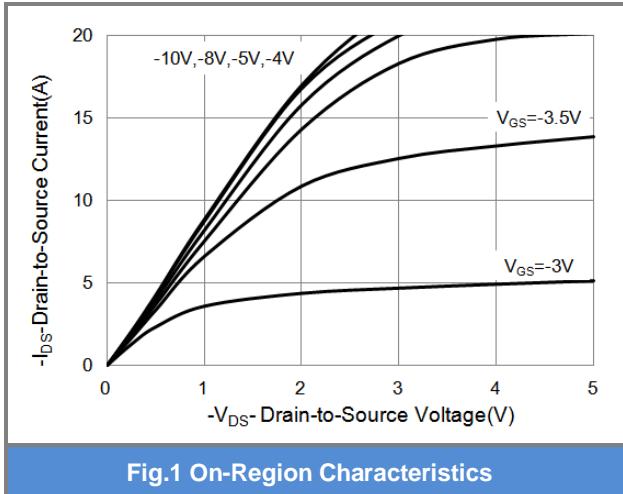


Fig.1 On-Region Characteristics

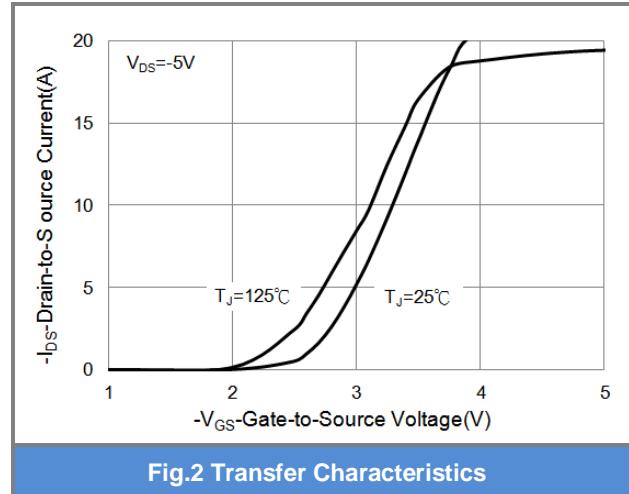


Fig.2 Transfer Characteristics

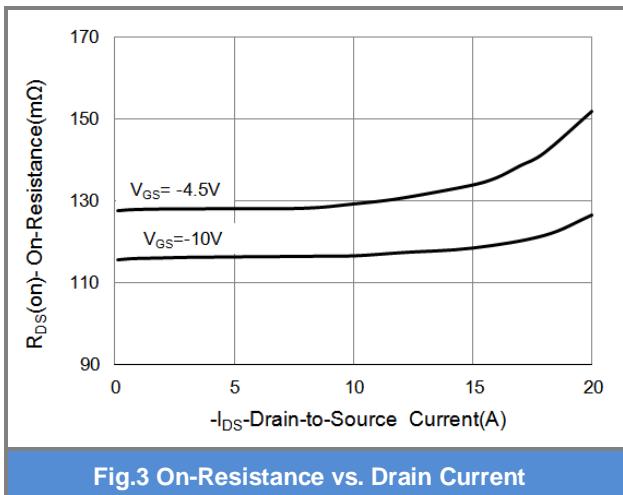


Fig.3 On-Resistance vs. Drain Current

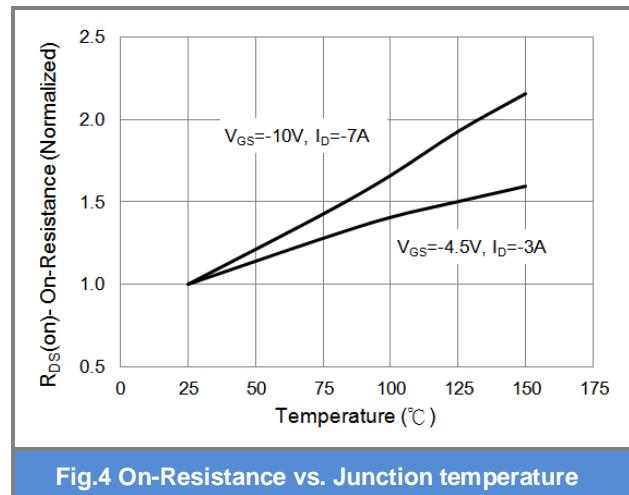


Fig.4 On-Resistance vs. Junction temperature

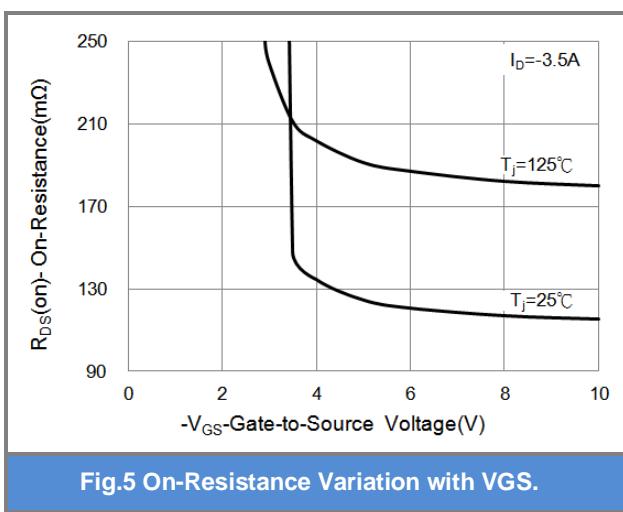


Fig.5 On-Resistance Variation with VGS.

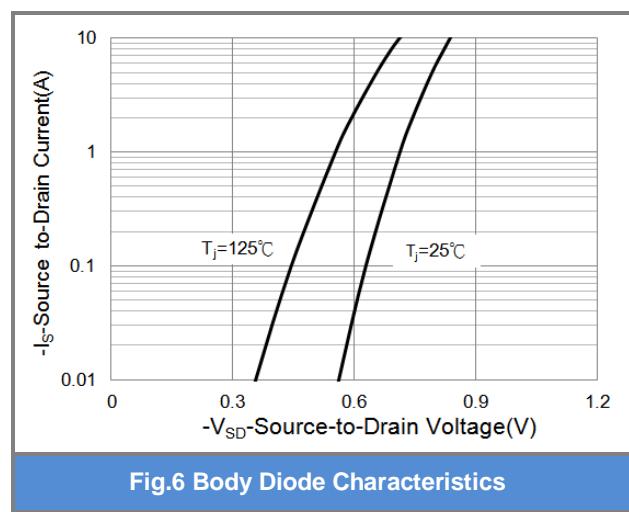


Fig.6 Body Diode Characteristics



PJD14P10A

TYPICAL CHARACTERISTIC CURVES

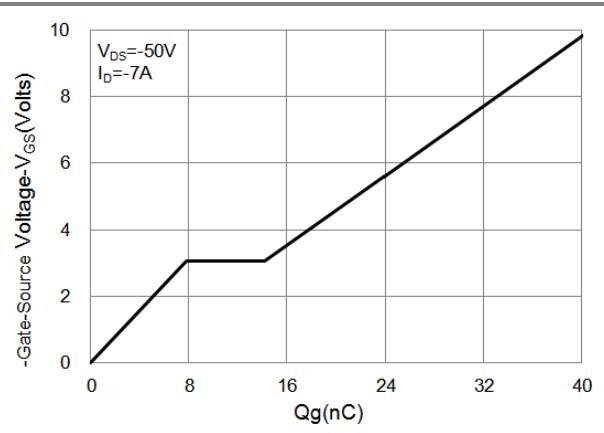


Fig.7 Gate-Charge Characteristics

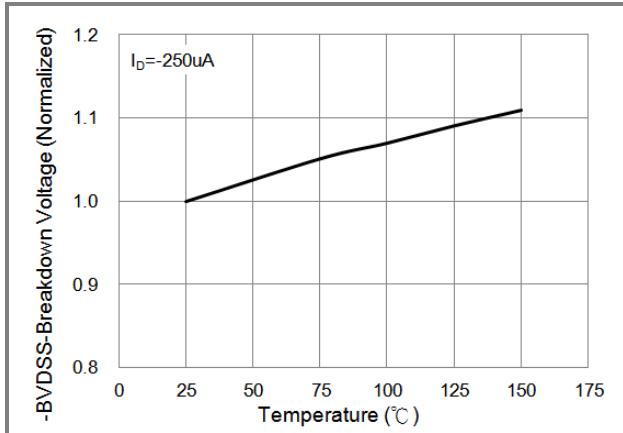


Fig.8 Breakdown Voltage Variation vs. Temperature

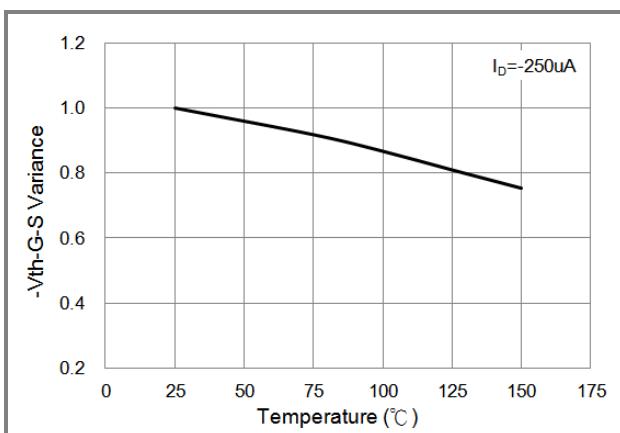


Fig.9 Threshold Voltage Variation with Temperature

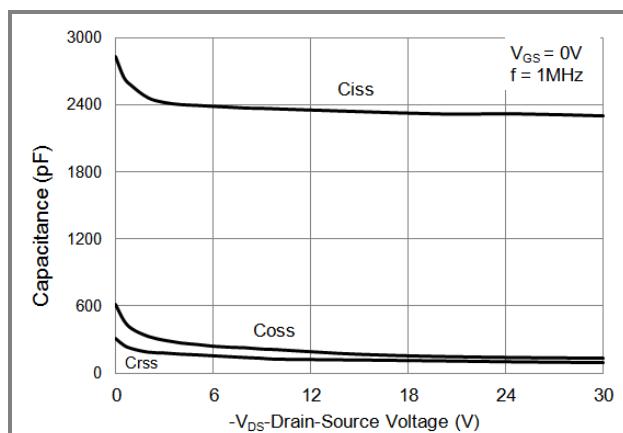


Fig.10 Capacitance vs. Drain-Source Voltage

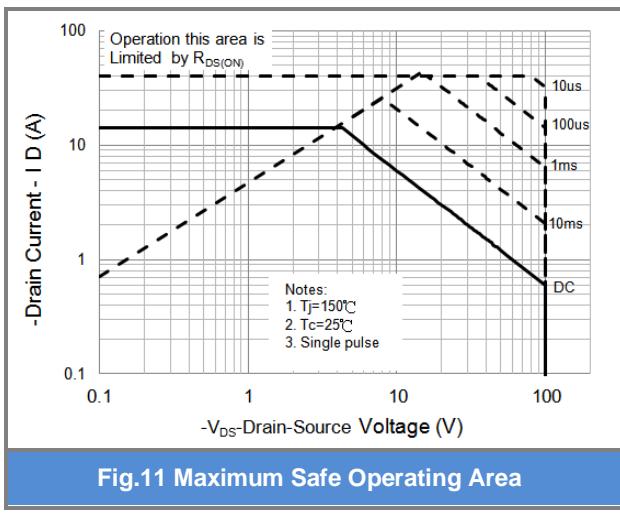
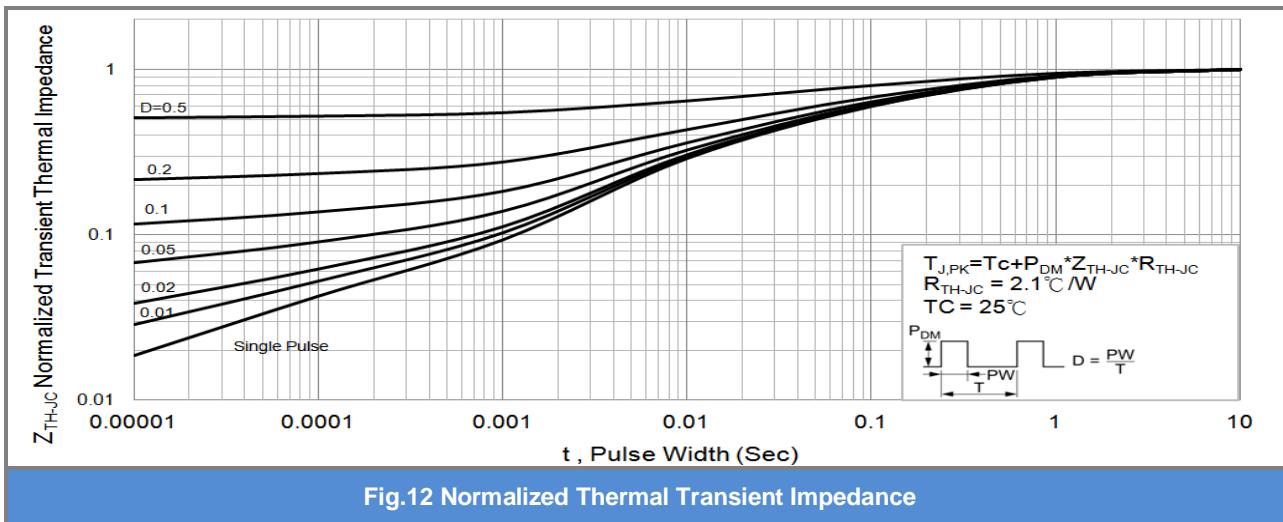


Fig.11 Maximum Safe Operating Area



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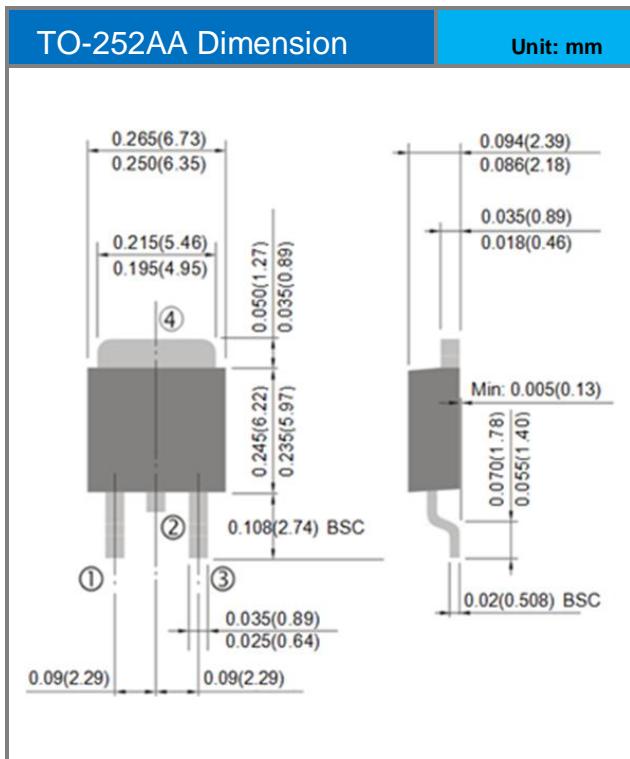
TYPICAL CHARACTERISTIC CURVES





PJD14P10A

Packaging Information



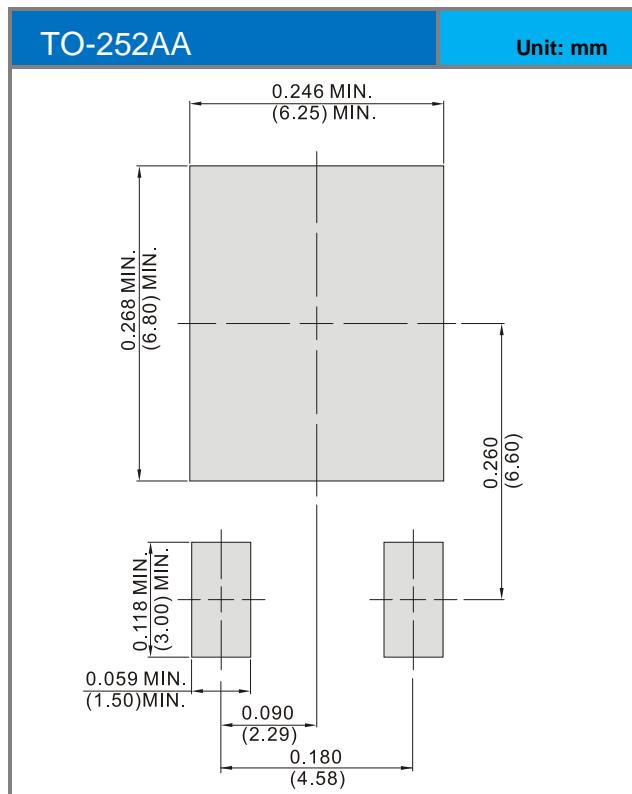


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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJD14P10A_L2_00001	TO-252AA	3,000pcs / 13" reel	D14P10A	Halogen free

MOUNTING PAD LAYOUT





PJD14P10A

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