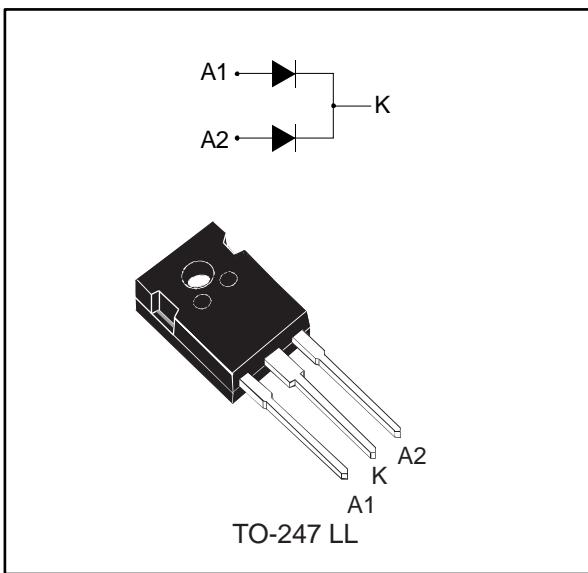


## 1200 V power Schottky silicon carbide diode

Datasheet - production data



### Features

- No or negligible reverse recovery
- Switching behavior independent of temperature
- Robust high voltage periphery
- Operating  $T_j$  from  $-40^\circ\text{C}$  to  $175^\circ\text{C}$
- ECOPACK®2 compliant

### Description

The SiC diode, available in TO-247 LL, is an ultrahigh performance power Schottky rectifier. It is manufactured using a silicon carbide substrate. The wide band-gap material allows the design of a low  $V_F$  Schottky diode structure with a 1200 V rating. Due to the Schottky construction, no recovery is shown at turn-off and ringing patterns are negligible. The minimal capacitive turn-off behavior is independent of temperature.

Especially suited for use in PFC and secondary side applications, this ST SiC diode will boost the performance in hard switching conditions. This rectifier will enhance the performance of the targeted application. Its high forward surge capability ensures a good robustness during transient phases.

Table 1: Device summary

Symbol	Value
$I_{F(AV)}$	2 x 5 A
$V_{RRM}$	1200 V
$T_j$ (max.)	175 °C
$V_F$ (typ.)	1.35 V

# 1 Characteristics

Table 2: Absolute ratings (limiting values per diode at 25 °C, unless otherwise specified)

Symbol	Parameter			Value	Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage ( $T_j = -40$ °C to +175 °C)			1200	V
I <sub>F(RMS)</sub>	Forward rms current			20	A
I <sub>F(AV)</sub>	Average forward current	T <sub>C</sub> = 160 °C, DC current	Per diode/per device	5/10	A
		T <sub>C</sub> = 135 °C, DC current		9/18	
		T <sub>C</sub> = 25 °C, DC current		19/38	
I <sub>FRM</sub>	Repetitive peak forward current	T <sub>C</sub> = 160 °C, T <sub>j</sub> = 175 °C, δ = 0.1			19 A
I <sub>FSM</sub>	Surge non repetitive forward current	t <sub>p</sub> = 10 ms sinusoidal	T <sub>C</sub> = 25 °C	35	A
			T <sub>C</sub> = 150 °C	30	
		t <sub>p</sub> = 10 μs square	T <sub>C</sub> = 25 °C	210	
T <sub>stg</sub>	Storage temperature range			-65 to +175	°C
T <sub>j</sub>	Operating junction temperature range			-40 to +175	°C

Table 3: Thermal resistance parameters

Symbol	Parameter		Typ. value	Max. value	Unit
R <sub>th(j-c)</sub>	Junction to case	Per diode	1.0	1.4	°C/W
		Per device	0.5	0.7	

Table 4: Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
I <sub>R</sub> <sup>(1)</sup>	Reverse leakage current	T <sub>j</sub> = 25 °C	V <sub>R</sub> = V <sub>RRM</sub>	-	2.5	30	μA
		T <sub>j</sub> = 150 °C		-	15	200	
V <sub>F</sub> <sup>(2)</sup>	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 5 A	-	1.35	1.50	V
		T <sub>j</sub> = 150 °C		-	1.75	2.25	

**Notes:**(1)Pulse test: t<sub>p</sub> = 10 ms, δ < 2%(2)Pulse test: t<sub>p</sub> = 500 μs, δ < 2%

To evaluate the conduction losses, use the following equation:

$$P = 1.08 \times I_{F(AV)} + 0.233 \times I_{F(RMS)}^2$$

Table 5: Dynamic electrical characteristics (per diode)

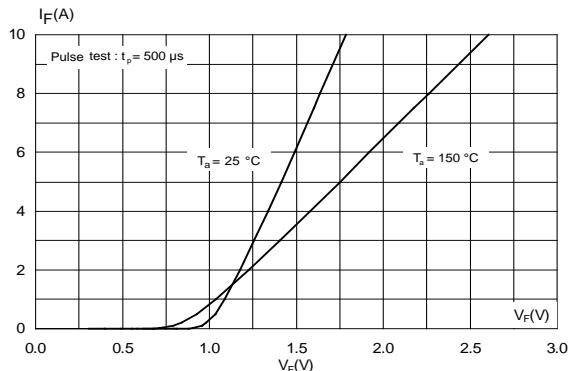
Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$Q_{Cj}^{(1)}$	Total capacitive charge	$V_R = 800 \text{ V}$	-	36	-	nC
$C_j$	Total capacitance	$V_R = 0 \text{ V}, T_c = 25 \text{ }^\circ\text{C}, F = 1 \text{ MHz}$	-	450	-	pF
		$V_R = 800 \text{ V}, T_c = 25 \text{ }^\circ\text{C}, F = 1 \text{ MHz}$	-	29	-	

**Notes:**

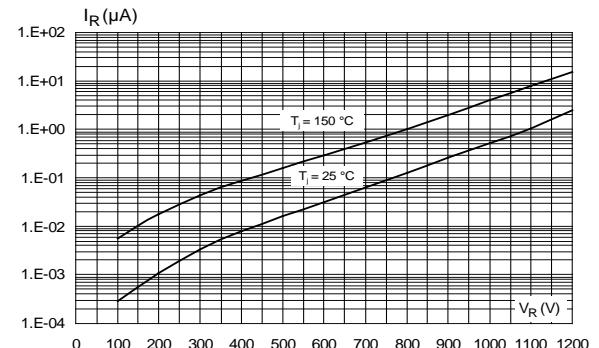
(1) Most accurate value for the capacitive charge:  $Q_{cj}(V_R) = \int_0^{V_R} C_j(V) dV$

## 1.1 Characteristics (curves)

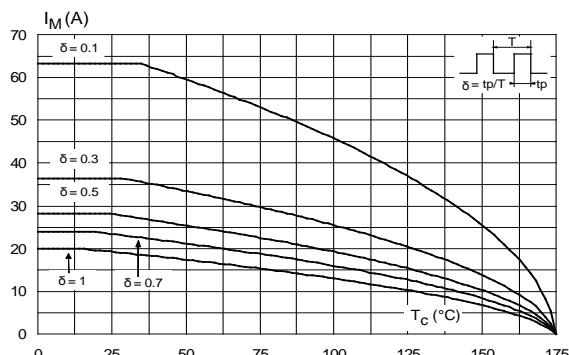
**Figure 1: Forward voltage drop versus forward current (typical values, per diode)**



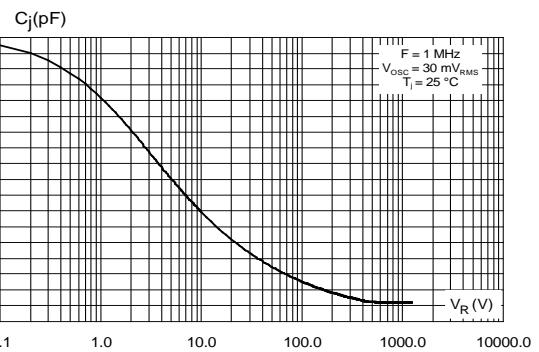
**Figure 2: Reverse leakage current versus reverse voltage applied (typical values, per diode)**



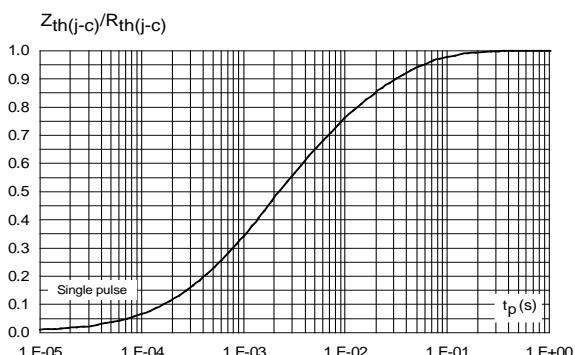
**Figure 3: Peak forward current versus case temperature (per diode)**



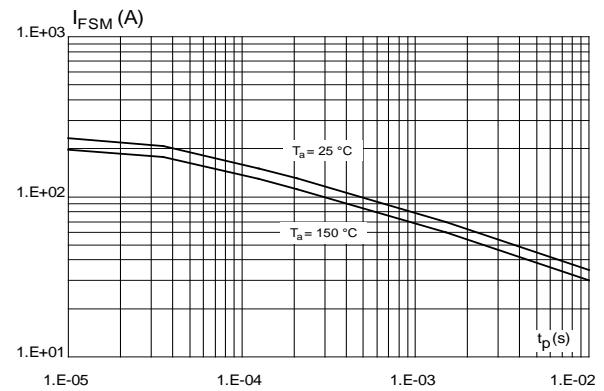
**Figure 4: Junction capacitance versus reverse voltage applied (typical values, per diode)**



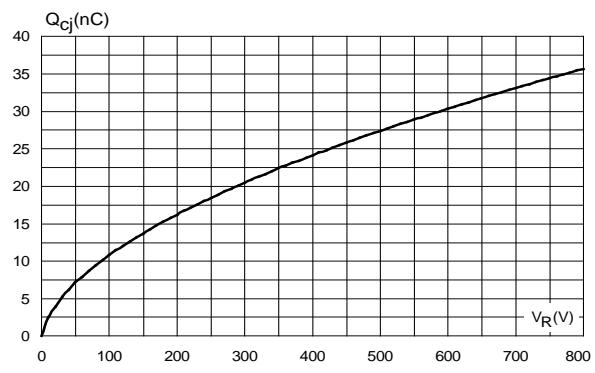
**Figure 5: Relative variation of thermal impedance junction to case versus pulse duration**



**Figure 6: Non-repetitive peak surge forward current versus pulse duration (sinusoidal waveform, per diode)**



**Figure 7: Total capacitive charges versus reverse voltage applied (typical values, per diode)**



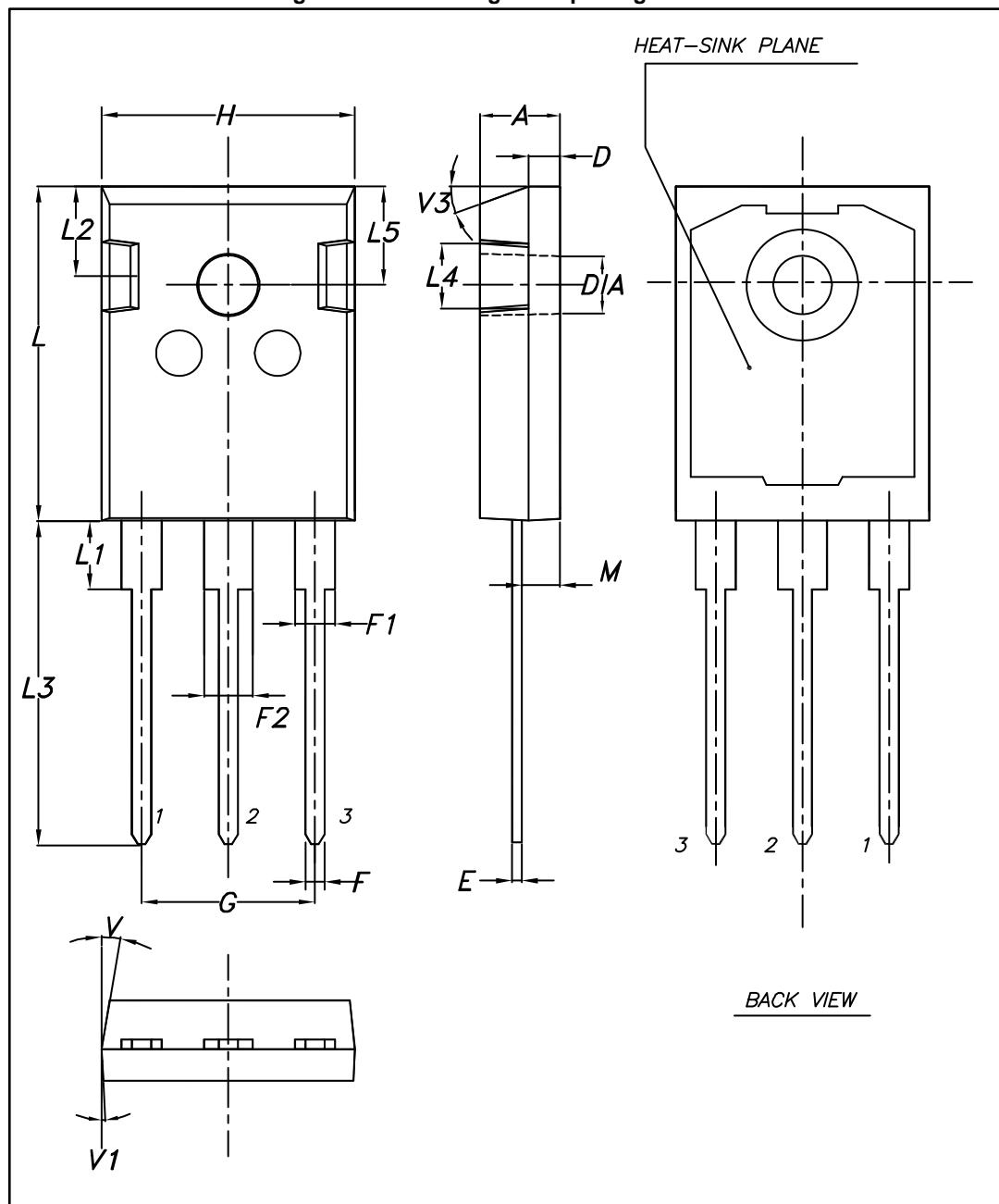
## 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com).  
ECOPACK® is an ST trademark.

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)
- Recommended torque value: 0.9 to 1.2 N·m

## 2.1 TO-247 long leads package information

Figure 8: TO-247 long leads package outline



**Table 6: TO-247 long leads package mechanical data**

Dim.	mm.			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.90		5.15	0.192		0.202
D	1.85		2.10	0.072		0.082
E	0.55		0.67	0.021		0.026
F	1.07		1.32	0.042		0.051
F1	1.90		2.38	0.074		0.093
F2	2.87		3.38	0.110		0.133
G	10.90 BSC			0.429 BSC		
H	15.77		16.02	0.620		0.630
L	20.82		21.07	0.810		0.820
L1	4.16		4.47	0.163		0.175
L2	5.49		5.74	0.216		0.225
L3	20.05		20.30	0.789		0.799
L4	3.68		3.93	0.144		0.154
L5	6.04		6.29	0.237		0.247
M	2.25		2.55	0.088		0.100
V		10°			10°	
V1		3°			3°	
V3		20°			20°	
DIA	3.55		3.66	0.139		0.143

### 3 Ordering information

Table 7: Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STPSC10H12CWL	STPSC10H12CWL	TO-247 LL	6.09 g	30	Tube

### 4 Revision history

Table 8: Document revision history

Date	Revision	Changes
28-Feb-2017	1	Initial release.

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