

# PNP Epitaxial Silicon Transistor

# **SS8550**

# **Features**

- 2 W Output Amplifier of Portable Radios in Class B Push–Pull Operation
- Complementary to SS8050
- Collector Current:  $I_C = 1.5 A$
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

# ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

| Parameter                 | Symbol           | Value      | Unit |
|---------------------------|------------------|------------|------|
| Collector-Base Voltage    | V <sub>CBO</sub> | -40        | V    |
| Collector–Emitter Voltage | V <sub>CEO</sub> | -25        | V    |
| Emitter-Base Voltage      | V <sub>EBO</sub> | -6         | V    |
| Collector Current         | I <sub>C</sub>   | -1.5       | Α    |
| Junction Temperature      | T <sub>J</sub>   | 150        | °C   |
| Storage Temperature       | T <sub>STG</sub> | -65 to 150 | °C   |

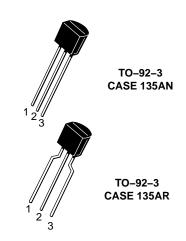
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

# THERMAL CHARACTERISTICS (Note 1)

(T<sub>A</sub> = 25°C unless otherwise noted)

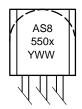
| Parameter                                  | Symbol         | Value | Unit  |
|--|----------------|-------|-------|
| Power Dissipation                          | $P_{D}$        | 1     | W     |
| Power Dissipation Derate Above 25°C        | $P_{D}$        | 8     | mW/°C |
| Thermal Resistance,<br>Junction–to–Ambient | $R_{	heta JA}$ | 125   | °C/W  |

<sup>1.</sup> PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.



- 1. Emitter
- 2. Base
- 3. Collector

# **MARKING DIAGRAM**



S8550x = Specific Device Code Line 1: A = Assembly Location

Line 2: x = C or D Line 3: Y = Year

WW= Work Week

# **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

# **SS8550**

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

| Symbol                | Parameter                            | Conditions   | Min. | Тур.  | Max.  | Unit |
|-----------------------|--------------------------------------|--|------|-------|-------|------|
| BV <sub>CBO</sub>     | Collector-Base Breakdown Voltage     | $I_C = -100 \mu A, I_E = 0$                          | -40  |       |       | V    |
| BV <sub>CEO</sub>     | Collector–Emitter Breakdown Voltage  | $I_C = -2 \text{ mA}, I_B = 0$                       | -25  |       |       | V    |
| BV <sub>EBO</sub>     | Emitter-Base Breakdown Voltage       | $I_E = -100 \ \mu A, \ I_C = 0$                      | -6   |       |       | V    |
| I <sub>CBO</sub>      | Collector Cut-Off Current            | $V_{CB} = -35 \text{ V}, I_{E} = 0$                  |      |       | -100  | nA   |
| I <sub>EBO</sub>      | Emitter Cut-Off Current              | $V_{EB} = -6 \text{ V}, I_C = 0$                     |      |       | -100  | nA   |
| h <sub>FE1</sub>      | DC Current Gain                      | $V_{CE} = -1 \text{ V, } I_{C} = -5 \text{ mA}$      | 45   | 170   |       |      |
| h <sub>FE2</sub>      |                                      | $V_{CE} = -1 \text{ V, } I_{C} = -100 \text{ mA}$    | 85   | 160   | 300   |      |
| h <sub>FE3</sub>      |                                      | $V_{CE} = -1 \text{ V, } I_{C} = -800 \text{ mA}$    | 40   | 80    |       |      |
| V <sub>CE</sub> (sat) | Collector–Emitter Saturation Voltage | $I_C = -800 \text{ mA}, I_B = -80 \text{ mA}$        |      | -0.28 | -0.50 | V    |
| V <sub>BE</sub> (sat) | Base-Emitter Saturation Voltage      | $I_C = -800 \text{ mA}, I_B = -80 \text{ mA}$        |      | -0.98 | -1.20 | V    |
| V <sub>BE</sub> (on)  | Base-Emitter On Voltage              | $V_{CE} = -1 \text{ V, } I_{C} = -10 \text{ mA}$     |      | -0.66 | -1.00 | V    |
| C <sub>ob</sub>       | Output Capacitance                   | $V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$ |      | 15    |       | pF   |
| f <sub>T</sub>        | Current Gain Bandwidth Product       | $V_{CE} = -10 \text{ V}, I_{C} = -50 \text{ mA}$     | 100  | 200   |       | MHz  |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

# $h_{\mbox{\scriptsize FE}}$ CLASSIFICATION

| Classification   | С         | D         |
|------------------|-----------|-----------|
| h <sub>FE2</sub> | 120 ~ 200 | 160 ~ 300 |

# ORDERING INFORMATION

| Part Number | Top Mark | Package                       | Shipping               |
|-------------|----------|-------------------------------|------------------------|
| SS8550CBU   | S8550C   | TO-92-3, case 135AN (Pb-Free) | 10,000 Units/ Bulk Box |
| SS8550CTA   | S8550C   | TO-92-3, case 135AR (Pb-Free) | 2,000 Units/ Fan-Fold  |
| SS8550DBU   | S8550D   | TO-92-3, case 135AN (Pb-Free) | 10,000 Units/ Bulk Box |
| SS8550DTA   | S8550D   | TO-92-3, case 135AR (Pb-Free) | 2,000 Units/ Fan-Fold  |

# **TYPICAL PERFORMANCE CHARACTERISTICS**

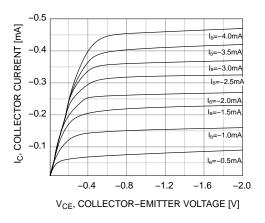


Figure 1. Static Characteristic

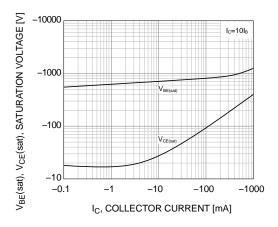


Figure 3. Base–Emitter Saturation Voltage and Collector–Emitter Saturation Voltage

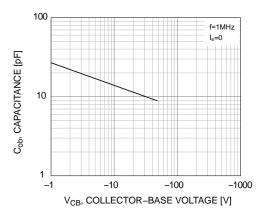


Figure 5. Collector Output Capacitance

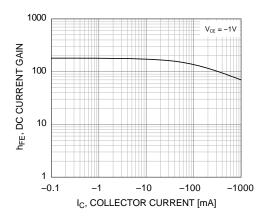


Figure 2. DC Current Gain

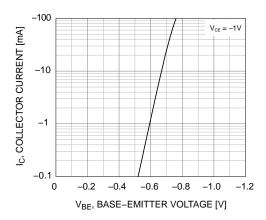


Figure 4. Base-Emitter On Voltage

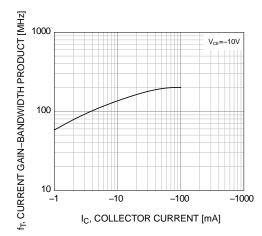
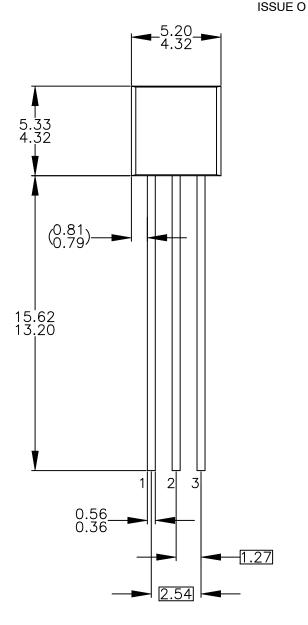
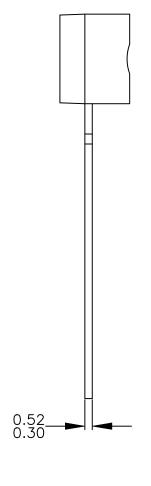


Figure 6. Current Gain Bandwidth Product

# TO-92 3 4.825x4.76 CASE 135AN

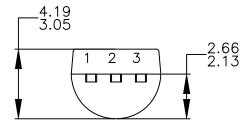
**DATE 31 JUL 2016** 





NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS. A)
- ALL DIMENSIONS ARE IN MILLIMETERS.
  DRAWING CONFORMS TO ASME Y14.5M—2009.



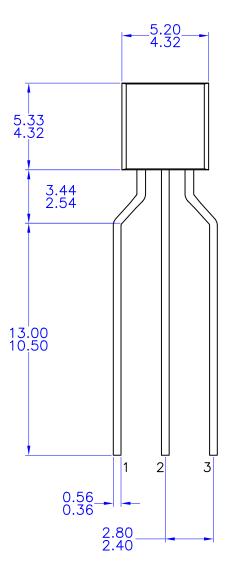
| DOCUMENT NUMBER: | 98AON13880G        | Electronic versions are uncontrolled except when accessed directly from the Document Reposito<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |             |
|------------------|--------------------|--|-------------|
| DESCRIPTION:     | TO-92 3 4.825X4.76 |  | PAGE 1 OF 1 |

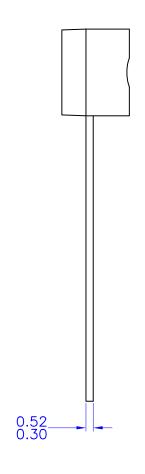
ON Semiconductor and at a trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

### TO-92 3 4.83x4.76 LEADFORMED

CASE 135AR ISSUE O

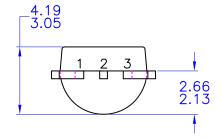
**DATE 30 SEP 2016** 





NOTES: UNLESS OTHERWISE SPECIFIED

- A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DRAWING CONFORMS TO ASME Y14.5M-1994



| DOCUMENT NUMBER: | 98AON13879G                  | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |             |
|------------------|------------------------------|---|-------------|
| DESCRIPTION:     | TO-92 3 4.83X4.76 LEADFORMED |   | PAGE 1 OF 1 |

ON Semiconductor and at a trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

#### ADDITIONAL INFORMATION

**TECHNICAL PUBLICATIONS:** 

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$ 

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales