

**DUAL N-CANNEL ENHANCEMENT MODE MOSFET  
POWERDI®**
**Product Summary**

$V_{(BR)DSS}$	$R_{DS(ON)}$ max	$I_D$ max $T_A = +25^\circ\text{C}$
20V	10.8mΩ @ $V_{GS} = 4.5\text{V}$	10.7A
	14.5mΩ @ $V_{GS} = 2.5\text{V}$	9.3A
	17.0mΩ @ $V_{GS} = 1.8\text{V}$	8.6A

**Description**

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

**Applications**

- Power Management Functions
- Load Switch

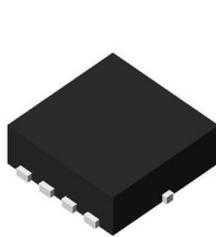
**Features**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- **ESD Protected Up to 2kV**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

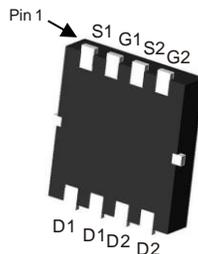
**Mechanical Data**

- Case: POWERDI®3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 **e3**
- Weight: 0.0065 grams (Approximate)

POWERDI®3333-8



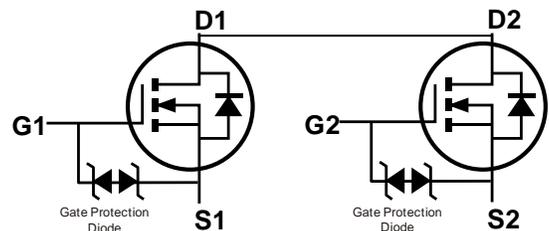
Top View



Bottom View



ESD PROTECTED

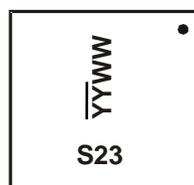


Internal Schematic

**Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN2022UNS-7	POWERDI®3333-8	2000/Tape & Reel
DMN2022UNS-13	POWERDI®3333-8	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

**Marking Information**


S23 = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Last Digit of Year (ex: 15 = 2015)  
 WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±10	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	10.7 8.6	A
	t < 10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	13.9 11.1	A
Maximum Body Diode Forward Current (Note 6)			I <sub>S</sub>	2	A
Pulsed Drain Current (10µs pulse, Duty cycle = 1%)			I <sub>DM</sub>	60	A
Avalanche Current (Note 7) L = 0.1mH			I <sub>AS</sub>	17.1	A
Avalanche Energy (Note 7) L = 0.1mH			E <sub>AS</sub>	14.7	mJ

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Total Power Dissipation (Note 5)			P <sub>D</sub>	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R <sub>θJA</sub>	107	°C/W
	t < 10s			64	
Total Power Dissipation (Note 6)			P <sub>D</sub>	1.9	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		R <sub>θJA</sub>	67	°C/W
	t < 10s			40	
Operating and Storage Temperature Range			T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±10V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.4	—	1	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	9.0	10.8	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4A
		—	9.2	11.2		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 4A
		—	9.8	13.0		V <sub>GS</sub> = 3.1V, I <sub>D</sub> = 4A
		—	10.5	14.5		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 4A
		—	13.9	17.0		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 4A
		—	—	—		—
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1.1	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 5A
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	C <sub>iss</sub>	—	1870	—	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	320	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	160	—	pF	
Gate Resistance	R <sub>g</sub>	—	96	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge	Q <sub>g</sub>	—	20.3	—	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 6.5A
Gate-Source Charge	Q <sub>gs</sub>	—	2.8	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	3.6	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	62	—	ns	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, R <sub>G</sub> = 6Ω, R <sub>L</sub> = 1.0Ω
Turn-On Rise Time	t <sub>r</sub>	—	101	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	596	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	224	—	ns	
Reverse Recovery Time	t <sub>RR</sub>	—	150	—	ns	
Reverse Recovery Charge	Q <sub>RR</sub>	—	135	—	nC	I <sub>F</sub> = 4A, di/dt = 100A/µs

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  - I<sub>AS</sub> and E<sub>AS</sub> rating are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

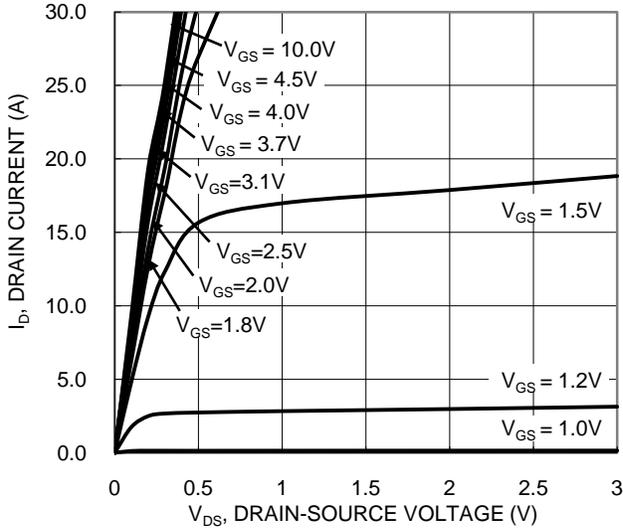


Figure 1. Typical Output Characteristic

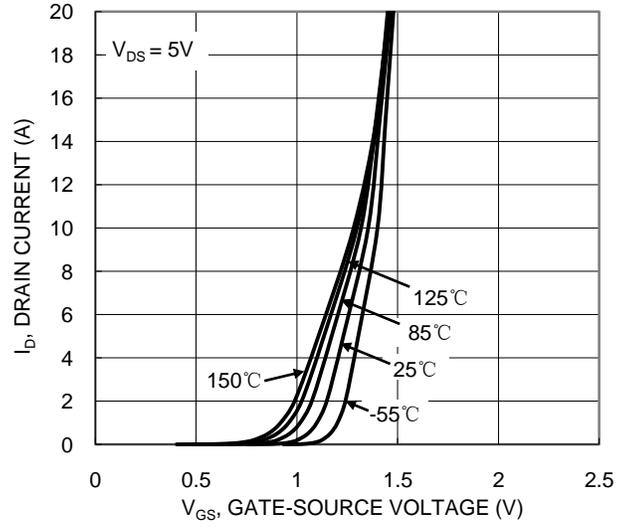


Figure 2. Typical Transfer Characteristic

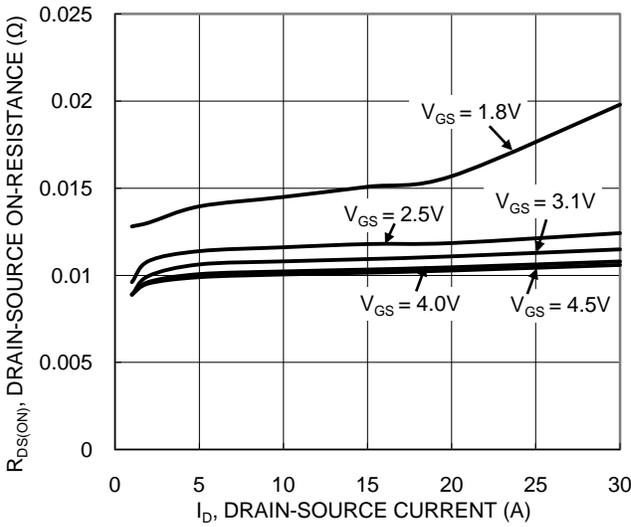


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

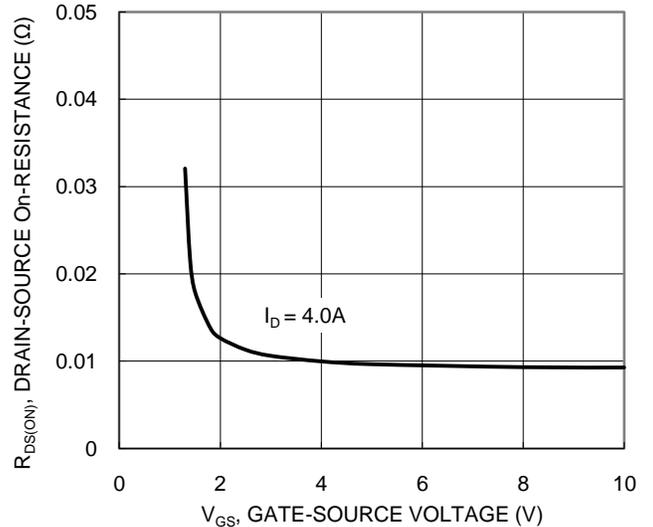


Figure 4. Typical Transfer Characteristic

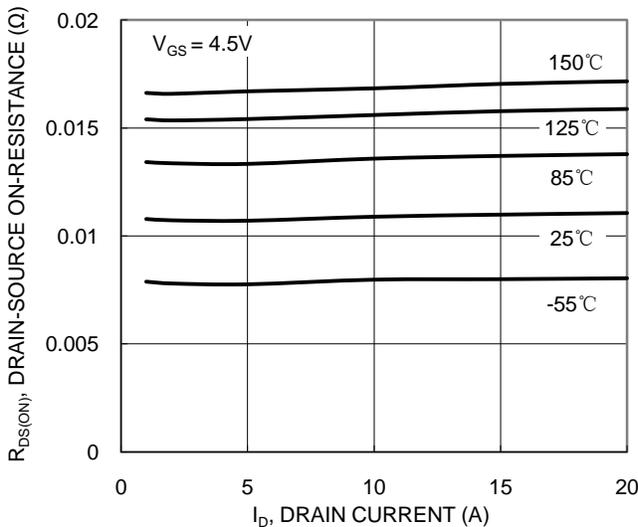


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

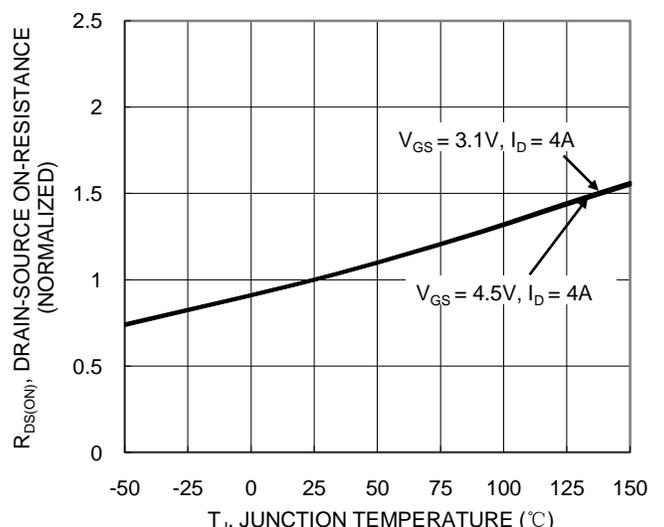
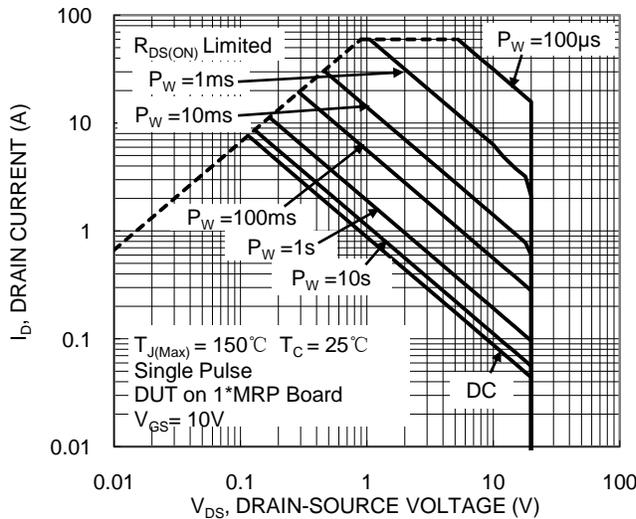
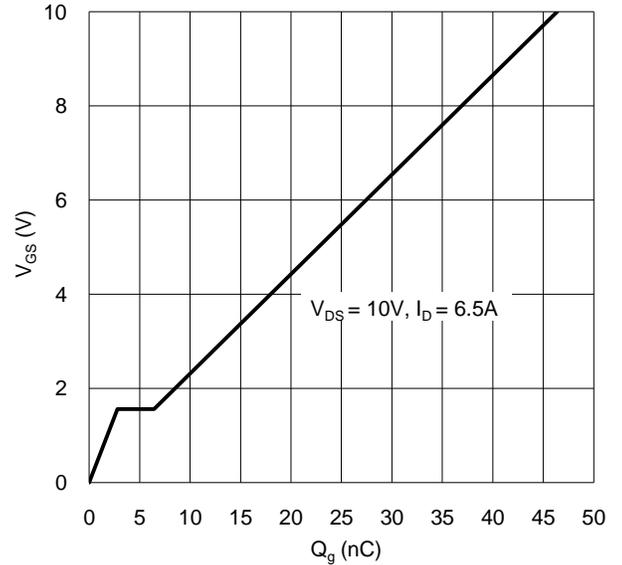
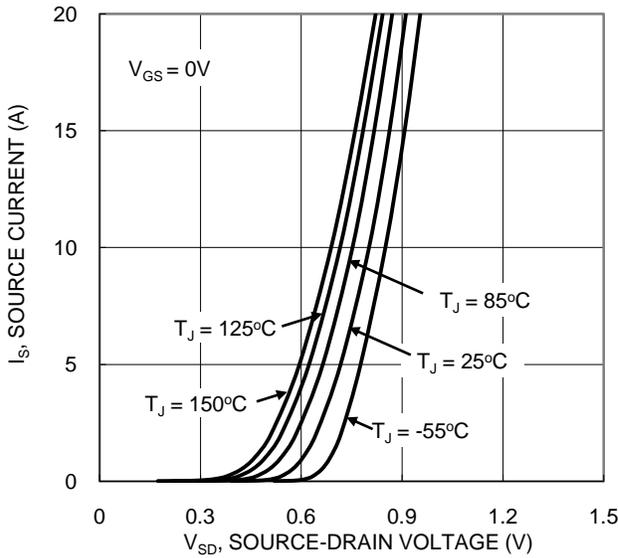
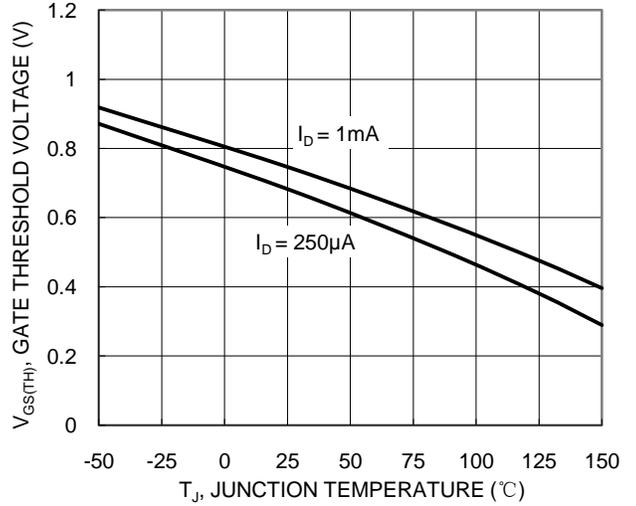
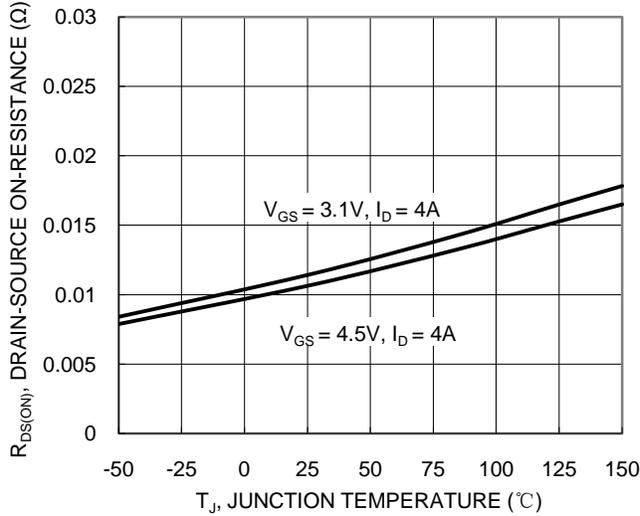


Figure 6. On-Resistance Variation with Junction Temperature



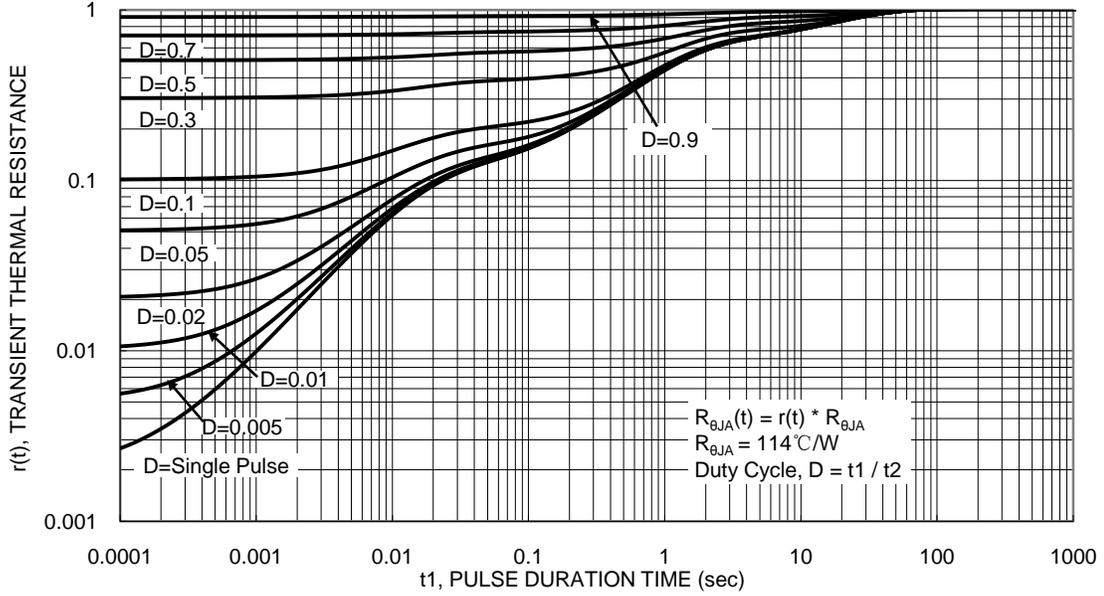
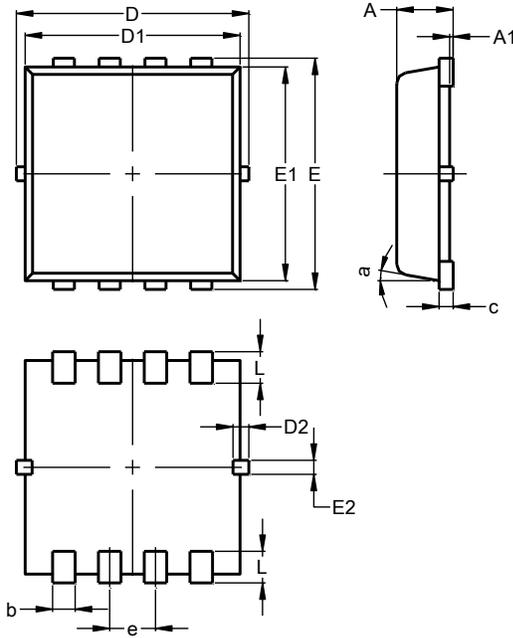


Figure 12. Transient Thermal Resistance

**Package Outline Dimensions**

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

**POWERDI®3333-8 (Type UXB)**



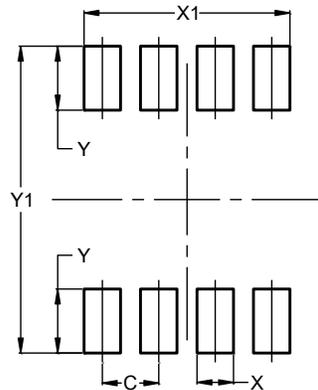
POWERDI®3333-8 (Type UXB)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	--
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	0.10	0.35	0.23
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	0.10	0.30	0.20
e	--	--	0.65
L	0.35	0.55	0.45
a	0°	12°	10°
All Dimensions in mm			

NEW PRODUCT

**Suggested Pad Layout**

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

**POWERDI®3333-8 (Type UXB)**



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	2.370
Y	0.730
Y1	3.500

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