

Customer Advisory (ADV) Number: ADV-2211-001 Issue Date: 28th November 2022

## **CUSTOMER ADVISORY**

## Post Divider (O Counter) setting option change for Trion<sup>®</sup> FPGAs in the Efinity<sup>®</sup> software, effective with patch release v2022.1.226.3.17

Advisory Details	Effective with the Efinity software v2022.1.226.3.17, the Post Divider (O Counter) option for the PLL setting changes as follows:
	• Post Divider (O Counter) = 1 is only allowed if 1 PLL clkout is used.
	• Post Divider (O Counter) = 1 is <b>disabled if more than 1 PLL clkout is used.</b> The Post Divider (O Counter) value must be 2 or greater if more than 1 PLL clkout signal is enabled.
	Existing customer designs that use more than 1 PLL clkout with the Post Divider (0 Counter) = 1 setting will need to update the setting and re-generate the bitstream with the new valid Post Divider (O Counter) setting.
	Customers can refer to the following collateral for details:
	Data sheet:
	• <u>T8 Data Sheet</u>
	• <u>T13 Data Sheet</u>
	• <u>T20 Data Sheet</u>
	• <u>T35 Data Sheet</u>
	• <u>T55 Data Sheet</u>
	• <u>T85 Data Sheet</u>
	• <u>T120 Data Sheet</u>
	Interfaces User Guide:
	<u>Trion Interfaces UG</u>
Parts Affected	All Trion devices and variants shown below:
	• T13/T20F169XX
	• T13/T20F256XX
	• T08/T20Q144XX
	• T55/T85/T120F484XX
	• T55/T85/T120F324XX
	• T55/T85/T120F576XX
	• T20/T35F324XX
	• T20/T35F400XX
	• T20W80XX
	XX = All speed grades
Contact:	For more information, please contact your local Sales or FAE or contact us online at <u>https://www.efinixinc.com/company-contact-us.html</u> .

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