



Product Summary

BV _{DSS}	Rds(on) max	I _D max T _A = +25°C
60V	2Ω @ V _{GS} = 5V	380mA
	2.5Ω @ Vgs = 2.5V	340mA

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor controls
- Power management functions
- Backlighting

N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/guality/product-definitions/

Mechanical Data

Package: SOT23

Package Material: Molded Plastic, "Green" Molding Compound.

- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 3 Weight: 0.008 grams (Approximate)



Ordering Information (Note 4)

Part Number	Paakago	Packing				
Fait Nulliber	Package	Qty.	Carrier			
DMN61D9U-7	SOT23	3000	Tape & Reel			
DMN61D9U-13	SOT23	10000	Tape & Reel			

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Notes:



Marking Information



1AA = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: J = 2022) M = Month (ex: 9 = September)

Date Code Key

Date Code Key												
Year	2015		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	С		J	K	L	М	Ν	0	Р	R	S	Т
					1	1						
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code		0	0		_	6	_	0				-

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic				Symbol	Value	Unit
Drain-Source Voltage				VDSS	60	V
Gate-Source Voltage				Vgss	±20	V
	Steady State	T _A = +25°C T _A = +70°C	7	lo	380 300	mA
Continuous Drain Current (Note 6) VGS = 5.0V	t<5s	T _A = +25°C T _A = +70°C		lø	430 340	mA
Maximum Continuous Body Diode Forward Current (Note 6)				Is	0.4	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 6)				Ідм	1.2	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	370	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	351	°C/W
mermai Resistance, Sunction to Ambient (Note 5)	t<5s	RUJA	292	0/11
Total Power Dissipation (Note 6)		PD	540	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Davi	221	°C/W
merinal Resistance, Sunction to Ambient (Note 0)	t<5s	Reja	197	0/10
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes:

Device mounted on FR-4 PCB, with minimum recommended pad layout.
Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						·
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	IDSS	—	_	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_	_	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	0.5	—	1.0	V	V _{DS} = 10V, I _D = 250µA
			1.2	2.0		$V_{GS} = 5.0V, I_D = 0.05A$
Static Drain-Source On-Resistance	RDS(ON)	—	1.6 2.5	2.5	Ω	Vgs = 2.5V, Id = 0.05A
				3.5		$V_{GS} = 1.8V, I_D = 0.05A$
Forward Transconductance	Y _{fs}	200		_	mS	Vps =10V, Ip = 0.2A
Diode Forward Voltage	Vsd	_	0.75	1.4	V	$V_{GS} = 0V$, Is = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	28.5	—	pF	
Output Capacitance	Coss	_	3.9	-	pF	V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2.5	Ţ	рF	1 = 1.000112
Gate Resistance	Rg	—	65		Ω	f = 1MHz , Vgs = 0V, Vps = 0V
Total Gate Charge	Qg	—	0.4		nC	
Gate-Source Charge	Qgs		0.1		nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q _{gd}	ł	0.1		nC	ID = 250mA
Turn-On Delay Time	tD(ON)	-	2.1	—	ns	
Turn-On Rise Time	tR	-	1.8	—	ns	V _{DD} = 30V, V _{GS} = 10V,
Turn-Off Delay Time	tD(OFF)		14.4	—	ns	$R_{G} = 25\Omega, I_{D} = 200 \text{mA}$
Turn-Off Fall Time	tF	1	8.4	-	ns	Ť

Notes: 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.



DMN61D9U





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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23



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