

SP4208 series 3.0pF, 30A Discrete TVS Diode



Pinout



Cathode polarity for unidirectional only

Description

The SP4208 components integrate low capacitance steering diodes with one or two avalanche breakdown diodes for unidirectional or bidirectional protection, respectively, to protect against ESD and lightning induced surge events. These components can safely absorb up to 30A per IEC 61000-4-5 2^{nd} edition (t_p =8/20µs) without performance degradation and a minimum ±30kV ESD per IEC 61000-4-2 International Standard. The low loading capacitance and high surge capability make it ideal for protecting telecommunication ports such as Ethernet and other high speed data interfaces.

Features

- ESD, IEC 61000-4-2, ±30kV contact, ±30kV air
- EFT, IEC 61000-4-4, 40A (5/50ns)
- Lightning, IEC 61000-4-5 2nd edition, 30A (t_p=8/20µs)
- Low capacitance of 3.0pF (@V_R=0V)
- Low leakage current

 Unidirectional and bidirectional configuration

F RoHS PO GREEN

- Small SOD323 package fits 0805 footprints
- AEC-Q101 qualified
- Halogen free, lead free and RoHS compliant
- Moisture Sensitivity Level(MSL -1)

Functional Block Diagram



Applications

- •10/100/1000 2.5 and 5 Gigabit Ethernet
- Medical Equipment
- Computers and PeripheralsInstrumentation

Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

Absolute Maximum Ratings

| Symbol | Parameter | Value | Units |
|-------------------|---|------------|-------|
| I _{pp} | Peak Current (t _p =8/20µs) | 30 | A |
| P _{Pk} | Peak Pulse Power (t _p =8/20µs) | 750 | W |
| T _{OP} | Operating Temperature | -40 to 125 | °C |
| T _{STOR} | Storage Temperature | -55 to 150 | °C |

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Electrical Characteristics (Top=25°C)

| | UP | - | | | | |
|------------------------------------|------------------|--|-----|------|-----|-------|
| Parameter | Symbol | Test Conditions | Min | Тур | Мах | Units |
| Breakdown Voltage | V _{BD} | I _R =1mA | - | 9.5 | - | V |
| Reverse Standoff Voltage | V _{RWM} | I _R ≤1µA | - | - | 8.0 | V |
| Leakage Current | I | V _R =8.0V | - | 0.02 | 0.5 | μA |
| Clamp Voltage ¹ | V _c | I _{pp} =1A, t _p =8/20µs, Fwd | - | 11.5 | - | V |
| | | I_{pp} =17A, t_p =8/20µs, Fwd | - | 19 | - | V |
| | | I_{pp} =30A, t_p =8/20µs, Fwd | - | 25 | - | V |
| Dynamic Resistance ² | R _{DYN} | TLP, tP=100ns, I/O to GND | - | 0.37 | - | Ω |
| ESD Withstand Voltage ¹ | V _{ESD} | IEC 61000-4-2 (Contact Discharge) | ±30 | - | - | kV |
| | | IEC 61000-4-2 (Air Discharge) | ±30 | - | - | kV |
| Diode Capacitance ¹ | C _D | Reverse Bias=0V, f=1MHz | - | 3.0 | - | pF |

Note:

Parameter is guaranteed by design and/or component characterization.
Transmission Line Pulse (TLP) with 100ns width, 2ns rise time, and average window t1=70ns to t2= 90ns

8/20µs Pulse Waveform



Positive Transmission Line Pulsing (TLP) Plot 45







Capacitance vs. Reverse Bias



IEC 61000-4-2 +8 kV Contact ESD Clamping Voltage



Clamping Voltage vs I_{PP} 8/20µS waveshape 30 25 Clamp Voltage (V_c) 20 15 10 5 0 0 5 10 15 20 25 30 Peak Pulse Current-IPP (A)

IEC 61000-4-2 -8 kV Contact ESD Clamping Voltage





Soldering Parameters

| Reflow Cond | lition | Pb – Free assembly | |
|--|--|-------------------------|--|
| | - Temperature Min (T _{s(min)}) | 150°C | |
| Pre Heat | - Temperature Max (T _{s(max)}) | 200°C | |
| | -Time (min to max) (t _s) | 60 – 180 secs | |
| Average ram | 3°C/second max | | |
| T _{S(max)} to T _L - Ramp-up Rate | | 3°C/second max | |
| Reflow | - Temperature (T_L) (Liquidus) | 217°C | |
| | - Temperature (t _L) | 60 – 150 seconds | |
| Peak Temperature (T _P) | | 260 ^{+0/-5} °C | |
| Time within 5°C of actual peak Temperature (t _p) | | 20 – 40 seconds | |
| Ramp-down Rate | | 6°C/second max | |
| Time 25°C to peak Temperature (T _p) | | 8 minutes Max. | |
| Do not exce | 260°C | | |



Product Characteristics

| Lead Plating | Matte Tin |
|--------------------|--|
| Lead Material | Iron Alloy |
| Lead Coplanarity | 0.004 inches(0.102mm) |
| Substrate material | Silicon |
| Body Material | Molded Compound |
| Flammability | UL Recognized compound meeting flammability rating V-0 |

Notes :

All dimensions are in millimeters
Dimensions include solder plating.
Dimensions are exclusive of mold flash & metal burr.

Part Numbering System



Ordering Information

| Part Number | Package | Marking | Min. Order Qty. |
|----------------|---------|---------|-----------------|
| SP4208-01FTG | SOD323 | Axx | 3000 |
| SP4208-01FTG-C | SOD323 | Bxx | 3000 |

Part Marking System



A= Part Code X= Assembly Site Y= Date Code





B= Part Code X= Assembly Site Y= Date Code



Package Dimensions -SOD323





A2

| Symbol | Millimeters | | | |
|--------|-------------|------|------|--|
| | Min | Nor | Max | |
| Α | 0.80 | 1.00 | 1.14 | |
| A1 | 0 | - | 0.10 | |
| A2 | 0.80 | 0.95 | 1.04 | |
| b | 0.25 | 0.30 | 0.35 | |
| С | 0.08 | - | 0.15 | |
| D | 1.15 | 1.30 | 1.45 | |
| E | 1.60 | 1.75 | 1.90 | |
| E1 | 2.44 | 2.55 | 2.75 | |
| L | 0.475 REF | | | |
| L1 | 0.22 | 0.35 | 0.45 | |
| L2 | 0.20 BSC | | | |

Recommended soldering pad layout

Embossed Carrier Tape & Reel Specification - SOD323

2



Symbol Millimeters A0 1.46+/-0.10 **B0** 2.90+/-0.10 8.0+0.3/-0.10 w D0 1.50+0.10 D1 0.45min/1.15max E1 1.75+/-0.10 E2 _ F 3.50+/-0.10 P0 4.00+/-0.10 Ρ 4.00+/-0.10 **P1** 2.00+/-0.05 К0 1.25+/-0.10 т 0.254+/-0.02

8mm TAPE AND REEL



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