

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
40V	11.5mΩ @ V <sub>GS</sub> = 10V	11.6A
	18mΩ @ V <sub>GS</sub> = 4.5V	9.3A

## Description

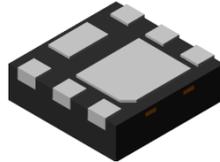
This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications:

- Power Management Functions
- DC-DC Converters
- Backlighting

U-DFN2020-6 (SWP) (Type F)



Top View



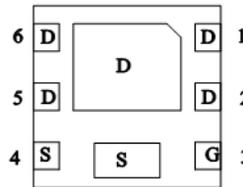
Bottom View

## Features

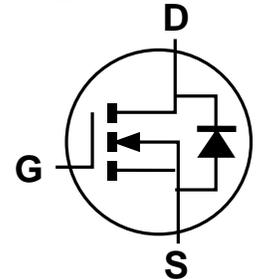
- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching, Test in Production – Ensures More Reliable and Robust End Application
- Low R<sub>DS(ON)</sub> – Ensures On State Losses Are Minimized
- 0.6mm Profile – Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **An Automotive-Compliant Part is Available Under Separate Datasheet ([DMTH4008LDFWQ](#))**

## Mechanical Data

- Case: U-DFN2020-6 (SWP) (Type F)
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.007 grams (Approximate)



Pin Out  
Bottom View



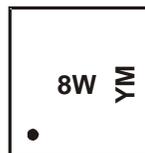
Internal Schematic

## Ordering Information (Note 4)

Part Number	Case	Quantity Per Reel
DMTH4008LDFW-7	U-DFN2020-6 (SWP) (Type F)	3,000
DMTH4008LDFW-13	U-DFN2020-6 (SWP) (Type F)	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, “Green” and Lead-free.
  3. Halogen- and Antimony-free “Green” products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



8W = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: F = 2018)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2017	2018	2019	2020	2021	2022	2023	2024
Code	E	F	G	H	I	J	K	L

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	40	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current (Note 7) $V_{GS} = 10\text{V}$	$I_D$	$T_A = +25^\circ\text{C}$ 11.6	A
		$T_A = +100^\circ\text{C}$ 8.2	
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)	$I_{DM}$	80	A
Continuous Source-Drain Diode Current (Note 7)	$I_S$	2.55	A
Pulsed Source-Drain Diode Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)	$I_{SM}$	80	A
Avalanche Current, $L = 0.3\text{mH}$ (Note 8)	$I_{AS}$	14.7	A
Avalanche Energy, $L = 0.3\text{mH}$ (Note 8)	$E_{AS}$	32.4	mJ

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_D$	0.99	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	153	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)	$P_D$	2.35	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	64.5	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case (Note 7)	$R_{\theta JC}$	14.8	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +175	$^\circ\text{C}$

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 9)						
Drain-Source Breakdown Voltage	$BV_{DSS}$	40	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS</b> (Note 9)						
Gate Threshold Voltage	$V_{GS(TH)}$	1	1.7	3	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	9.1	11.5	m $\Omega$	$V_{GS} = 10\text{V}, I_D = 10\text{A}$
			12.9	18		$V_{GS} = 4.5\text{V}, I_D = 8.5\text{A}$
Diode Forward Voltage	$V_{SD}$	—	0.8	1.0	V	$V_{GS} = 0\text{V}, I_S = 10\text{A}$
<b>DYNAMIC CHARACTERISTICS</b> (Note 10)						
Input Capacitance	$C_{iss}$	—	1030	—	pF	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Output Capacitance	$C_{oss}$	—	324	—		
Reverse Transfer Capacitance	$C_{rss}$	—	27	—		
Gate Resistance	$R_g$	—	1.82	—	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge ( $V_{GS} = 4.5\text{V}$ )	$Q_g$	—	6.8	—	nC	$V_{DD} = 20\text{V}, I_D = 10\text{A}$
Total Gate Charge ( $V_{GS} = 10\text{V}$ )	$Q_g$	—	14.2	—		
Gate-Source Charge	$Q_{gs}$	—	2.0	—		
Gate-Drain Charge	$Q_{gd}$	—	2.7	—		
Turn-On Delay Time	$t_{D(ON)}$	—	3.1	—	ns	$V_{DD} = 20\text{V}, V_{GS} = 10\text{V}, R_g = 6\Omega, I_D = 10\text{A}$
Turn-On Rise Time	$t_R$	—	3.1	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	14.2	—		
Turn-Off Fall Time	$t_F$	—	5.8	—		
Reverse Recovery Time	$t_{RR}$	—	19.6	—	ns	$I_F = 10\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Reverse Recovery Charge	$Q_{RR}$	—	8.2	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
  - Thermal resistance from junction to soldering point (on the exposed drain pad).
  - $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25^\circ\text{C}$ .
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

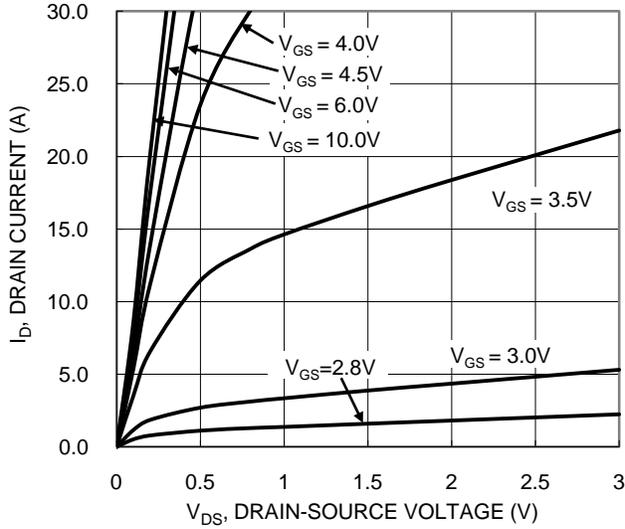


Figure 1. Typical Output Characteristic

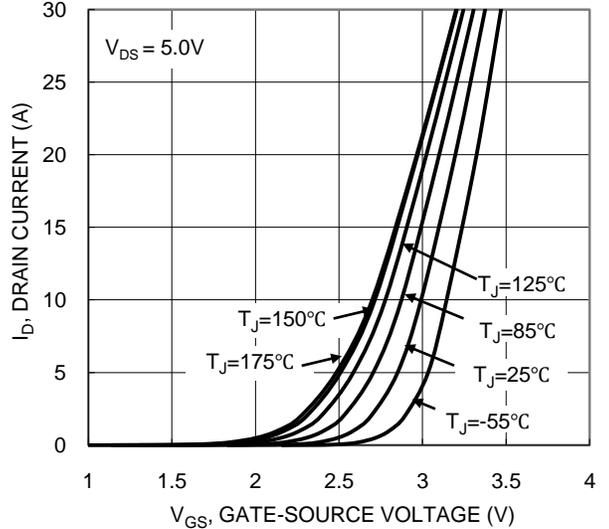


Figure 2. Typical Transfer Characteristic

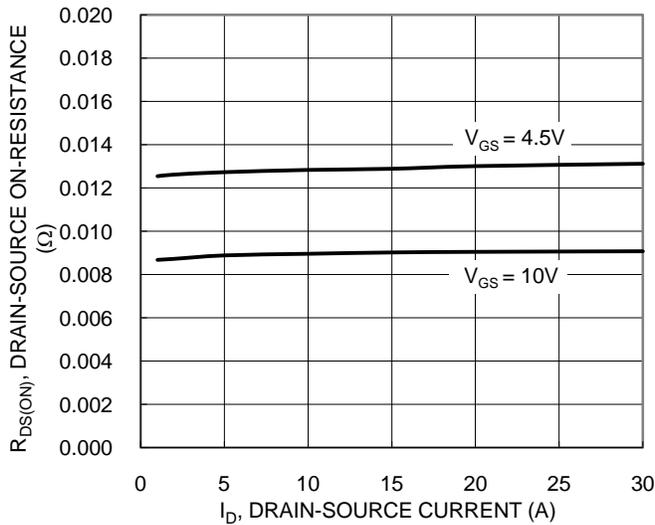


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

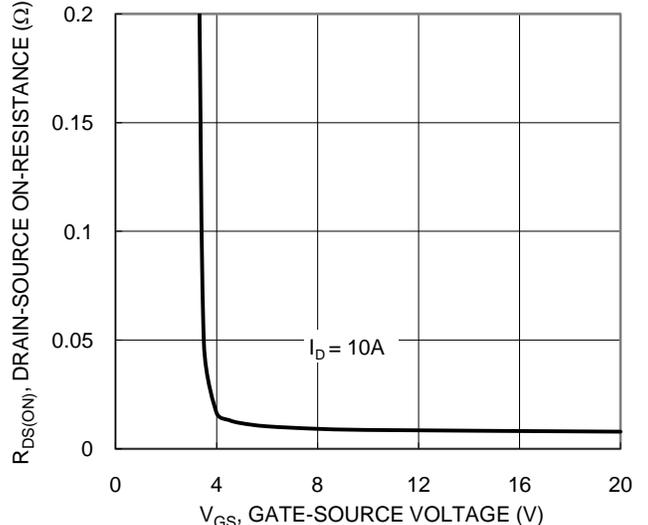


Figure 4. Typical Transfer Characteristic

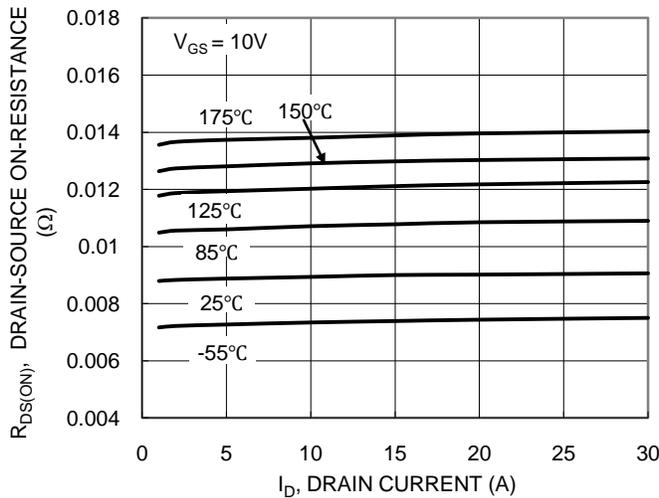


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

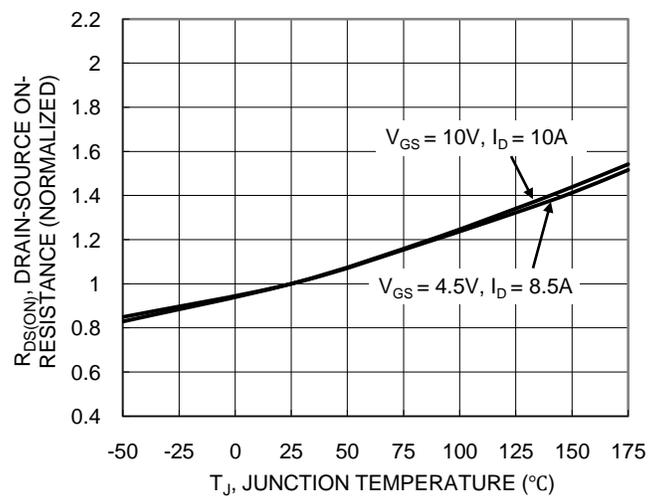


Figure 6. On-Resistance Variation with Temperature

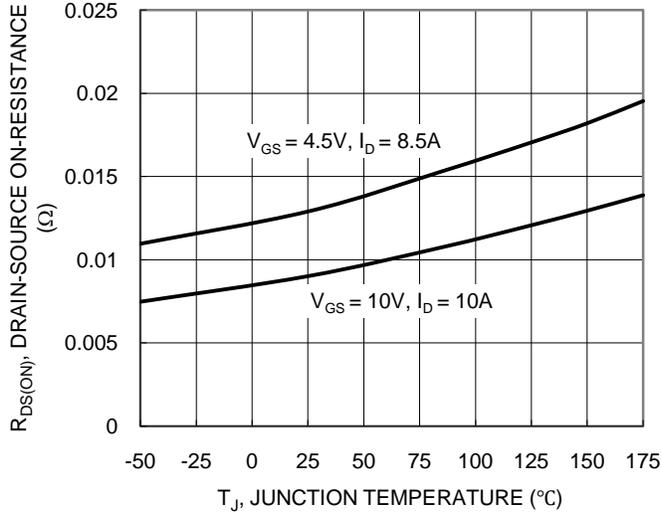


Figure 7. On-Resistance Variation with Temperature

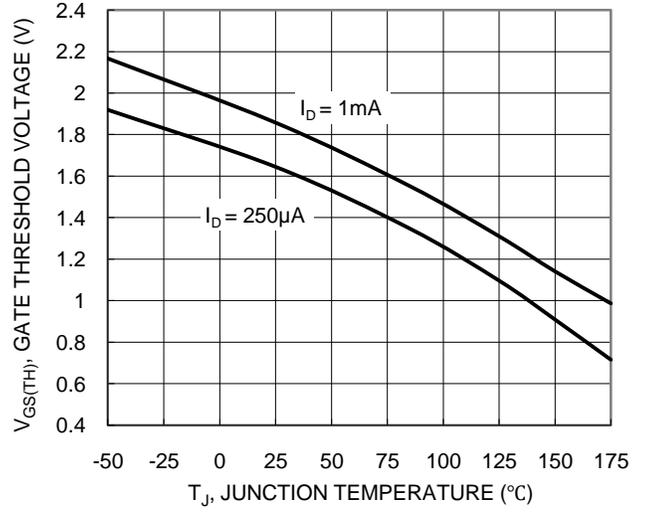


Figure 8. Gate Threshold Variation vs. Junction Temperature

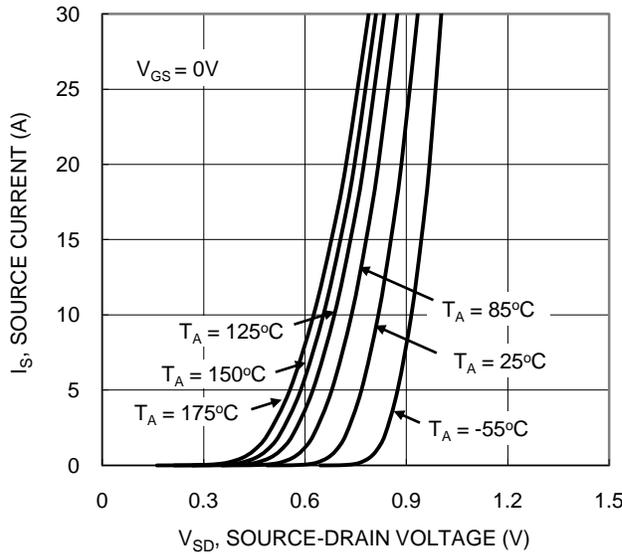


Figure 9. Diode Forward Voltage vs. Current

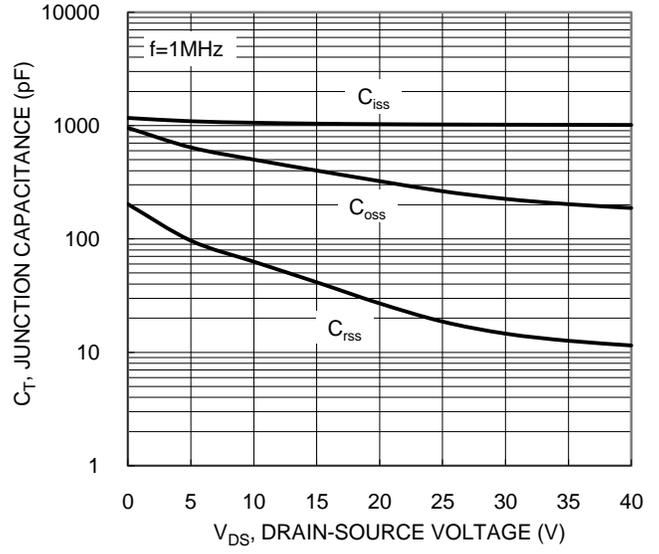


Figure 10. Typical Junction Capacitance

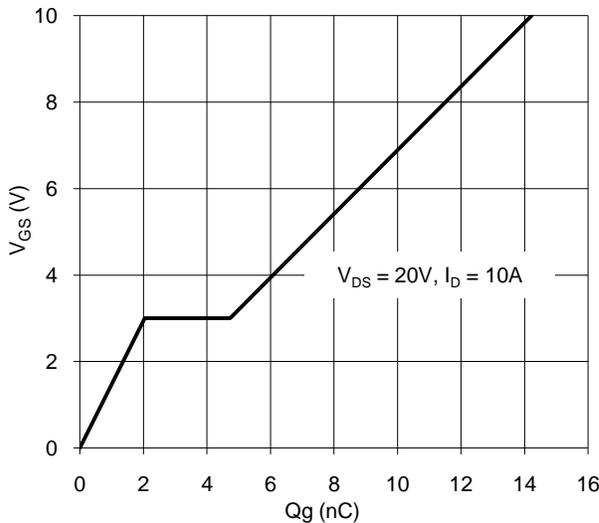


Figure 11. Gate Charge

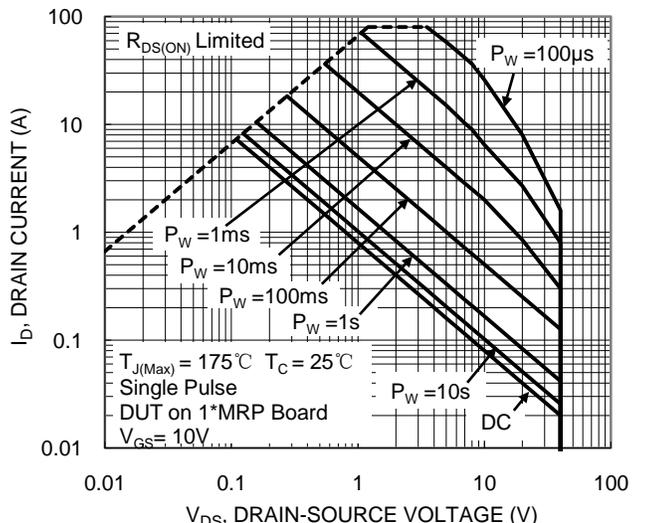


Figure 12. SOA, Safe Operation Area

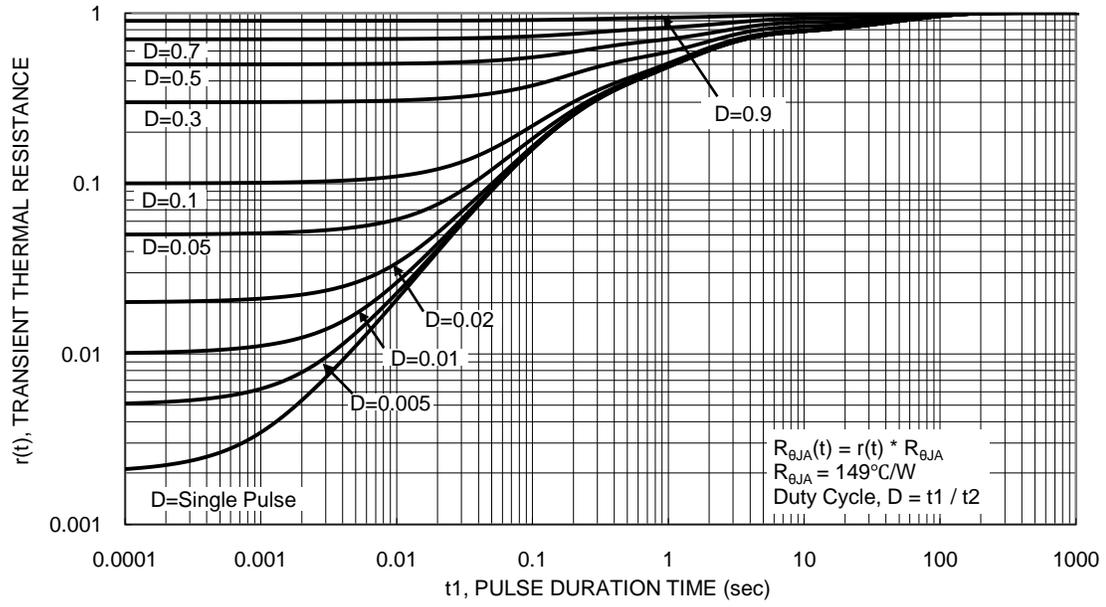
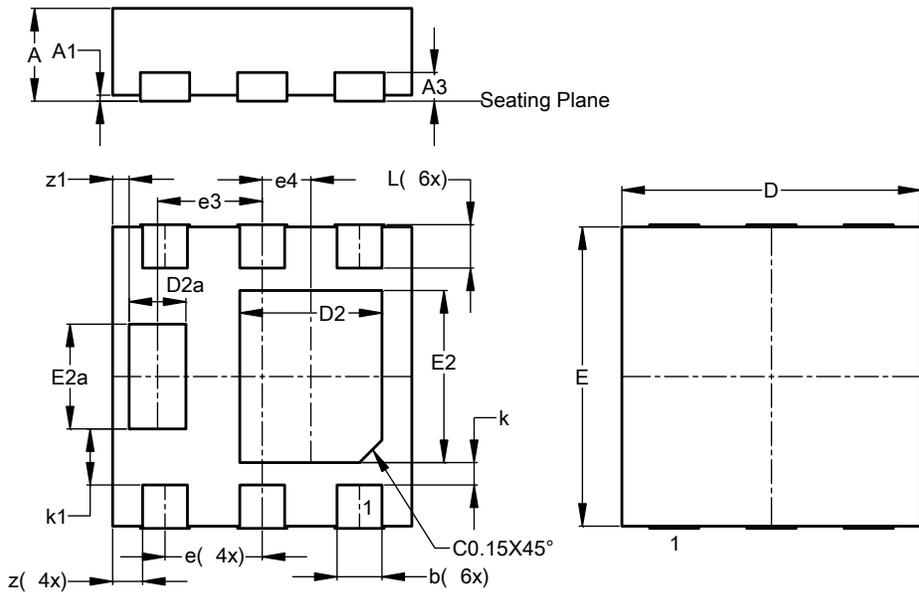


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (SWP) (Type F)

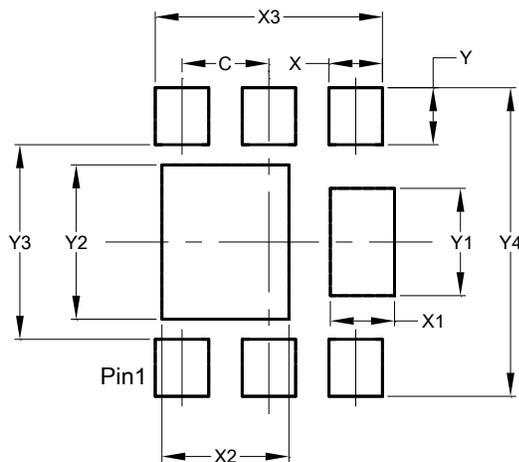


U-DFN2020-6 (SWP) (Type F)			
Dim	Min	Max	Typ
A	0.59	0.65	0.62
A1	0.00	0.05	0.03
A3	-	-	0.192
b	0.28	0.38	0.33
D	1.95	2.05	2.00
D2	0.87	1.07	0.97
D2a	0.35	0.45	0.40
E	1.95	2.05	2.00
E2	1.07	1.27	1.17
E2a	0.67	0.77	0.72
e	0.65 BSC		
e3	0.70 BSC		
e4	0.325 BSC		
k	--	--	0.15
k1	--	--	0.375
L	0.225	0.355	0.305
z	--	--	0.20
z1	--	--	0.11
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (SWP) (Type F)



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300

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