



N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _C = +25°C
	5mΩ @ V _{GS} = 10V	80A
24V	6.5mΩ @ V _{GS} = 4.5V	70A
	10mΩ @ V _{GS} = 2.5V	55A

Features and Benefits

- Low R_{DS(ON)} Minimizes On-State Losses
- Excellent Q_{qd} x R_{DS(ON)} Product (FOM)
- Advanced Technology for DC-DC Converters
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- 100% Unclamped Inductive Switching Ensures More Reliability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

PowerDI5060-8

- Backlighting
- Power Management Functions
- DC-DC Converters

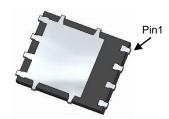
Mechanical Data

- Case: PowerDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 <a>®
- Weight: 0.097 grams (Approximate)

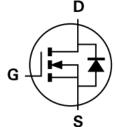




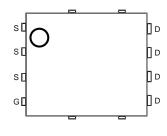
Notes:



Bottom View



Internal Schematic



Top View Pin Configuration

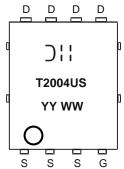
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT2004UPS-13	PowerDI5060-8	2,500/Tape & Reel

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



);; = Manufacturer's Marking
T2004US = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 16 = 2016)
WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	24	V
Gate-Source Voltage			V_{GSS}	±12	V
Continuous Drain Current (Note 7) $V_{GS} = 10V$ Steady $T_C = +25^{\circ}C$ State $T_C = +70^{\circ}C$			Ι _D	80 65	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	160	Α		
Continuous Source-Drain Diode Current (Note 6)			Is	2	Α
Avalanche Current (Note 8) L = 0.1mH			I _{AS}	26	Α
Avalanche Energy (Note 8) L = 0.1mH			E _{AS}	36	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ heta JA}$	88	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	3.0	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ heta JA}$	42	°C/W
Thermal Resistance, Junction to Case (Note 7)		$R_{\theta JC}$	2.5	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

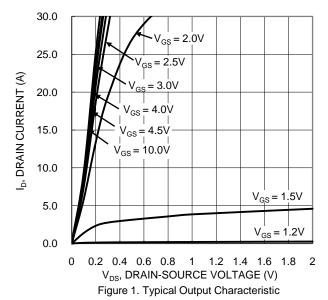
Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

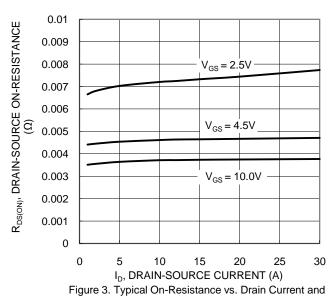
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	24	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current (T _J = +25°C)	I _{DSS}	_	_	1	μΑ	V _{DS} = 20V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 10V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(TH)}	0.55	_	1.45	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
		_	3.7	5.0	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance	RDS(ON)	_	4.5	6.5		$V_{GS} = 4.5V, I_D = 20A$	
		_	7.5	10.0		$V_{GS} = 2.5V, I_D = 20A$	
Diode Forward Voltage	V _{SD}	_	0.65	1.0	V	V _{GS} = 0V, I _S = 2A	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C _{iss}	_	1683	_		$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	
Output Capacitance	Coss	_	581	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	559	_			
Gate Resistance	R _G	_	1.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	29.6	_			
Total Gate Charge (V _{GS} = 10V)	Qq	_	53.7	_			
Gate-Source Charge	Q_{gs}	_	4.2	_	nC	$V_{DD} = 15V, I_D = 9A$	
Gate-Drain Charge	Q _{qd}	_	13.4	_			
Turn-On Delay Time	t _{D(ON)}	_	3.9	_			
Turn-On Rise Time	t _R	_	9.6	_		$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	30.8	_	ns	$R_G = 3\Omega$, $I_D = 9A$	
Turn-Off Fall Time	t _F	_	38.6	_			
Reverse Recovery Time	t _{RR}	_	11.2	_	ns	1. 1.54 11/11 1004/	
Reverse Recovery Charge	Q _{RR}	_	22.9	_	nC	I _F = 1.5A, di/dt = 100A/μs	

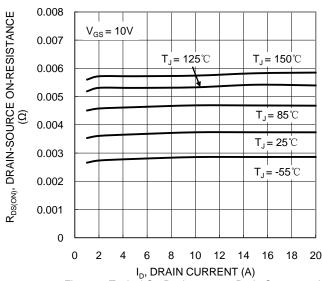
Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 7. Thermal resistance from junction to soldering point (on the exposed drain pad).
 8. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
 9. Short duration pulse test used to minimize self-heating effect.
 10. Guaranteed by design. Not subject to product testing.



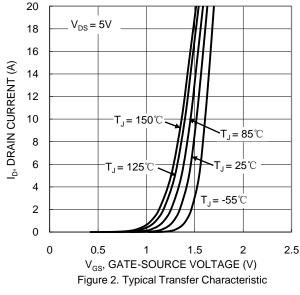


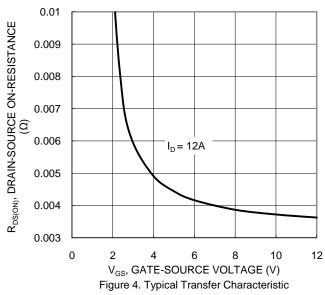




Gate Voltage

Figure 5. Typical On-Resistance vs. Drain Current and Temperature





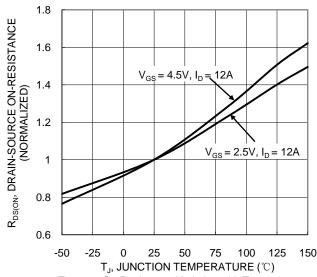


Figure 6. On-Resistance Variation with Temperature





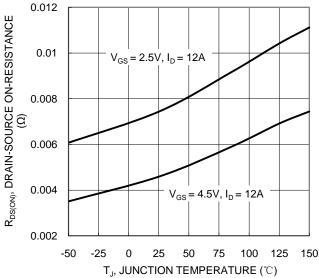
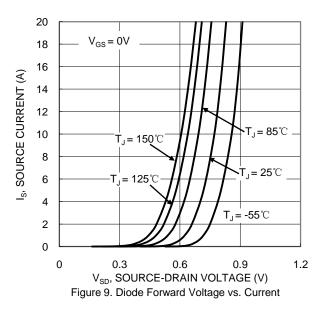


Figure 7. On-Resistance Variation with Temperature



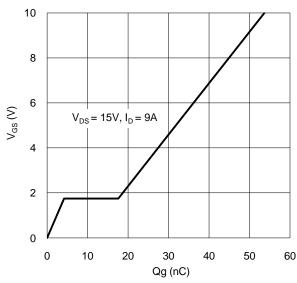


Figure 11. Gate Charge

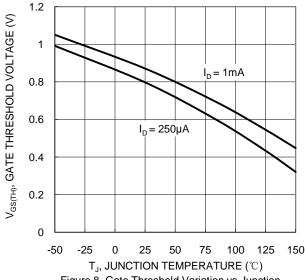
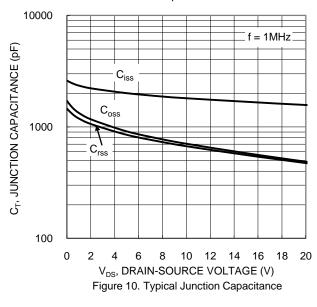
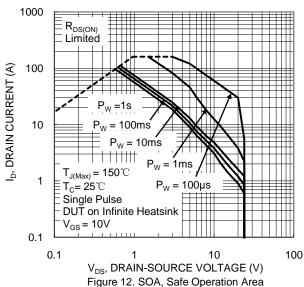


Figure 8. Gate Threshold Variation vs. Junction Temperature







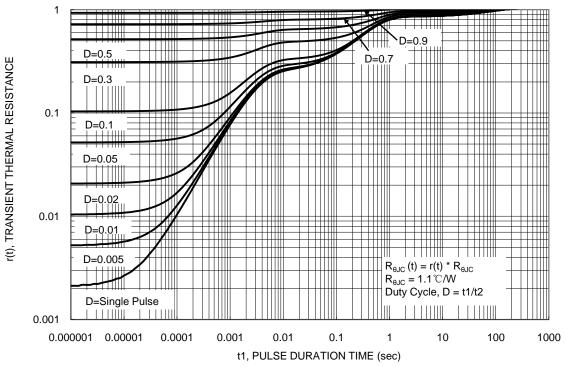


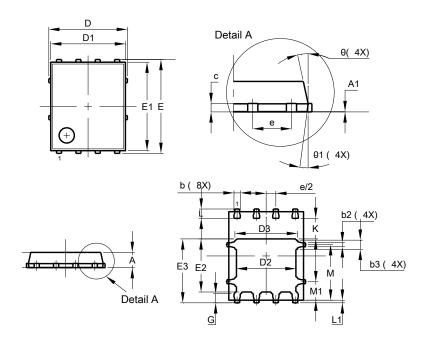
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

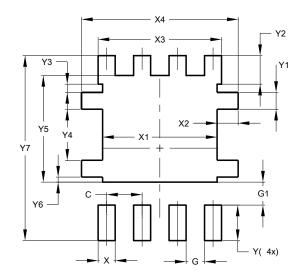


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05			
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D	;	5.15 BSC	;		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	(6.15 BSC	;		
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51				
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
М	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12º	11º		
Θ1	6º	8º	7º		
All	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
Х	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Y	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			



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