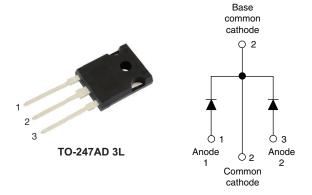


Ultrafast Rectifier, 2 x 30 A FRED Pt®



PRODUCT SUMMARY					
Package	TO-247AD 3L				
I _{F(AV)}	2 x 30 A				
V_{R}	600 V				
V _F at I _F	1.75 V				
t _{rr} typ.	26 ns				
T _J max.	175 °C				
Diode variation	Common cathode				

FEATURES

- · Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Designed and qualified according to commercial qualification







ROHS COMPLIANT HALOGEN FREE

DESCRIPTIONS / APPLICATIONS

VS-CPU60... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Repetitive peak reverse voltage	V _{RRM}		600	V			
Average rectified forward current per leg	I _{F(AV)}	T _C = 131 °C	30	Δ.			
Non-repetitive peak surge current per leg	I _{FSM}	$T_C = 25$ °C, $t_p = 8.3$ ms half sine wave	250	A			
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C			

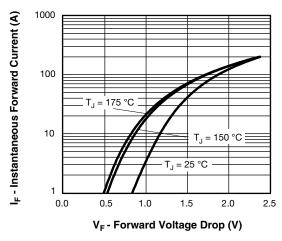
ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	i	.,		
Forward voltage	V _F	I _F = 30 A	-	1.4	1.75	1.75 1.4		
Torward voltage		I _F = 30 A, T _J = 150 °C	-	1.1	1.4			
Deverage leakage augrent		$V_R = V_R$ rated	-	0.02	30			
Reverse leakage current	I _R	T _J = 150 °C, V _R = V _R rated	-	30	200	μΑ		
Junction capacitance	C _T	V _R = 600 V	-	20	-	pF		



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A}, dI_F/dt = 1.0 \text{ A}$	$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		26	-			
Reverse recovery time	t _{rr}	T _J = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	42	-	ns		
		T _J = 125 °C		-	100	-			
Pook recovery ourrent	I _{RRM}	T _J = 25 °C		-	5	-	Α		
Peak recovery current		T _J = 125 °C		-	10	-			
Reverse recovery charge	0	T _J = 25 °C		-	125	-	nC		
	Q _{rr}	T _J = 125 °C		-	580	-	IIC		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	0.7	1		
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W	
Thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-		
Maint			-	6.0	-	g	
Weight			-	0.21	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style TO-247AD 3L	CPU6006L				





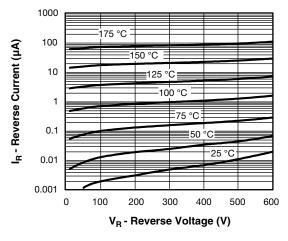


Fig. 1 - Typical Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

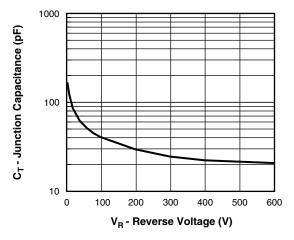


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

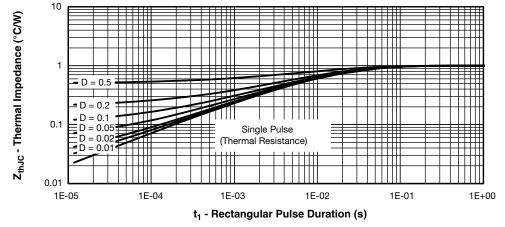


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics



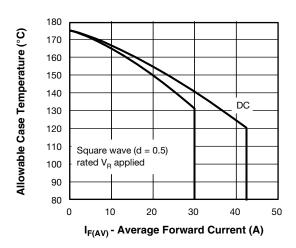


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

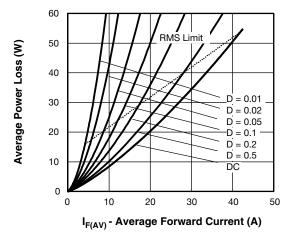


Fig. 6 - Forward Power Loss Characteristics

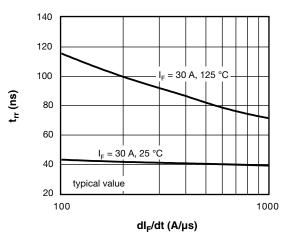


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

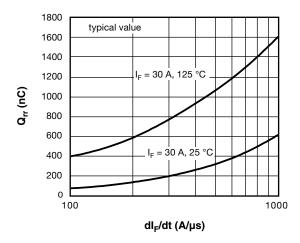
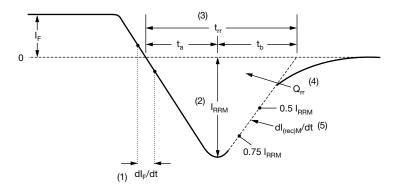


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at } V_{R1} = \text{Rated } V_R \\ \end{array}$





- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RBM} and 0.50 I_{RBM} extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

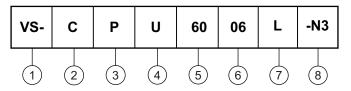
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Circuit configuration:

C = common cathode

- **3** P = TO-247
- 4 U = ultrafast recovery time
- 5 Current code (60 = 2 x 30 A)
- Voltage code (06 = 600 V)
- 7 L = long lead
- 8 Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

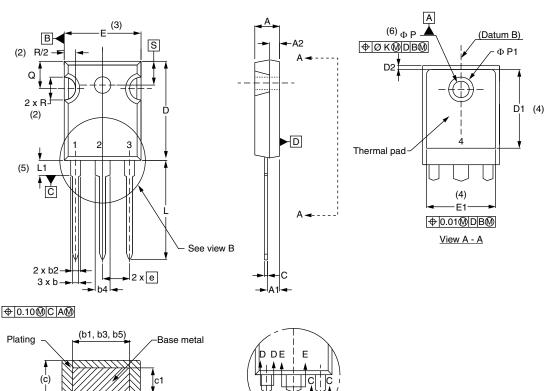
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-CPU6006L-N3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626			
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007			



TO-247AD 3L

DIMENSIONS in millimeters and inches



View B

	MILLIMETERS INCHES					
SYMBOL	IVIILLIIV	IETEKS	INC	NOTES		
011111202	MIN.	MAX.	MIN.	MAX.		
Α	4.65	5.31	0.183	0.209		
A1	2.21	2.59	0.087	0.102		
A2	1.50	2.49	0.059	0.098		
b	0.99	1.40	0.039	0.055		
b1	0.99	1.35	0.039	0.053		
b2	1.65	2.39	0.065	0.094		
b3	1.65	2.34	0.065	0.092		
b4	2.59	3.43	0.102	0.135		
b5	2.59	3.38	0.102	0.133		
С	0.38	0.89	0.015	0.035		
c1	0.38	0.84	0.015	0.033		
D	19.71	20.70	0.776	0.815	3	
D1	13.08	-	0.515	-	4	

Section C - C, D - D, E - E

SYMBOL	MILLIMETERS		INC	INCHES		
OTIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D2	0.51	1.30	0.020	0.051		
E	15.29	15.87	0.602	0.625	3	
E1	13.46	-	0.53	-		
е	5.46	BSC	0.215	BSC		
ØΚ	0.2	0.254		0.010		
L	19.81	20.32	0.780	0.800		
L1	3.71	4.29	0.146	0.169		
ØΡ	3.56	3.66	0.14	0.144		
Ø P1	-	6.98	-	0.275		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	0.178	0.216		
S	5.51 BSC		0.217	BSC		

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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