



AM4967

ADJUSTABLE-SPEED SINGLE-PHASE FULL-WAVE PRE-DRIVER FOR MOTOR

Description

The DIODESTM AM4967 is a single-coil, single-phase motor pre-driver designed by bipolar process. Its rotation speed can be controlled through an external PWM. This IC requires few external components and has the merit of high efficiency which makes it suitable for use in great air volume, large current and high-voltage applications.

The AM4967 is available in SSOP-16 package.

Pin Assignments

GS Package (SSOP-16) OUT2 16 OUT1 OUT2B 15 OUT1B 2 VCC 3 14 GND CS 4 13 VREF VMIN 5 12 LS 11 **VPWM** СТ 6 COSC 7 10 HIN+ FG/RD 9 HIN-

Figure 1. Pin Assignment of AM4967

Features

- Single-phase Full-wave Pre-drive
- Rotation Speed Control via External PWM
- Maximum Operating Current Limit
- Linear Start Function for Reducing Large Current Impulse at the Moment of Turn-on
- Minimum Rotation Speed Setting
- Lock Protection and Auto Start Function
- Rotation Speed Indication
- Overtemperature Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Applications

- CPU and OA (Office Automation) Cooler Fan
- Brushless DC Motor



Figure 2. Package Type of AM4967

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Number	Pin Name	Function					
1	OUT2	Second channel P-type driver output. Open-drain output, must be connected					
, 	0012	with pull-up resistor to drive external power devices in operation					
2	OUT2B	Second channel N-type driver output. Push-pull output to drive external					
		power devices					
3	VCC	Power supply					
4	cs	Current limiter indicator. Fixed to 0.2V internally. Maximum allowable current is decided by external sample resistance. When the voltage of this pin exceeds 0.2V, the output will be shut down by current limit. If this function is not used, connect this pin to ground directly					
		Minimum speed setting terminal. Connect it to VREF when disused. In case it					
5	VMIN	is tied to other power supply for controlling, an isolated resistor is necessary					
6	VPWM	Speed control terminal. It is full speed mode when connected to ground. If the speed is controlled by PWM mode, the frequency of control signal is 20kHz to 100kHz. Moreover, a low pass network is necessary to transfer PWM to DC level					
_	2000	PWM basic oscillation frequency generator when connected with a capacitor					
7	cosc	of 220pF. 30kHz frequency is available.					
8	FG/RD	"FG" means pin 8 is an output pin of FG signal which is used for detecting rotation speed. "RD" means pin 8 is an output pin of RD signal, and is used for detecting fan rotate-stop state. "FG" and "RD" can be selected according to the requirement. This pin need to be connected with pull-up resistor when used. A new pulse will be generated once input signal changed. Keep this pin					
		open when it is not to be used Hall sensor negative input. To prevent noise, this pin is recommended to be					
9	HIN-	placed as close as possible to Hall circuit. If necessary, a capacitor may be added between HIN+ and HIN- to reduce the influence caused by noise					
		Hall sensor positive input. To avoid noise, this pin is recommended to be					
10	HIN+	placed as close as possible to Hall circuit					
11	СТ	Lock protection and auto start frequency generator. It is connected with an external 0.47µF capacitor which decides lock-rotate time and start time. If lock-rotate protection function is disused, please connect this pin to GND					
12	LS	Linear start terminal. This pin is connected to VREF externally via a capacitor of 0.47µF to 1µF which enables fan start steadily. Start time is dependent to the capacitance. If linear start function is not used, connect this pin to ground					
13	VREF	5V voltage reference					
14	GND	Ground for circuit control					
15	OUT1B	First channel N-type driver output. Push-pull output to drive external power devices					
16	OUT1	First channel P-type driver output. Open-drain output, must be connected with pull-up resistor to drive external power devices in operation					



Functional Block Diagram

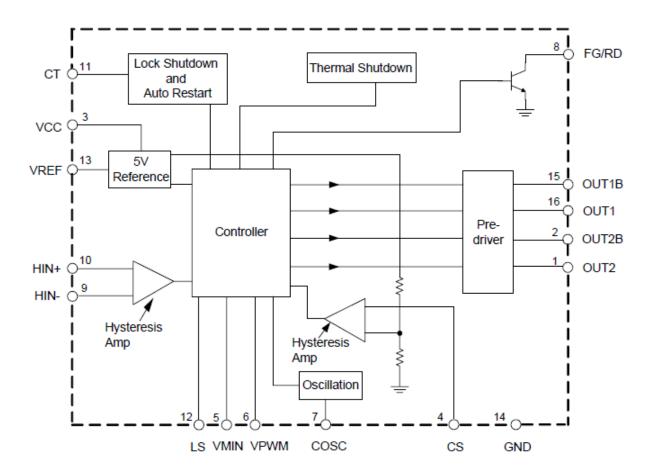


Figure 3. Functional Block Diagram of AM4967



Truth Table

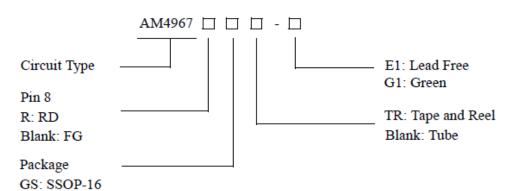
1. Driver-lock. $V_{\mbox{OSC}}$ is High, $V_{\mbox{PWM}},\,V_{\mbox{VMIN}}$ and $V_{\mbox{LS}}$ are low.

HIN-	HIN+	СТ	OUT1	OUT1B	OUT2	OUT2B	FG/RD	Mode	
Н	L	L	L	L	OFF	Н	L	OUT1	→ OUT2 (Driver)
L	Н	_	OFF	Н	L	L	OFF	OUT2	→ OUT1 (Driver)
Н	L	Н	OFF	L	OFF	Н	L	Lock Protection	
L	Н		OFF	Н	OFF	L	OFF		

2. Speed control. $\rm V_{CT},\, \rm V_{LS}$ are low.

VPWM, VMIN	cosc	HIN-	HIN+	OUT1	OUT1B	OUT2	OUT2B	Mode	
ı	Н	Н	L	L	L	OFF	Н	OUT1	→ OUT2 (Driver)
_	• •	L	Н	OFF	Н	L	L	OUT2	→ OUT1 (Driver)
Н	ı	Н	L	OFF	L	OFF	Н	Regenerative	
	_	L	Н	OFF	Н	OFF	L		togonorativo

Ordering Information (Note 4)



Package Ter	Temperature	Part N	umber	Ма	Packing Type	
	remperature	Lead Free	Green	Lead Free	Green	racking type
SSOP-16 -30		AM4967GS-E1	AM4967GS-G1	AM4967GS	AM4967GS-G1	Tube
	00.1- 4050	AM4967GSTR-E1	AM4967GSTR-G1	AM4967GS	AM4967GS-G1	Tape & Reel
	-30 to 105°C	AM4967RGS-E1	AM4967RGS-G1	AM4967RGS	AM4967RGS-G1	Tube
		AM4967RGSTR-E1	AM4967RGSTR-G1	AM4967RGS	AM4967RGS-G1	Tape & Reel

BCD Semiconductor's Pb-free products, as designated with "E1" suffix in the part number, are RoHS compliant. Products with "G1" suffix are available in green package.

 $Note: \ \ 4. For packaging \ details, go \ to \ our \ website \ at \ \underline{https://www.diodes.com/design/support/packaging/diodes-packaging/} \ .$



Absolute Maximum Ratings (Note 5)

Parameter	Symbol	Value	Unit
Supply Voltage	VCC	18	V
OUT1B/OUT2B Output Current	loutb	20	mA
OUT1/OUT2 Sink Current	lout	20	mA
OUT1B/OUT2B/OUT1/OUT2 Withstand Voltage	V _{OUT}	18	V
VPWM/VMIN Withstand Voltage	V _{PWM} /V _{VMIN}	7	V
LS Withstand Voltage	V_{LS}	7	V
FG/RD Withstand Voltage	V _{FG} /V _{RD}	Vcc	V
FG/RD Output Current	I _{FG} /I _{RD}	10	mA
VREF Output Current	I _{REF}	20	mA
Power Dissipation (Note 6)	PD	800	mW
Storage Temperature Range	T _{STG}	-55 to 150	°C
ESD (Human Body Model)	ESD	2000	V

Notes:

6. When mounted on the specified printed circuit board (114.3mmX76.1mmX1.6mm, glass epoxy).

Recommended Operating Conditions

Parameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	V _{CC}	5.5		16	V
VPWM/VMIN Input Voltage		0		5	V
Hall Input Voltage		0.2		3	V
Ambient Temperature	TA	-30		105	°C

^{5.} Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.



Electrical Characteristics (V_{CC}=12V, T_A=25°C, unless otherwise specified.)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Ouissant Comment	IQ1	Lock off	8	10	12	A
Quiescent Current	IQ2	Lock on	8	10	12	mA
VREF Reference Voltage	VREF	IOUT=5mA	4.5	5.0	5.5	V
Current Limit Voltage	VLIM		186	200	215	mV
COSC High Level Voltage	Vosch		3.0	3.4	3.8	V
COSC Low Level Voltage	Voscl		0.9	1.1	1.3	V
COSC Frequency	fosc	CL=220pF	25.4	30	35.5	kHz
CT High Level Voltage	VCTH		2.4	3.3	3.6	V
CT Low Level Voltage	VCTL		1.0	1.3	1.6	V
CT Charge Current	ICHG	VCT=0.5V	1.5	2	2.5	μΑ
CT Discharge Current	IDHG	VCT=3.5V	0.13	0.2	0.25	μΑ
CT Charge and Discharge Ratio	RCD	ICHG/IDHG	8.5	10	14.5	
Linear Start Charge Current	ILS	VLS=1V		0.5	0.7	μΑ
OUT1B/OUT2B High Level Voltage	VOBH	IOUT=10mA	Vcc ~1.8	Vcc ~0.9	Vcc	V
OUT1B/OUT2B Low Level Voltage	VOBL	IOUT=10mA		0.9	1.5	V
OUT1/OUT2 Low Level Voltage	VOL	IOUT=10mA		0.45	0.8	V
FG Signal Hall Input Sensitivity	VHN	IHIN+,IHIN- difference voltage (include offset and hysteresis)		±10		mV
FG Saturation Voltage	VSAT	ISINK=5mA		200	500	mV
FG Leakage Current	ILFG	VFG=VCC			30	μΑ
VPWM/VMIN Bias Current	IPWMB/IVMINB	VOSC=VPWM/VVMIN=2V		0.1	3	μΑ



Typical Performance Characteristics

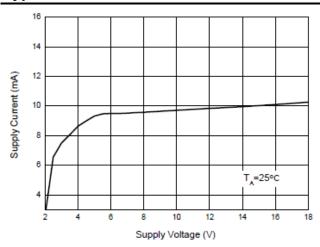


Figure 4. Supply Current vs. Supply Voltage

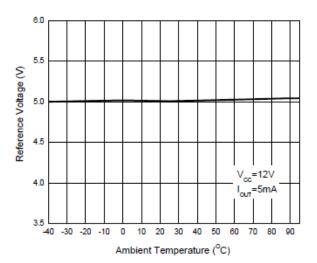
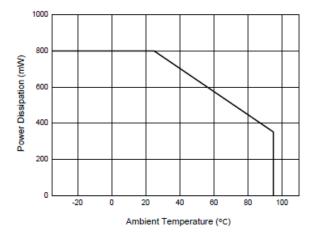


Figure 6. Reference Voltage vs. Ambient Temperature



Current

Figure 8. Power Dissipation vs. Ambient Temperature

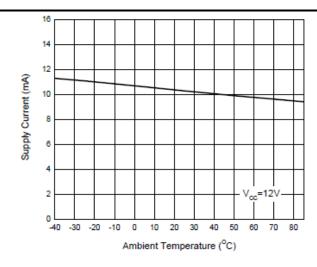


Figure 5. Supply Current vs. Ambient Temperature

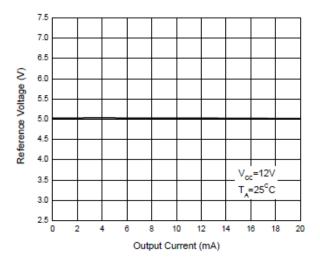


Figure 7. Reference Voltage vs. Output



Operating Diagram

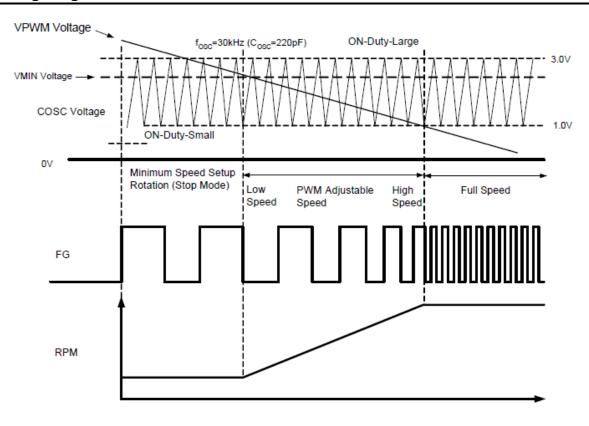


Figure 9. Operating Diagram 1 of AM4967 (Speed Control, Note 7)

Note:

- a. Minimum Speed Setting Mode (Stop Mode)
 - When fan rotate at low speed, its lowest speed is settable by VMIN pin voltage. If not, fan motor stops.
- b. Low Speed to High Speed Setting Mode

PWM control system works by comparing the voltage of VPWM and COSC (1.0V to 3.0V). When VPWM voltage is low, the transistors of the upper and lower side outputs are ON. On contrary, when VPWM voltage is high, the upper side transistors are OFF. And coil current re-circulates in a lower side transistor. Therefore, as the VPWM voltage becomes lower, output ON duty becomes large. Sequentially, coil current increases and the motor runs at high speed which is monitored by FG output while rotation-stop state is monitored by RD output.

c. Full Speed Rotation Mode
When VPWM voltage drops to 1.0V or lower, motor fan will run at full speed. (If the speed is not controlled, VPWM=GND)



Operating Diagram (Continued)

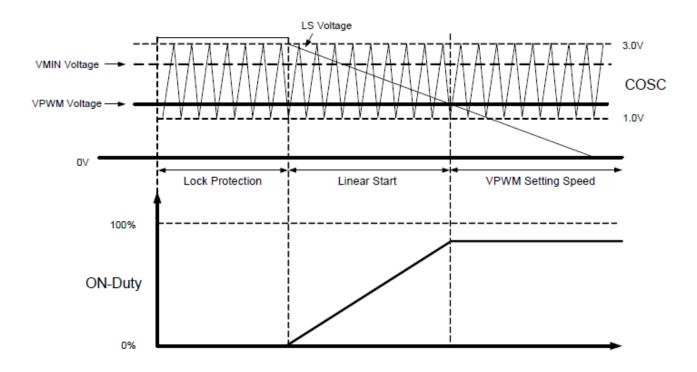


Figure 10. Operating Diagram 2 of AM4967 (Linear Start, VPWM Voltage<VMIN Voltage, Note 4)

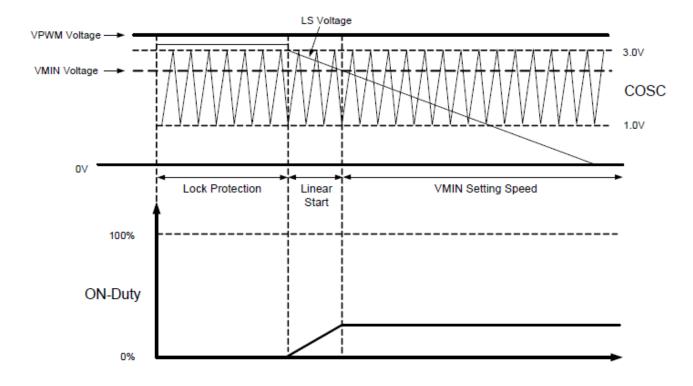


Figure 11. Operating Diagram 3 of AM4967 (Linear Start, VPWM Voltage>VMIN Voltage, Note 4)



Operating Diagram (Continued)

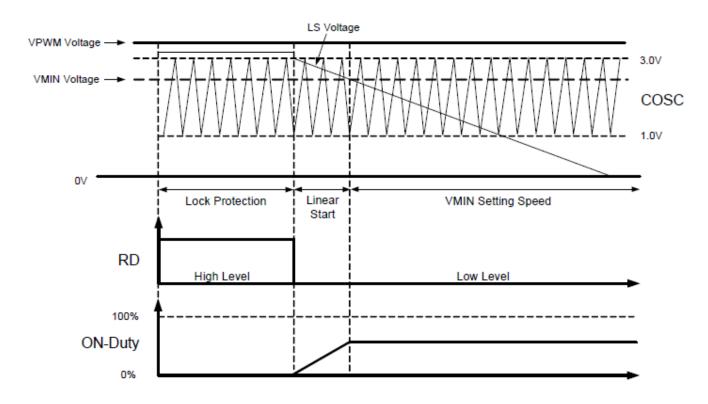


Figure 12. Operating Diagram 4 of AM4967 (Note 8)

Note: 8. The inclination of LS voltage is adjusted between VLS and VREF according to the capacitance which is recommended to be 0.1µF to 1.0µF



Typical Application

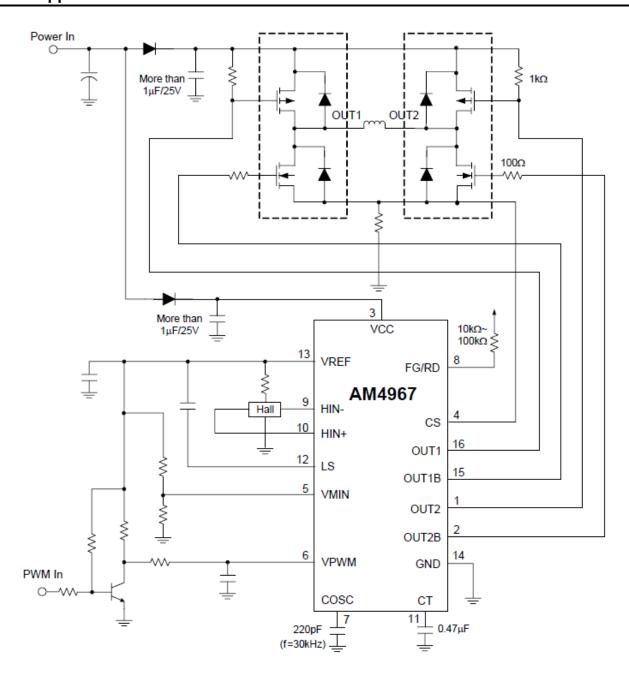


Figure 13. Typical Application of AM4967 (VCC=12V)

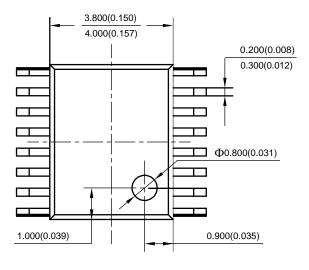


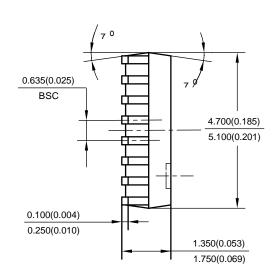
Package Outline Dimensions

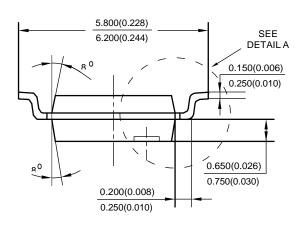
Please see http://www.diodes.com/package-outlines.html for the latest version.

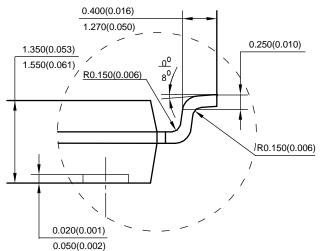
SSOP-16

Unit: mm(inch)









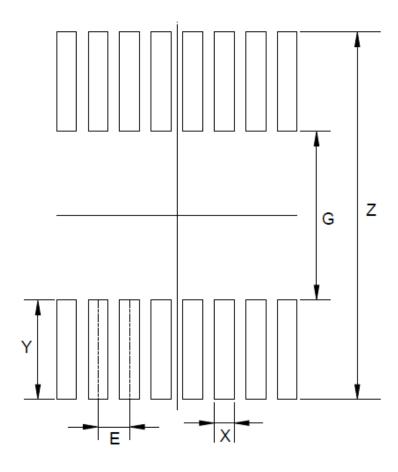
DETAIL A



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SSOP-16



Dimensions	Z	G	X	Y	E	
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	
Value	7.400/0.291	3.400/0.134	0.400/0.016	2.000/0.079	0.635/0.025	



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