



ECH8660

Power MOSFET

30V, 4.5A, 59mΩ, -30V, -4.5A, 59mΩ, Complementary Dual ECH8

ON Semiconductor®

<http://onsemi.com>

Features

- The ECH8660 incorporates an N-channel MOSFET and a P-channel MOSFET that feature low ON-resistance and high-speed switching, thereby enabling high-density mounting
- 4V drive
- Halogen free compliance

Specifications

Absolute Maximum Ratings at Ta=25°C

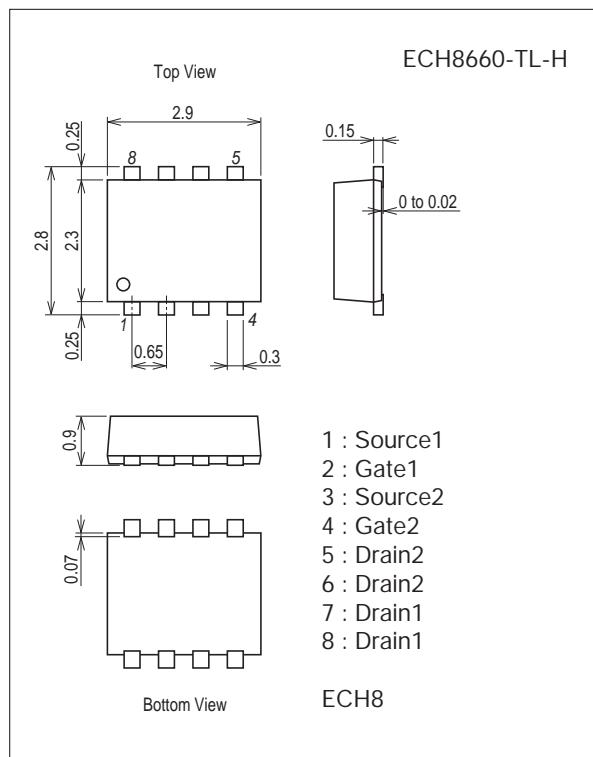
Parameter	Symbol	Conditions	N-channel	P-channel	Unit
Drain-to-Source Voltage	V _{DSS}		30	-30	V
Gate-to-Source Voltage	V _{GSS}		±20	±20	V
Drain Current (DC)	I _D		4.5	-4.5	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	30	-30	A
Allowable Power Dissipation	P _D	When mounted on ceramic substrate (1200mm ² ×0.8mm) 1unit	1.3		W
Total Dissipation	P _T	When mounted on ceramic substrate (1200mm ² ×0.8mm)	1.5		W
Channel Temperature	T _{ch}		150		°C
Storage Temperature	T _{stg}		-55 to +150		°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

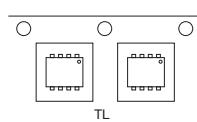
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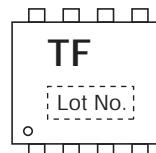
Product & Package Information

- Package : ECH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

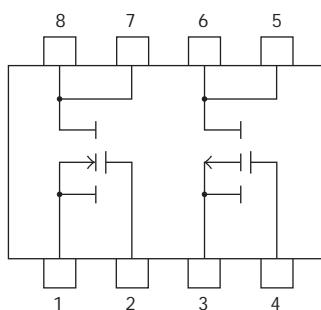
Packing Type : TL



Marking



Electrical Connection

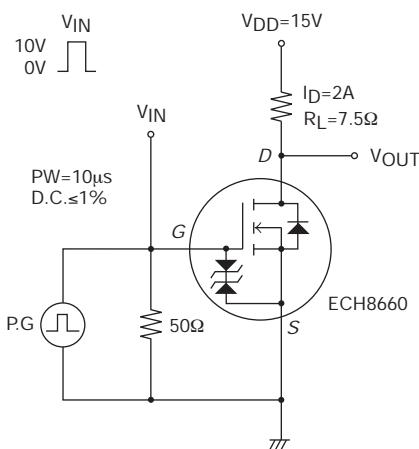


Electrical Characteristics at Ta=25°C

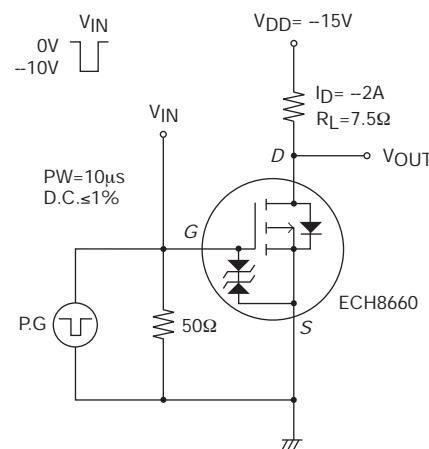
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[N-channel]						
Drain-to-Source Breakdown Voltage	V(BR)DSS	Id=1mA, VGS=0V	30			V
Zero-Gate Voltage Drain Current	IdSS	VDS=30V, VGS=0V			1	μA
Gate-to-Source Leakage Current	IGSS	VGS=±16V, VDS=0V			±10	μA
Cutoff Voltage	VGS(off)	VDS=10V, Id=1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	VDS=10V, Id=2A	1	1.66		S
Static Drain-to-Source On-State Resistance	RDS(on)1	Id=2A, VGS=10V	45	59		mΩ
	RDS(on)2	Id=1A, VGS=4.5V	85	119		mΩ
	RDS(on)3	Id=1A, VGS=4V	110	155		mΩ
Input Capacitance	Ciss	VDS=10V, f=1MHz	240			pF
Output Capacitance	Coss	VDS=10V, f=1MHz	45			pF
Reverse Transfer Capacitance	Crss	VDS=10V, f=1MHz	30			pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.	6.2			ns
Rise Time	t _r	See specified Test Circuit.	11			ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit.	17			ns
Fall Time	t _f	See specified Test Circuit.	7.5			ns
Total Gate Charge	Qg	VDS=10V, VGS=10V, Id=4.5A	4.4			nC
Gate-to-Source Charge	Qgs	VDS=10V, VGS=10V, Id=4.5A	1.1			nC
Gate-to-Drain "Miller" Charge	Qgd	VDS=10V, VGS=10V, Id=4.5A	0.64			nC
Diode Forward Voltage	VSD	Is=4.5A, VGS=0V	0.84	1.2		V
[P-channel]						
Drain-to-Source Breakdown Voltage	V(BR)DSS	Id=-1mA, VGS=0V	-30			V
Zero-Gate Voltage Drain Current	IdSS	VDS=-30V, VGS=0V			-1	μA
Gate-to-Source Leakage Current	IGSS	VGS=±16V, VDS=0V			±10	μA
Cutoff Voltage	VGS(off)	VDS=-10V, Id=-1mA	-1.2		-2.3	V
Forward Transfer Admittance	yfs	VDS=-10V, Id=-2A	2.5	4.2		S
Static Drain-to-Source On-State Resistance	RDS(on)1	Id=-2A, VGS=-10V	45	59		mΩ
	RDS(on)2	Id=-1A, VGS=-4.5V	71	100		mΩ
	RDS(on)3	Id=-1A, VGS=-4V	82	115		mΩ
Input Capacitance	Ciss	VDS=-10V, f=1MHz	430			pF
Output Capacitance	Coss	VDS=-10V, f=1MHz	105			pF
Reverse Transfer Capacitance	Crss	VDS=-10V, f=1MHz	75			pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.	7.5			ns
Rise Time	t _r	See specified Test Circuit.	26			ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit.	45			ns
Fall Time	t _f	See specified Test Circuit.	35			ns
Total Gate Charge	Qg	VDS=-10V, VGS=-10V, Id=-4.5A	10			nC
Gate-to-Source Charge	Qgs	VDS=-10V, VGS=-10V, Id=-4.5A	2.0			nC
Gate-to-Drain "Miller" Charge	Qgd	VDS=-10V, VGS=-10V, Id=-4.5A	2.5			nC
Diode Forward Voltage	VSD	Is=-4.5A, VGS=0V	-0.85	-1.2		V

Switching Time Test Circuit

[N-channel]

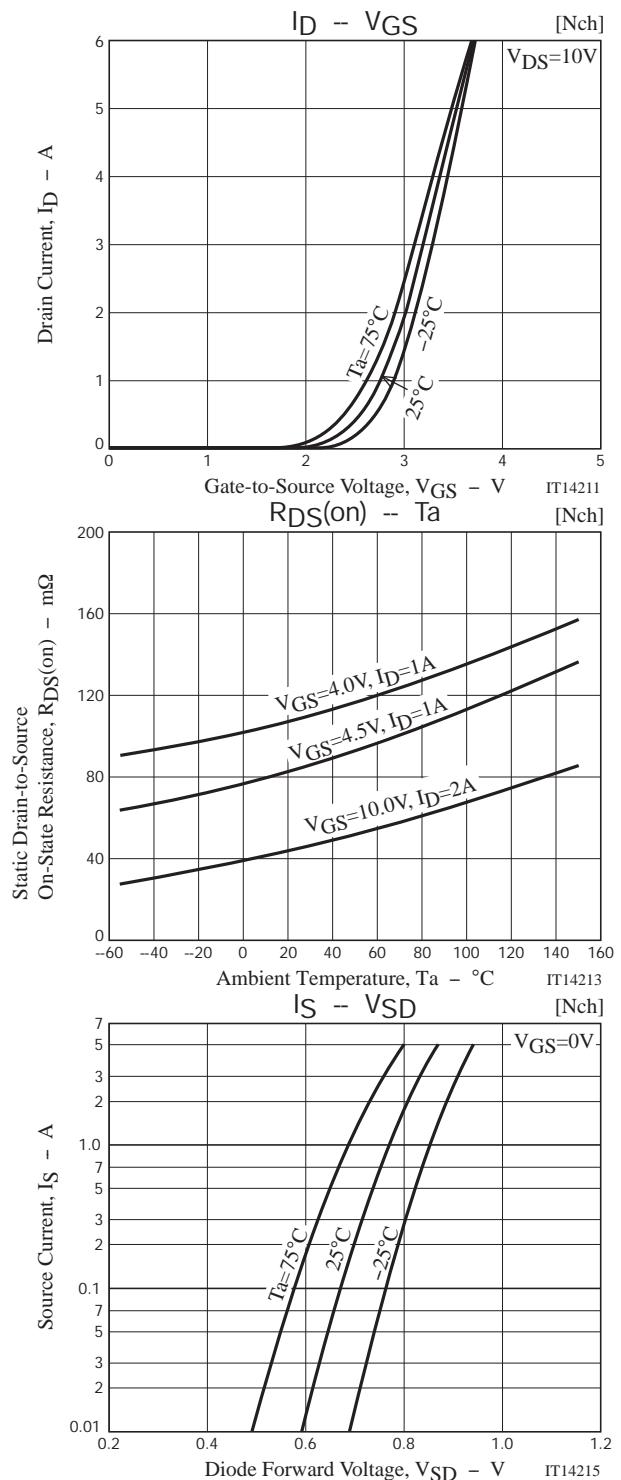
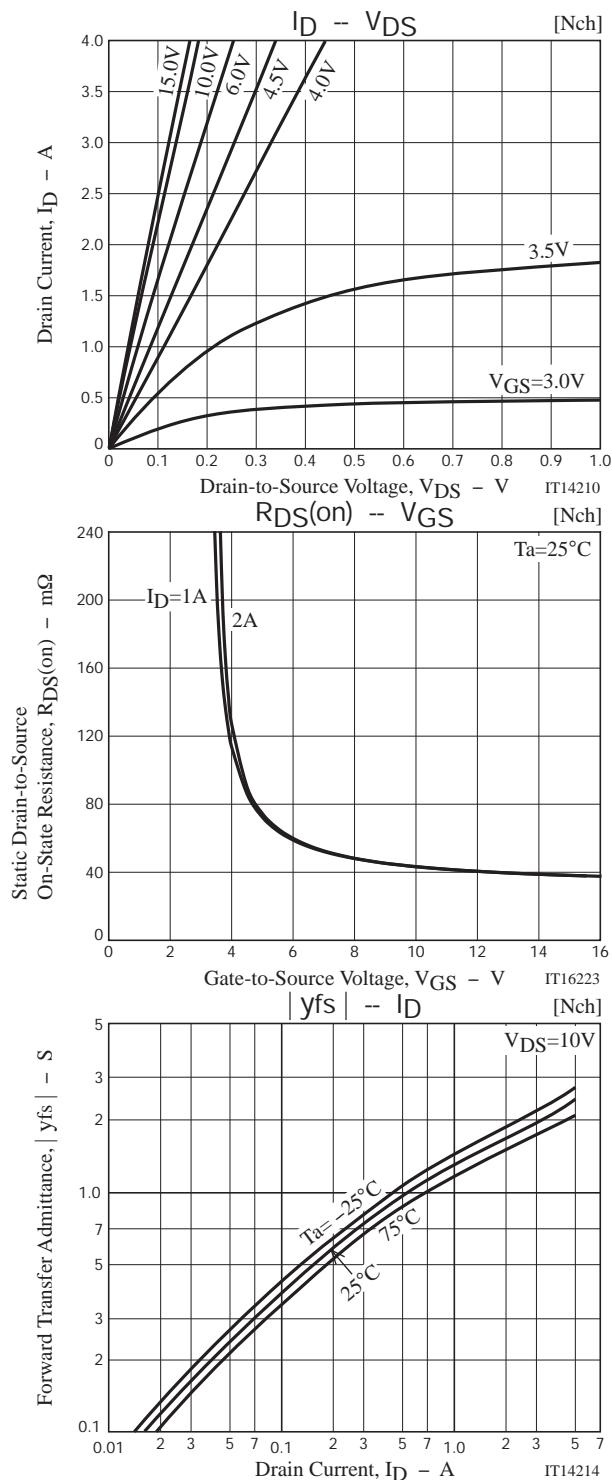


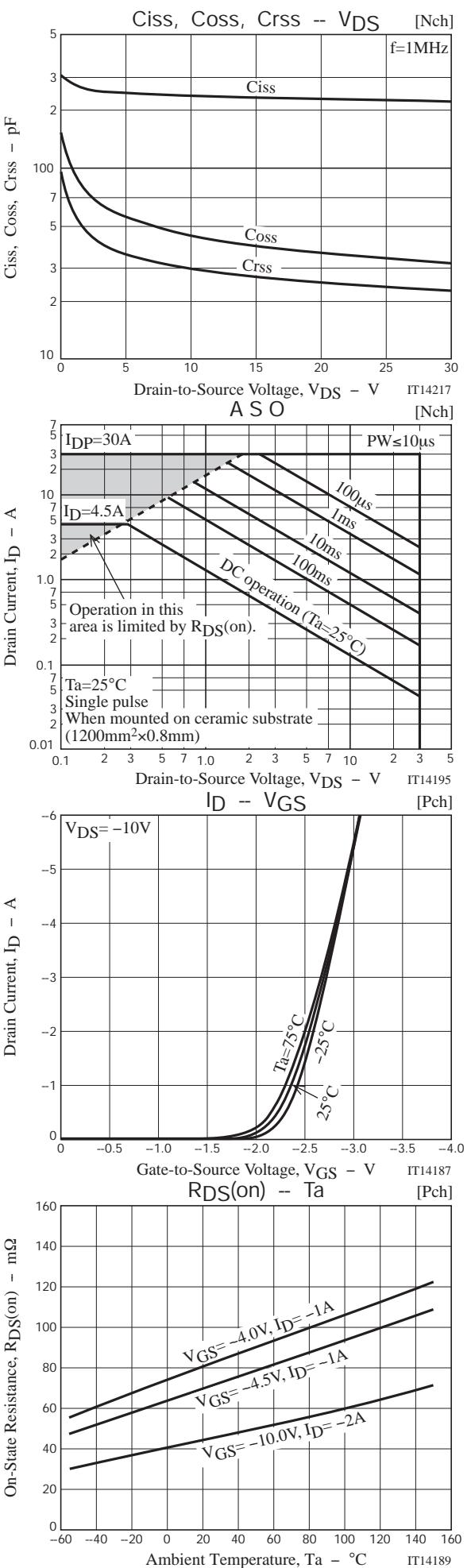
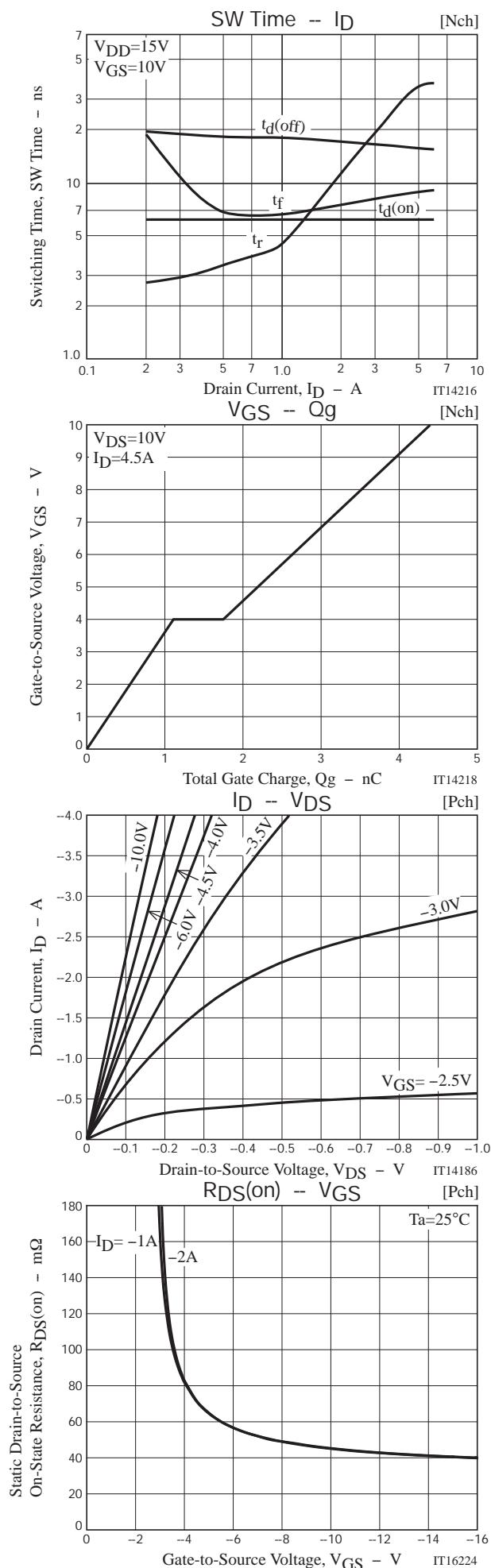
[P-channel]

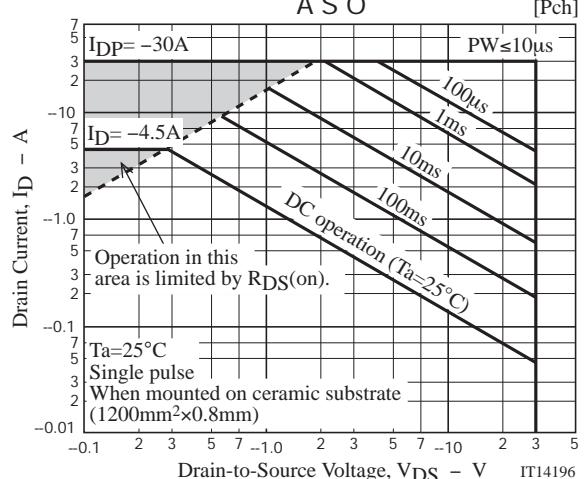
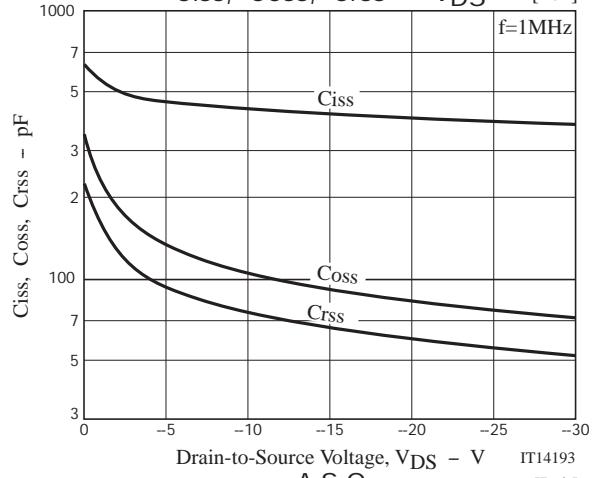
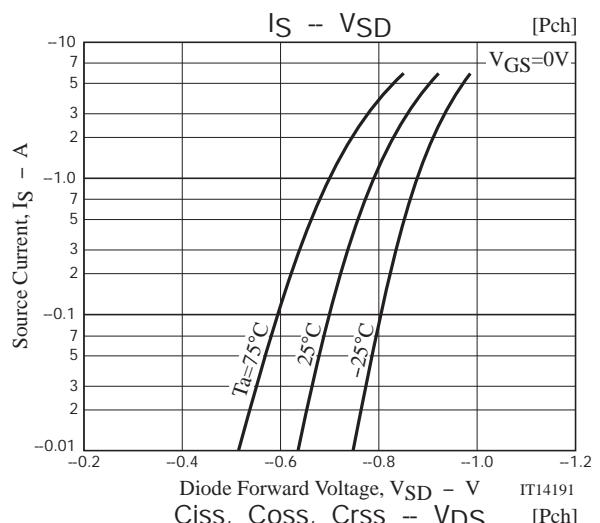
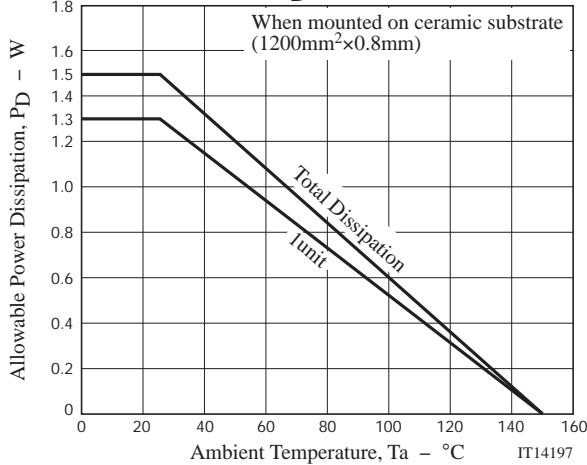
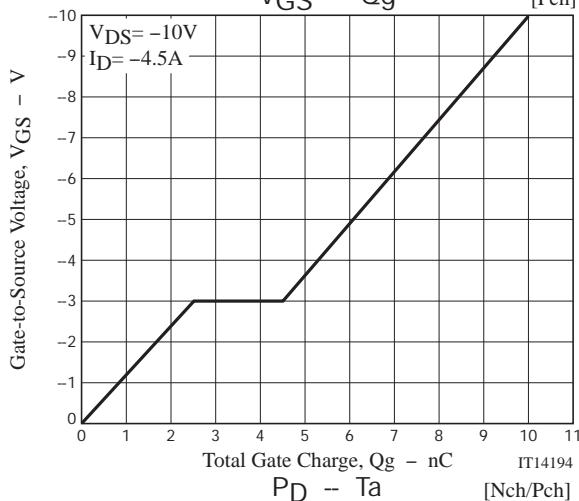
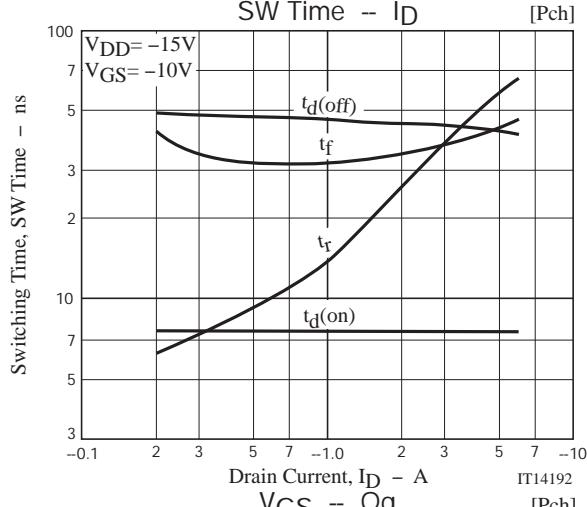
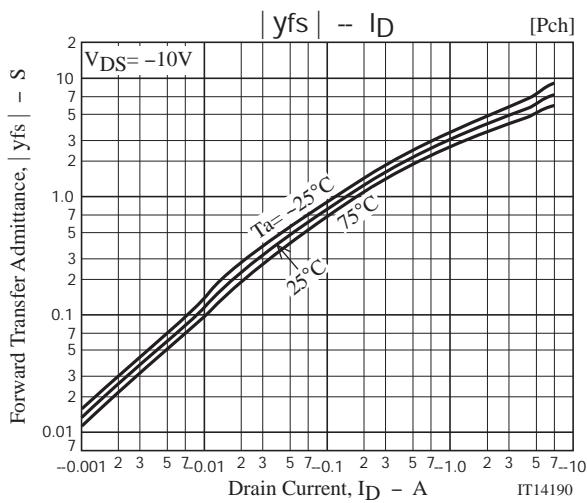


Ordering Information

Device	Package	Shipping	memo
ECH8660-TL-H	ECH8	3,000pcs./reel	Pb-Free and Halogen Free





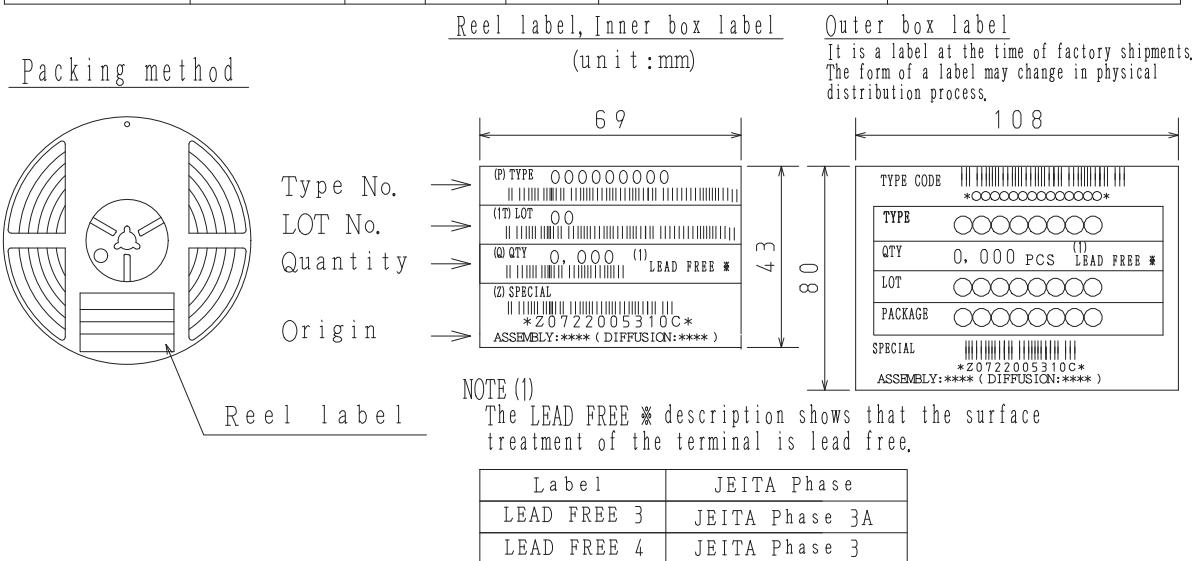


Embossed Taping Specification

ECH8660-TL-H

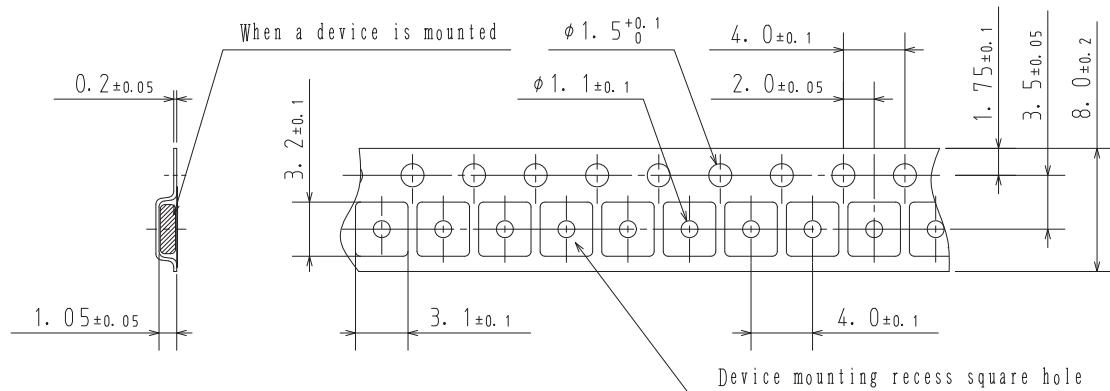
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
ECH8	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) $183 \times 72 \times 185$	6 inner boxes contained Dimensions:mm (external) $440 \times 195 \times 210$

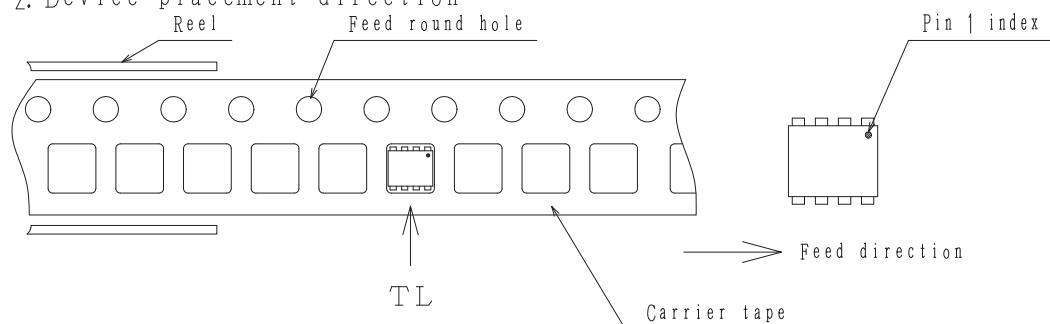


2. Taping configuration

2-1. Carrier tape size (unit:mm)



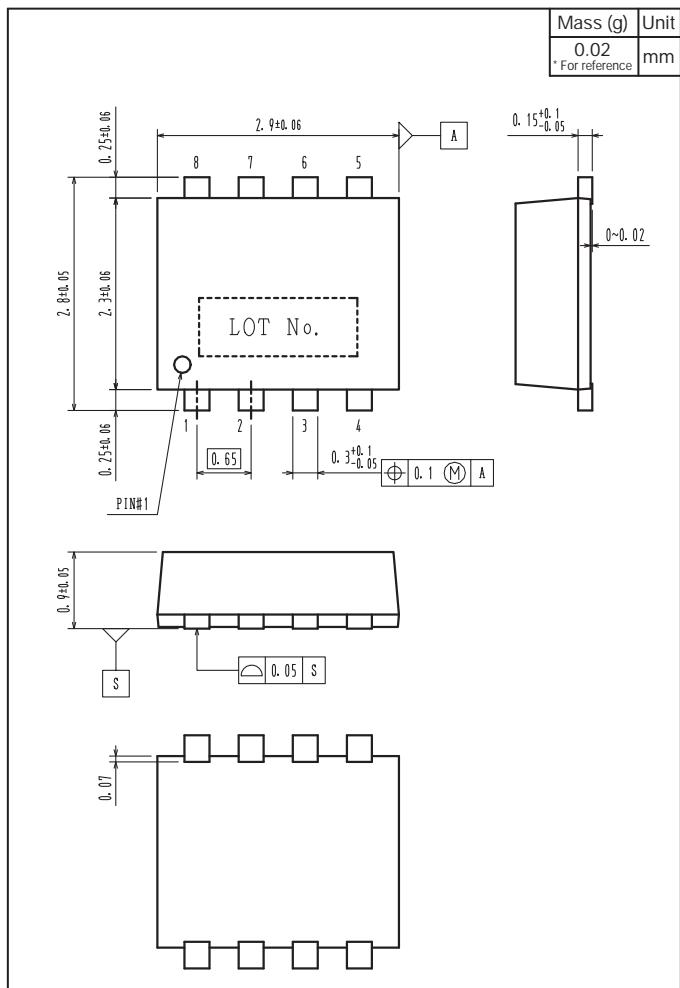
2-2. Device placement direction



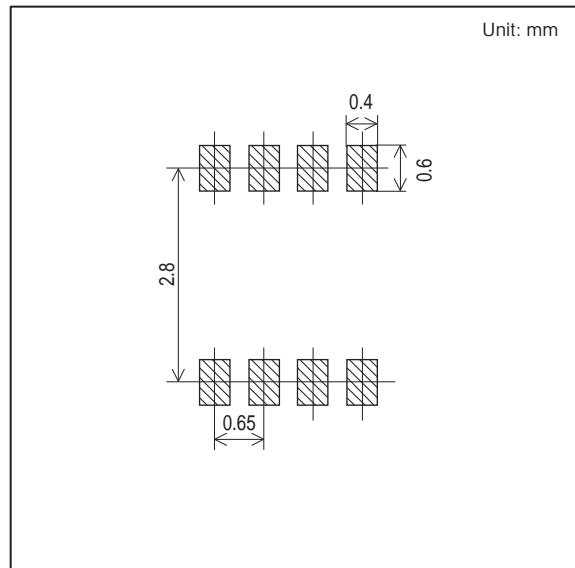
Those with pin 1 index on the feed hole side.....TL

Outline Drawing

ECH8660-TL-H



Land Pattern Example



Note on usage : Since the ECH8660 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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