

**PCN Number: RC134804**

**Notification Date\*:** December 11, 2013

<b>Title:</b> ATSAM3U1 and ATSAM3N1- Migration to Marketing Revision Level B																								
<b>Product Identification:</b>																								
		<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 50%;">Current Part Number</th> <th style="width: 50%;">New Part Number</th> </tr> </thead> <tbody> <tr><td>ATSAM3U1CA-AU</td><td>ATSAM3U1CB-AU</td></tr> <tr><td>ATSAM3U1CA-CU</td><td>ATSAM3U1CB-CU</td></tr> <tr><td>ATSAM3U1EA-AU</td><td>ATSAM3U1EB-AU</td></tr> <tr><td>ATSAM3U1EA-CU</td><td>ATSAM3U1EB-CU</td></tr> <tr><td>ATSAM3N1AA-AU</td><td>ATSAM3N1AB-AU</td></tr> <tr><td>ATSAM3N1AA-MU</td><td>ATSAM3N1AB-MU</td></tr> <tr><td>ATSAM3N1BA-AU</td><td>ATSAM3N1BB-AU</td></tr> <tr><td>ATSAM3N1BA-MU</td><td>ATSAM3N1BB-MU</td></tr> <tr><td>ATSAM3N1CA-AU</td><td>ATSAM3N1CB-AU</td></tr> <tr><td>ATSAM3N1CA-CU</td><td>ATSAM3N1CB-CU</td></tr> </tbody> </table>	Current Part Number	New Part Number	ATSAM3U1CA-AU	ATSAM3U1CB-AU	ATSAM3U1CA-CU	ATSAM3U1CB-CU	ATSAM3U1EA-AU	ATSAM3U1EB-AU	ATSAM3U1EA-CU	ATSAM3U1EB-CU	ATSAM3N1AA-AU	ATSAM3N1AB-AU	ATSAM3N1AA-MU	ATSAM3N1AB-MU	ATSAM3N1BA-AU	ATSAM3N1BB-AU	ATSAM3N1BA-MU	ATSAM3N1BB-MU	ATSAM3N1CA-AU	ATSAM3N1CB-AU	ATSAM3N1CA-CU	ATSAM3N1CB-CU
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<b>Reason for Change:</b>	<input type="checkbox"/> Material / Composition <input type="checkbox"/> Processing / Manufacturing <input checked="" type="checkbox"/> Design / Firmware <input checked="" type="checkbox"/> Datasheet	<input checked="" type="checkbox"/> Manufacturing Location <input type="checkbox"/> Quality / Reliability <input type="checkbox"/> Logistics <input type="checkbox"/> Other:																						
<b>Change Description:</b>																								
<b>SAM3U1 rev A to rev B</b>																								
<p>A complete list of differences between the marketing revision level (MRL) A and B can be found in chapter 46 of the ATSAM3U series datasheet with Atmel document reference 6430F.</p> <p>Following errata have been corrected on the ATSAM3U1 rev B:  <i>Flash: Read Flash in 64-bit Mode</i>  <i>ADC12B: Saturation</i></p> <p>The ChipID has been incremented to:</p> <p style="margin-left: 40px;">SAM3U1C (Rev B) : 0x2809 0562          SAM3U1E (Rev B) : 0x2819 0562</p> <p>The product is manufactured in 0.15micron embedded Flash technology in UMC Taiwan.</p>																								

**SAM3N1 rev A to rev B**

A complete list of differences between the marketing revision level (MRL) A and B can be found in chapter 38 of the ATSAM3N series datasheet with Atmel document reference 11011B.

Following errata have been corrected on the ATSAM3U1 rev B:  
*Flash: Flash Programming*

The ChipID has been incremented to:

- SAM3N1A Revision B chip ID is 0x2938 0561
- SAM3N1B Revision B chip ID is 0x2948 0561
- SAM3N1C Revision B chip ID is 0x2958 0561

The product is manufactured in 0.15micron embedded Flash technology in UMC Taiwan

**Identification Method to Distinguish Change:**

Marketing Revision Level (MRL) has been incremented on the top side marking to B.

<b>Qualification Data:</b>	<input checked="" type="checkbox"/> Available	<input type="checkbox"/> Will be available (mm/dd/yr):	<input type="checkbox"/> Not Applicable
<b>Samples:</b>	<input checked="" type="checkbox"/> Available	<input type="checkbox"/> Will be available (mm/dd/yr):	<input type="checkbox"/> Not Applicable

**Quantifiable Impact on Quality & Reliability:**

None

**Forecasted Availability Date:** December 4, 2013

**Last Time Buy Date:** March 4, 2014

**Last Ship Date:** December 11, 2014

*\*All orders placed after the notification date are non-cancellable and non-returnable (NCNR).*

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