# N-Channel Power MOSFET 60 V, 97 A, 7.8 m $\Omega$

#### **Features**

- Low R<sub>DS(on)</sub>
- High Current Capability
- 100% Avalanche Tested
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

## MAXIMUM RATINGS (T<sub>J</sub> = 25°C Unless otherwise specified)

Parameter			Symbol	Value	Unit	
Drain-to-Source Voltage			$V_{DSS}$	60	V	
Gate-to-Source Voltage - Continuous			V <sub>GS</sub>	±20	V	
Gate-to-Source Voltage – Nonrepetitive (T <sub>P</sub> < 10 μs)			V <sub>GS</sub>	30	٧	
Continuous Drain Current	Steady State	T <sub>C</sub> = 25°C	I <sub>D</sub>	97	Α	
Current	Siale	T <sub>C</sub> = 100°C		68		
Power Dissipation	Steady State	T <sub>C</sub> = 25°C	P <sub>D</sub>	150	W	
Pulsed Drain Current	t <sub>p</sub> = 10 μs		I <sub>DM</sub>	383	Α	
Operating and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	
Source Current (Body Diode)			Is	97	Α	
Single Pulse Drain-to-Source Avalanche Energy (L = 0.1 mH, I <sub>L(pk)</sub> = 56 A)			E <sub>AS</sub>	157	mJ	
Peak Diode Recovery (dV/dt)			dV/dt	4.1	V/ns	
Lead Temperature for Soldering Purposes (1/8" from Case for 10 Seconds)			TL	260	°C	

## THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case (Drain) Steady State	$R_{\theta JC}$	1.0	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	36	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

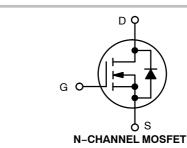
 Surface mounted on FR4 board using 1 sq in pad size, (Cu Area 1.127 sq in [2 oz] including traces).

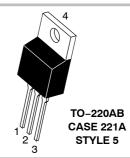


## ON Semiconductor®

http://onsemi.com

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX
60 V	7.8 m $\Omega$ @ 10 V	97 A





## MARKING DIAGRAMS & PIN ASSIGNMENTS

A Drain

NTP
5863NG
AYWW

1 3 Source
2 Drain
= Pb-Free Device

G = Pb-Free Device A = Assembly Location

WW = Work Week

## **ORDERING INFORMATION**

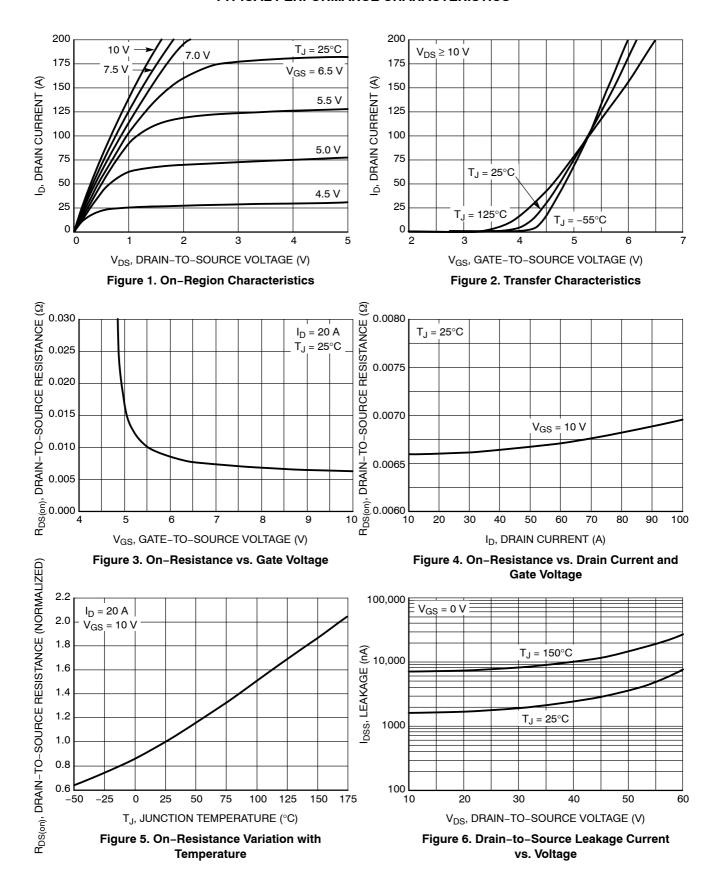
See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

## **ELECTRICAL CHARACTERISTICS** ( $T_J = 25$ °C Unless otherwise specified)

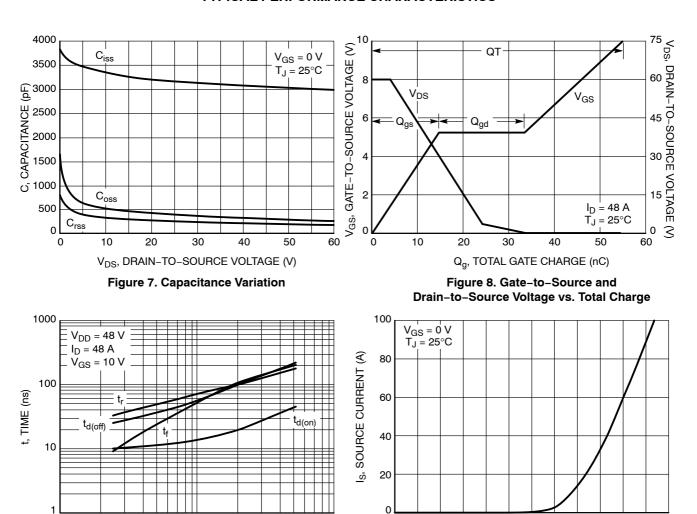
Characteristics	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•			-	•	-	
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>DS</sub> = 0 V, I <sub>D</sub> = 250 μA		60			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA, ref to 25°C			47		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$ $V_{J} = 25^{\circ}C$				1.0	μΑ
		V <sub>DS</sub> = 60 V	T <sub>J</sub> = 125°C			50	1
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$				±100	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{GS} = V_{DS}, I_D = 250 \mu A$		2.0		4.0	V
Negative Threshold Temperature Coefficient	V <sub>GS(th)</sub> /T <sub>J</sub>				9.1		mV/°C
Drain-to-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A			6.5	7.8	mΩ
Forward Transconductance	9FS	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A			12		S
CHARGES, CAPACITANCES & GATE RESIST	ANCE						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz			3200		pF
Output Capacitance	C <sub>oss</sub>				350		
Transfer Capacitance	C <sub>rss</sub>				230		
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 48 V, I <sub>D</sub> = 48 A			55		nC
Threshold Gate Charge	Q <sub>G(TH)</sub>				3.4		1
Gate-to-Source Charge	$Q_{GS}$				14.5		
Gate-to-Drain Charge	$Q_{GD}$				19		
Gate Resistance	$R_{G}$				0.4		Ω
SWITCHING CHARACTERISTICS, V <sub>GS</sub> = 10 V	(Note 3)						
Turn-On Delay Time	t <sub>d(on)</sub>				10		ns
Rise Time	t <sub>r</sub>	$V_{GS}$ = 10 V, $V_{DD}$ = 48 V, $I_{D}$ = 48 A, $R_{G}$ = 2.5 $\Omega$			34		
Turn-Off Delay Time	t <sub>d(off)</sub>				25		
Fall Time	t <sub>f</sub>				9.0		
DRAIN-SOURCE DIODE CHARACTERISTICS	3						
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V I <sub>S</sub> = 48 A	T <sub>J</sub> = 25°C		0.96	1.5	V <sub>dc</sub>
			T <sub>J</sub> = 150°C		0.85		
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0 \ V_{dc}, \ I_S = 48 \ A_{dc}, \ dI_S/dt = 100 \ A/\mu s$			32		ns
Charge Time	ta				20		1
Discharge Time	t <sub>b</sub>				12		
Reverse Recovery Stored Charge	Q <sub>RR</sub>				28		nC

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

## TYPICAL PERFORMANCE CHARACTERISTICS



## TYPICAL PERFORMANCE CHARACTERISTICS



100

0.0

0.2

0.4

0.6

V<sub>SD</sub>, SOURCE-TO-DRAIN VOLTAGE (V)

Figure 10. Diode Forward Voltage vs. Current

0.8

1.0

1.2

Figure 9. Resistive Switching Time Variation vs. Gate Resistance

1000

100

0.1

ID, DRAIN CURRENT (A)

10

 $R_G$ , GATE RESISTANCE ( $\Omega$ )

160 100 μs = 10 μs  $\mathsf{E}_{\mathsf{AS}},$  SINGLE PULSE DRAIN-TO-SOURCE AVALANCHE ENERGY ( $\mathfrak{mJ}$ )  $I_{D} = 56 A$ 120 100 80  $V_{GS} = 10 V$ 60 Single Pulse  $T_C = 25^{\circ}C$ 40 R<sub>DS(on)</sub> Limit 20 Thermal Limit Package Limit 0 0.1 10 25 100 125 175 T<sub>J</sub>, STARTING JUNCTION TEMPERATURE (°C) V<sub>DS</sub>, DRAIN-TO-SOURCE VOLTAGE (V)

Figure 11. Maximum Rated Forward Biased Safe Operating Area

Figure 12. Maximum Avalanche Energy vs. Starting Junction Temperature

## TYPICAL PERFORMANCE CHARACTERISTICS

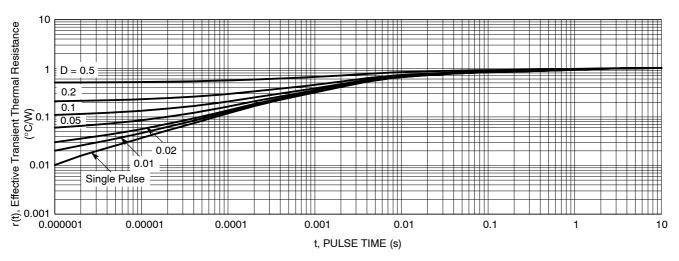


Figure 13. Thermal Response

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTP5863NG	TO-220AB (Pb-Free)	50 Units / Rail

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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