



181220414 Si3406x V1.0 Datasheet and V1.1 Errata Published

Bulletin Issue Date: 12/20/2018

Effective Date: 12/20/2018

Description of Change

Silicon Labs is pleased to announce the publication of the version 1.0 data sheet for the Si3406, Si34061, and Si34062 PD controllers for Power Over Ethernet. This data sheet updates specifications based on characterization data and removes all TBD values. Please refer to the change list in the data sheet for a complete list of changes.

Silicon Labs announces Errata revision 1.1 for Si3406, Si34061, and Si34062 PoE PD devices. Revision 1.1 of the Errata updates the following errata item:

Description: Si3406-A-GM, Si34061-A-GM, and Si34062-A-GM products may not comply with IEEE 802.3at section 33.3.5.2.1 under certain operating conditions and may not present a non-valid detection signature during the mark phase of a 2 event classification.

Impact: Checking for a non-valid detect signature is not part of standard power sourcing equipment (PSE) operation, as the IEEE 802.3at spec does not require the PSE to check for this non-valid detection signature (resistance). IEEE 802.3at PoE powered device (PD) conformance testing may check for this non-valid detection signature and report an error.

Resolution: Products with date code 1838 or later do not have this issue.

Reason for Change

Silicon errata discovered after extensive characterization of the devices across all process, voltage and temperature conditions has been fixed.

Publication of Si3406x version 1.0 data sheet

Product Identification

Si3406-A-GM
Si34061-A-GM
Si34062-A-GM
Si3406-A-GMR
Si34061-A-GMR
Si34062-A-GMR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

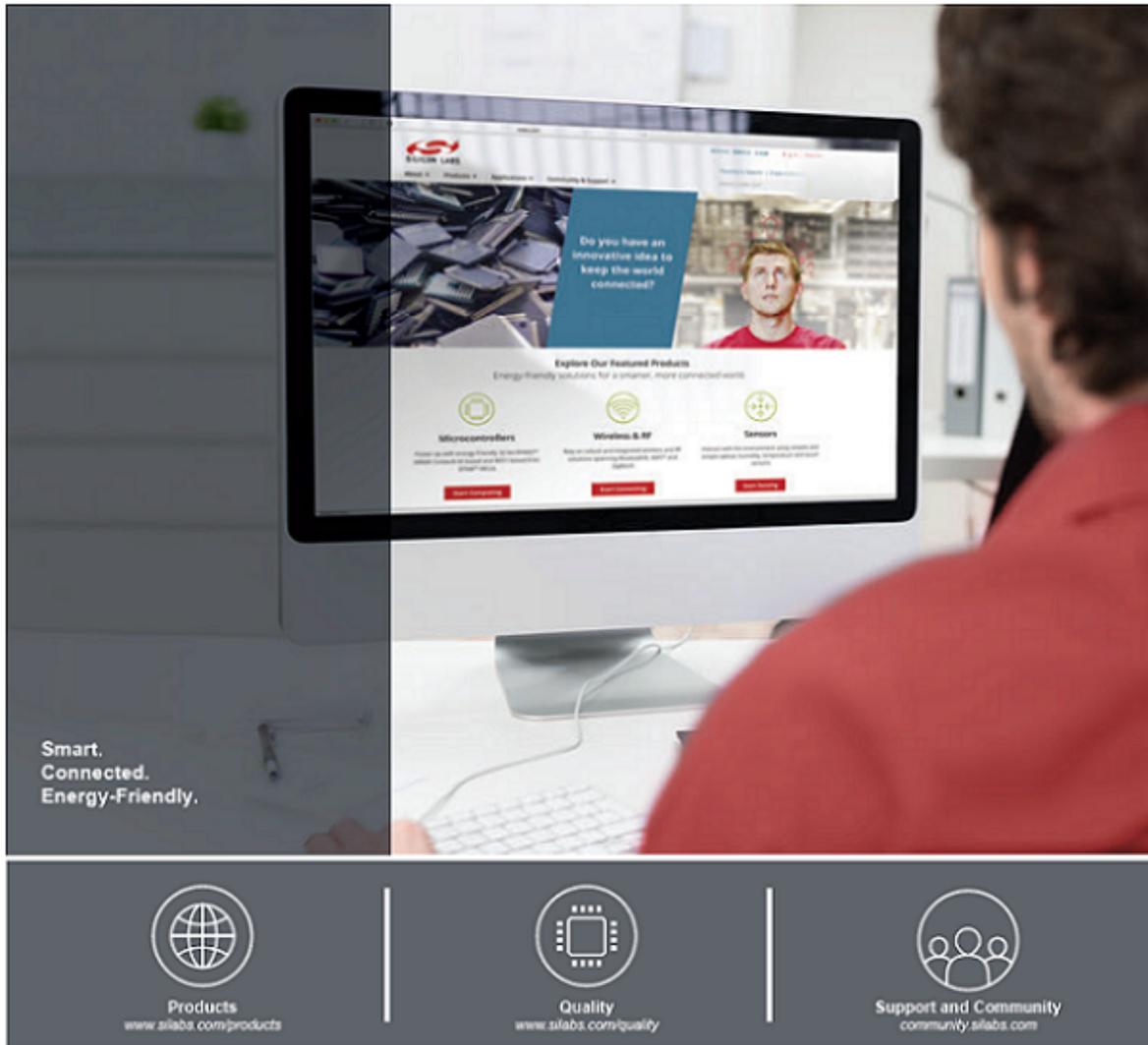
Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at <http://www.silabs.com>.

Customer Actions Needed:

None

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, “how-to” and design documents, product change notices (PCN) and other valuable content available only to registered users. <http://www.silabs.com/profile>



Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOModem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

<http://www.silabs.com>