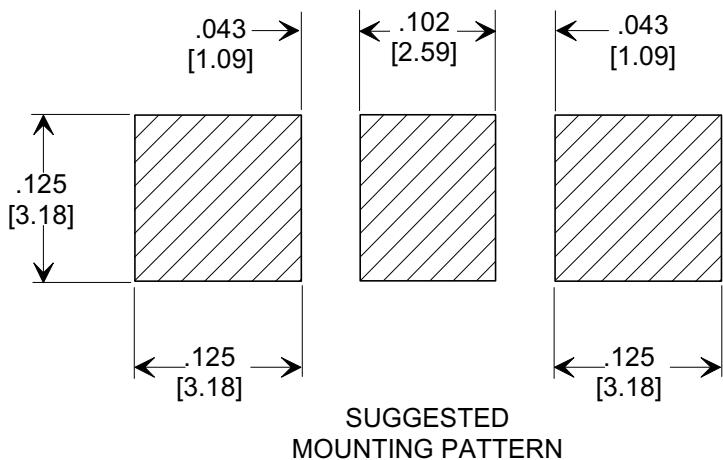
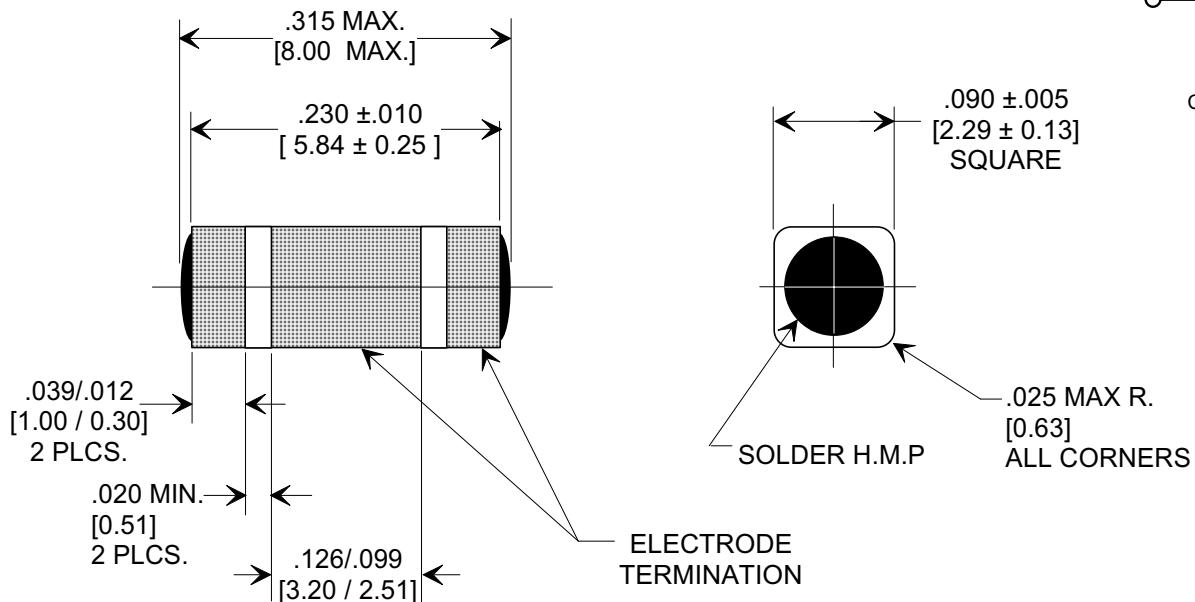
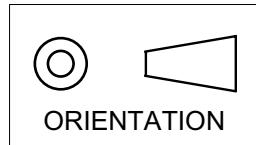


Electrical Testing  
per Tusonix standard  
test plans and Mil-Std-202  
Test Methods.

**MARKETING SALES DRAWING**  
DIMENSIONS IN INCHES - DO NOT SCALE THIS DRAWING  
DIMENSIONS IN METRIC [ ]



SUGGESTED  
MOUNTING PATTERN

NOTES:

1. INDUCTANCE TO BE 0.1  $\mu$ H MINIMUM.
2. PACKAGE: BULK OR BLISTER TAPE TO IEC-60286-3 TYPE II AND EIA-481-A.

CAP.	TOLERANCE	WVDC		TEST VOLTAGE	IR MIN. @ 100 (VDC)	$I_{DC}$	ELECTRODE TERMINATION	TYPICAL NO LOAD INSERTION LOSS dB AT 25°C PER MIL-STD-220		
		85°C	125°C					10 MHZ	100 MHZ	1 GHZ
100 pF	+80% / -20%	100	100	300 VDC	10 G $\Omega$	20 A	SOLDER	----	1	19
REVISION RECORD										
Original Release C.O. WAS SK8060-S-TYPE. S.M. 04-22-02										
<b>20020418-4-06</b>	<b>0</b>							--TOLERANCES-- Unless Otherwise Specified		
RELOCATED MOUNTING PATTERN L.E. 07-09-02								DECIMAL $\pm$ .005 [0.13]		
<b>20020709-2-01</b>	<b>1</b>							ANGLES $\pm$ //		
ADDED INSERTION LOSS COLUMN. S.M. 04-07-03								Drawn L.E. 03-01-02		
<b>20030407-1-01</b>	<b>2</b>							Scale 7 X		
REVISED DRAWING. S.M. 06-23-03								Approved B.M. 03-01-02		
<b>20030619-2-12</b>	<b>3</b>							<b>TUSONIX</b> TUCSON, ARIZONA		
<b>A</b>		<b>4702-000</b>								