

## Product Change Notification - SYST-03GOSJ084

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**Date:**

05 Jul 2019

**Product Category:**

Ethernet Switches

**Affected CPNs:****Notification subject:**

Data Sheet - KSZ9893R Data Sheet Document Revision

**Notification text:**

SYST-03GOSJ084

Microchip has released a new DeviceDoc for the KSZ9893R Data Sheet of devices. If you are using one of these devices please read the document located at [KSZ9893R Data Sheet](#).

**Notification Status:** Final

**Description of Change:** 1) Table 1-1, General Terms: Added GPO, IBA, and IBS. 2) Table 3-2, Pin Descriptions: Corrected SDI/SDA/MDIO description to indicate SDA and MDIO are open-drain signals. 3) Table 3-3, Configuration Strap Descriptions: Updated LED2\_0 description for RGMII operation. Updated LED2\_1 description for clarity. 4) Section 4.1.5, Pair-Swap, Alignment, and Polarity Check: Updated first bullet description. 5) Section 4.3.3, Back-Off Algorithm: Updated second sentence - After 16 consecutive collisions, the packet is dropped. 6) Section 4.3.5, Legal Packet Size: Updated paragraph. 7) Section 4.3.6, Flow Control: Fixed typo. 8) Table 4-9, Hashed(DA) + FID Lookup in VLAN Mode: Updated the description for the Yes action. 9) Section 4.4.3.2.1, Tag Insertion and Removal: Removed sentence - This feature is controlled on a per port basis. 10) Section 4.4.8, Multiple Spanning Tree Support: Removed mention of Reserved Multicast Address Table. 11) Section 4.4.9, Tail Tagging Mode: Section redone with additional information. 12) Table 4-16, Matching Rule Options: For MD[1:0]=01, the ENB[1:0] 01 and 10 definitions swapped. 13) Table 4-17, ACL Matching Rule Parameters for MD = 01: For ENB[1:0], 01 and 10 definitions swapped. 14) Section 4.9, In-Band Management: (a) First paragraph rewritten for greater clarity. (b) Corrected number of ports in third paragraph. (c) Added additional information to sixth paragraph. 15) Section 4.10.3, Reduced Gigabit Media Independent Interface (RGMII): (a) Added additional bullet to end of first bulleted list: In-Band Status is supported when connected to a compatible PHY. (b) Updated first paragraph after bulleted lists. (c) Added new subsection Section 4.10.3.1, RGMII In-Band Status (IBS). 16) Section 5.1.2.2, In-Band Management (IBA) Control Register: Added to description of bit 31 to indicate the polarity is reversed compared to the LED1\_1 pin. 17) Section 5.1.2.5, LED Override Register, Section 5.1.2.6, LED Output Register: Corrected GPIO references to GPO. 18) Section 5.1.3.2, LED StrapIn Register: Updated field 3:0 description. 19) Section 5.2.1.7, Port Operation Control 0 Register: Updated bit 7 and 6 descriptions to indicate they are in relation to the MAC. 20) Section 5.2.2.15, PHY Remote Loopback Register: Removed last sentence of bit 8 description. 21) Section 5.2.3.2, XMMII Port Control 1 Register: (a) Bit 6 description 1 and 0 definitions swapped. (b) Bit 2 definition updated to include RGMII. 22) Section 5.2.3.3, XMMII Port Control 3 Register: Added new register. 23) Section 5.2.4.1, Port MAC Control 0 Register: Made bit 0 reserved. 24) Table 6-11, Power-up and Reset Timing Values: Added row for trw

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Manufacturability

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 05 July 2019

**NOTE:** Please be advised that this is a change to the document only the product has not been



changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

**Attachment(s):**

[KSZ9893R Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

KSZ9893RNXC

KSZ9893RNXC-TR

KSZ9893RNXI

KSZ9893RNXI-TR