



General Description

The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W are intelligent, stand-alone constant-current, constant-voltage (CCCV), thermally regulated linear chargers designed for charging a single-cell lithium-ion (Li+) battery. The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W integrate a current-sense circuit, MOSFET pass element, thermal-regulation circuitry, and eliminate the external reverse-blocking Schottky diode to create the simplest and smallest charging solutions for handheld equipment.

The ICs control the charging sequence from the prequalification state through constant current fast-charge, top-off charge, and full-charge indication. Proprietary thermal-regulation circuitry limits the die temperature during fast-charging or when the ICs are exposed to high ambient temperatures, allowing maximum charging current without damaging the ICs.

The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W achieve high flexibility by providing adjustable fastcharge currents (SETI) and an adjustable top-off current threshold (MIN) through external resistors. The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W feature a booting assistant circuit that distinguishes input sources and battery connection and provides an enable signal (ABO-MAX8845Z and \overline{ABO} -MAX8845Y/MAX8845X/MAX8845W) for system booting.

The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W also integrate an overvoltage-protected output (SAFEOUT) for low voltage-rated USB or charger inputs in system, and a battery-pack detection circuit (DETBAT) that disables the charger when the battery pack is absent. Other features include an active-low control input (EN), an active-low input power source detection output (POK), and a fully charged top-off threshold detection output (CHG).

The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W accept an input supply range from 4.25V (4.4V for MAX8845W only) to 28V, but disable charging if the supply voltage exceeds +7.5V (+8.0V for MAX8845X only) to protect against unqualified or faulty AC adapters. The ICs operate over the extended temperature range (-40°C to +85°C) and are available in a compact 12-pin, thermally enhanced thin QFN, 3mm x 3mm package (0.8mm max height).

Applications

Cellular and Cordless Phones Smart Phones and PDAs Digital Still Cameras MP3 Players **USB** Appliances Charging Cradles and Docks Bluetooth® Equipment

Features

- ♦ CCCV, Thermally Regulated Linear 1-Cell Li+ **Battery Charger**
- ♦ No External MOSFET, Reverse Blocking Diode, or **Current-Sense Resistor**
- ♦ Programmable Fast-Charge Currents (1ARMS max)
- **♦** Programmable Top-Off Current Threshold (MIN)
- ♦ Input Overvoltage Protected 4.7V Output (MAX8845Z/MAX8845Y/MAX8845W) or 4.85V (MAX8845X) (SAFEOUT)
- **♦ Proprietary Die Temperature Regulation Control** (+115°C)
- ♦ 4.25V (4.4V for MAX8845W only) to 28V Input **Voltage Range with Input Overvoltage Protection** Above +7.5V (+8.0V for MAX8845X only)
- ♦ Low-Dropout Voltage (300mV at 500mA)
- ♦ Input Power-Source Detection Output (POK), Charge Status Output (CHG), Charge-Enable Input
- ♦ Output for Autobooting (ABO—MAX8845Z, ABO—MAX8845Y/MAX8845X/MAX8845W)
- ◆ Tiny, 3mm x 3mm 12-Pin Thin QFN Package, 0.8mm Height (max)

Ordering Information

PART	PIN-PACKAGE	N-PACKAGE TOP MARK	
MAX8845ZETC+	12 Thin QFN-EP*	ABL	Active high
MAX8845YETC+	12 Thin QFN-EP*	ABM	Active low
MAX8845XETC+	12 Thin QFN-EP*	ABQ	Active low
MAX8845WETC+	12 Thin QFN-EP*	ABR	Active low

Note: All devices are specified over the -40°C to +85°C operating temperature range.

+Denotes a lead(Pb)-free/RoHS-compliant package.

Typical Operating Circuit and Pin Configurations appear at end of data sheet.

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Maxim Integrated Products 1

^{*}EP = Exposed pad.

ABSOLUTE MAXIMUM RATINGS

IN to GNDABI, BATT, EN, POK, ABO, ABO, CHG, DETE	BAT, SETI, MIN,	BATT Short-Circuit Duration Operating Temperature Ran
SAFEOUT to GND	0.3V to +6V	Junction Temperature
IN to BATT Continuous Current	1A _{RMS}	Storage Temperature Range
Continuous Power Dissipation ($T_A = +70^{\circ}C$)		Lead Temperature (soldering
12-Thin QFN (derate 14.7mW/°C above +7	′0°C)	
(multilayer PCB)	1176.5mW	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{IN}=5V, V_{BATT}=4V, R_{\overline{POK}}=1M\Omega$ to BATT, \overline{EN} = unconnected, $R_{SETI}=2.8k\Omega$ to GND, $V_{DETBAT}=0$, $C_{BATT}=2.2\mu F$, $T_{A}=-40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted. Typical values are at $T_{A}=+25^{\circ}C$.) (Note 1)

PARAMETER	CONE	S	MIN	TYP	MAX	UNITS	
Input Supply Voltage Range				0		28	V
Input Supply Operating Voltage Range				4.25		7.00	v
input Supply Operating Voltage hange	MAX8845W only			4.40		7.00	V
Overvoltage Lockout Trip Threshold	V _{IN} rising, 100mV hys	teresis	s (typ)	7.0	7.5	8.0	v
Overvoitage Lockout Trip Trireshold	MAX8845X only			7.5	8.0	8.5	V
	Constant current char I _{IN} - I _{BATT} , I _{BATT} = 0	rging			0.22	0.50	
January Commons	Constant current char I _{IN} - I _{BATT} , I _{BATT} = 50				1		Λ
Input Current	IC disabled, $V_{\overline{EN}} = 5V_{\overline{EN}}$	/			0.20	0.50	mA
	$V_{IN} = 4V,$ $V_{BATT} = 4.2V (4.35V)$	for	T _A = +25°C		0.02		
	MAX8845W only)		0.03				
BATT, CHG, POK							
Minimum BATT Bypass Capacitance					2.2		μF
V _{BATT} Prequalification Threshold Voltage	VBATT rising, 100mV	nystere	esis (typ)	2.3	2.5	2.7	V
	I _{BATT} = 0	 T_	$T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$		4.200	4.225]
Battery Regulation Voltage	IDATT - 0	I'A -	40 0 10 105 0	4.158	4.200	4.242	<u> </u>
Dattery Regulation Voltage	MAX8845W only			4.324	4.350	4.376	· I
	IVII UNGO TOVV OTTIY			4.306	4.350	4.394	
Regulator Dropout Voltage (VIN - VBATT)	VBATT = 4.1V, IBATT =	425m	Α		260		mV
BATT Input Current	$V_{IN} = 0 \text{ to } 4V,$ $V_{BATT} = 4.2V (4.35V)$	for MA	X8845W only)		5	10	μΑ
	IC disabled				3		
Current-Sense Amplifier Gain (IBATT to ISETI)	I _{BATT} = 500mA				1016		μΑ/Α
	$V_{BATT} = 3.5V$,	$T_A = 0$	0°C to +85°C	460	500	540	
Fast-Charge Current	$R_{SETI} = 2.8k\Omega$	T _A = -	-40°C to +85°C	425	500 575		mA
rast-Charge Current	VDATT - 2 FV		0°C to +85°C	85	100	115	IIIA
CHG Top-Off Threshold	I_{BATT} falling, battery i $R_{MIN} = 1.75 k\Omega$	s char	ged		106		mA

ELECTRICAL CHARACTERISTICS (continued)

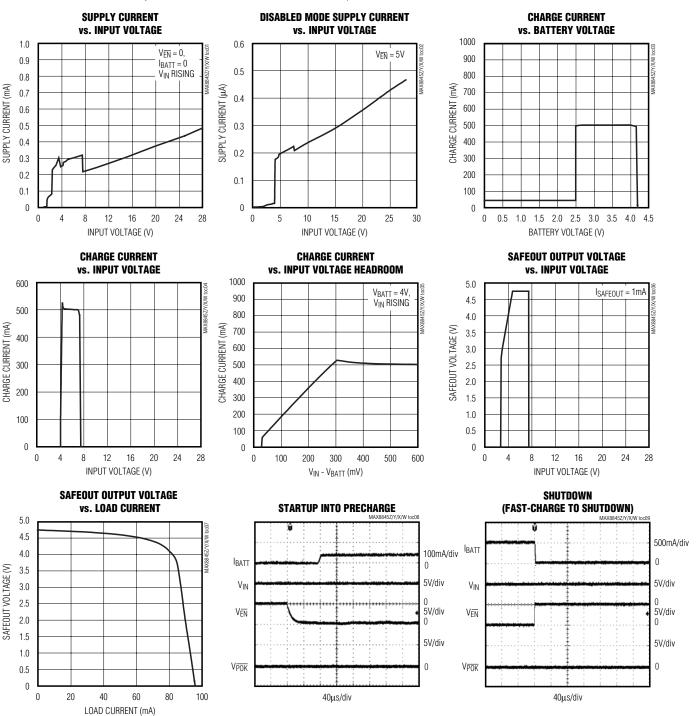
 $(V_{IN}=5V, V_{BATT}=4V, R_{\overline{POK}}=1M\Omega$ to BATT, $\overline{EN}=$ unconnected, $R_{SETI}=2.8k\Omega$ to GND, $V_{DETBAT}=0$, $C_{BATT}=2.2\mu F$, $T_{A}=-40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted. Typical values are at $T_{A}=+25^{\circ}C$.) (Note 1)

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
CHG Hysteresis	I_{BATT} rising after top-of $R_{MIN} = 1.75k\Omega$	I_{BATT} rising after top-off is detected, $R_{MIN}=1.75 k\Omega$				mA
CHG Detection Delay	IBATT falls below top-of	f threshold	4.0	6.2	10.7	ms
Prequalification Charge Current	Percentage of the fast- charge current, V _{BATT} = 2.2V	T _A = 0°C to +85°C	5	10	15	%
CHG, POK Output Low Threshold	IPOK = 5mA , ICHG = 5	mA			0.4	V
CHG, POK Output High Leakage Current	$V_{\overline{POK}} = 5.5V$,	$T_A = +25^{\circ}C$			1	μΑ
Crid, i Ok Output riigii Leakage Current	V CHG = 5.5V	T _A = +85°C		0.01		μΑ
POK Threshold	V _{IN} - V _{BATT}	V _{IN} rising		40		mV
T OK THIOSHOID	VIII VEATT	V _{IN} - V _{BATT}		30		1117
DETBAT, SAFEOUT			1			T
DETBAT Logic-Input Low Threshold			4.0		0.4	V
DETBAT Logic-Input High Threshold	DETENT: W OV		1.3	470		1.0
DETBAT Pullup Resistor	DETBAT to V _L = 3V			470		kΩ
Minimum SAFEOUT Bypass Capacitance	ISAFEOUT = 30mA, V _{IN} = 5V, T _A = 0°C to			1		μF
SAFEOUT Regulated Output	ISAFEOUT = 30mA, VIN +85°C (MAX8845Z/MA	4.5	4.7	4.9	V	
SAI LOOT Negulated Output	ISAFEOUT = 20mA, V _{IN} +85°C (MAX8845X)	I _{SAFEOUT} = 20mA, V _{IN} = 5V, T _A = 0°C to +85°C (MAX8845X)			5.0	V
SAFEOUT Current Limit				100		mA
EN, ABI, ABO, ABO			_			
EN, ABI Internal Pulldown Resistor			100	200	400	kΩ
EN Logic-Input Low Threshold	4.25V (4.4V for MAX884	$45W \text{ only}) \le V_{\text{IN}} \le 7V$			0.4	V
EN Logic-Input High Threshold	4.25V (4.4V for MAX884	$45W \text{ only}) \le V_{\text{IN}} \le 7V$	1.3			V
ABI Logic-Input Low Threshold	$V_{BATT} = 4V, V_{IN} = 0$				0.4	V
ABI Logic-Input High Threshold	$V_{BATT} = 4V, V_{IN} = 0$		1.3			V
ABO Output Low Threshold (MAX8845Y/ MAX8845X/MAX8845W)	Open drain, IABO(SINK)	Open drain, IABO(SINK) = 1mA			0.4	V
ABO Output High Threshold (MAX8845Y/ MAX8845X/MAX8845W)	Open drain, 100kΩ pullup to BATT		V _{BATT} - 0.4V			V
ABO Output Low Threshold (MAX8845Z)	I _{ABO} (SINK) = 1mA				0.4	V
ABO Output High Threshold (MAX8845Z)	IABO(SOURCE) = 1mA	V _{BATT} - 0.4V	_		V	
THERMAL	•					
Die Temperature Regulation Threshold				115		°C

Note 1: Specifications are 100% production tested at T_A = +25°C. Limits over the operating temperature range are guaranteed by design and characterization.

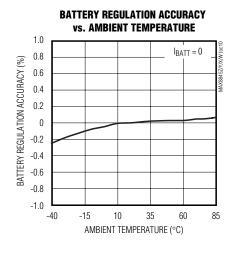
Typical Operating Characteristics

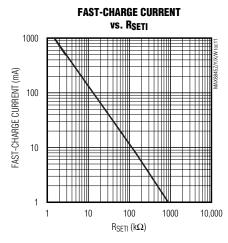
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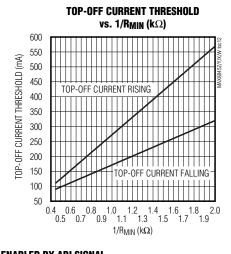


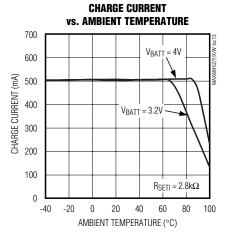
Typical Operating Characteristics (continued)

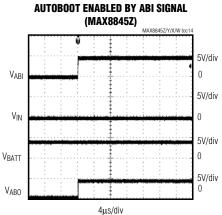
 $(V_{IN}=5V,V_{BATT}=4V,R_{\overline{POK}}=R_{\overline{CHG}}=200k\Omega$ to $5V,\overline{EN}=$ unconnected, $R_{SETI}=2.8k\Omega$ to GND, $R_{MIN}=1.74k\Omega$ to GND, $R_{DETBAT}=4.7k\Omega$ to GND, $R_{DETBAT}=4.7k\Omega$

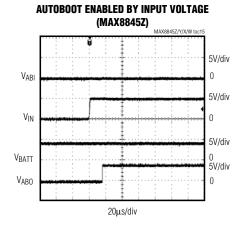


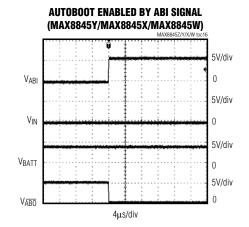












Pin Description

PIN								
MAX8845Z	MAX8845Y/ MAX8845X/ MAX8845W NAME		FUNCTION					
1	1	SETI	Charge-Current Program and Fast-Charge Current Monitor. Output current from SETI is 1016µA per ampere of battery charging current. Set the charging current by connecting a resistor (RSETI in Figure 3) from SETI to GND. IFAST-CHARGE = 1400V/RSETI. To configure the MAX8845Z/MAX8845Y/MAX8845X/MAX8845W as a USB charger, see Figure 5.					
2	2	DETBAT	Battery Pack ID Resistor Detection Input. If DETBAT is pulled low through a pulldown resistor less than $51 k\Omega$ the charger is enabled. If DETBAT is left unconnected, the charger is disabled.					
3	3	ABI	Autobooting External Input. See the <i>Autobooting Assistant</i> section and Table 1 for autobooting conditions. ABI is pulled to GND through an internal 200kΩ resistor.					
4	4	ĒN	Active-Low, Logic-Level Enable Input. Drive $\overline{\text{EN}}$ high to disable charger. Drive $\overline{\text{EN}}$ low or leave unconnected for normal operation. $\overline{\text{EN}}$ has an internal 200k Ω pulldown resistor.					
5	5	GND	Ground. Connect GND and the exposed pad to a large copper ground plane for maximum power dissipation. Connect GND to the exposed pad directly under the IC.					
6	6	POK	Active-Low, Input Voltage Status Indicator. \overline{POK} is an open-drain output that asserts low when 2.35V < V _{IN} < 7V (7.5V for MAX8845X only) and (V _{IN} - V _{BATT}) \geq 40mV. If V _{IN} > +7.5V (+8.0V for MAX8845X only) or V _{BATT} > V _{IN} the IC is shut down and \overline{POK} becomes high impedance. Connect a pullup resistor to the microprocessor's I/O voltage when interfacing with a microprocessor logic input.					
7	_	ABO	Active-High, Autobooting Logic Output. See the <i>Autobooting Assistant</i> section and Table 1 for autobooting conditions.					
_	7	ĀBO	Active-Low, Open-Drain Logic Output. See the <i>Autobooting Assistant</i> section and Table 1 for autobooting conditions.					
8	8	BATT	Li+ Battery Connection. Bypass BATT to GND with a 2.2µF ceramic capacitor.					
9	9	MIN	Top-Off Current Threshold Programmable Input. I _{MIN} (mA) falling = 148V/R _{MIN} (k Ω) + 22 (mA).					
10	10	СНG	Active-Low, Charging Indicator. $\overline{\text{CHG}}$ is an open-drain output that is pulled low once charging begins. $\overline{\text{CHG}}$ is high impedance when the battery current drops below MIN, or when the IC is disabled. Connect a pullup resistor to the microprocessor's I/O voltage when interfacing with a microprocessor logic input.					
11	11	IN	Input Supply Voltage. Bypass IN to GND with a 1µF or larger ceramic capacitor to improve line noise and input transient rejection.					
12	12	SAFEOUT	4.7V (MAX8845Z/MAX8845Y/MAX8845W) or 4.85V (MAX8845X) Regulated LDO Output with Input Overvoltage Protection. Bypass SAFEOUT to GND with a 1µF or larger ceramic capacitor. SAFEOUT can be used to supply low voltage-rated USB systems.					
_	_	EP	Exposed Pad. Connect the exposed pad to a large ground plane for maximum power dissipation. Connect GND to the exposed pad directly under the IC.					

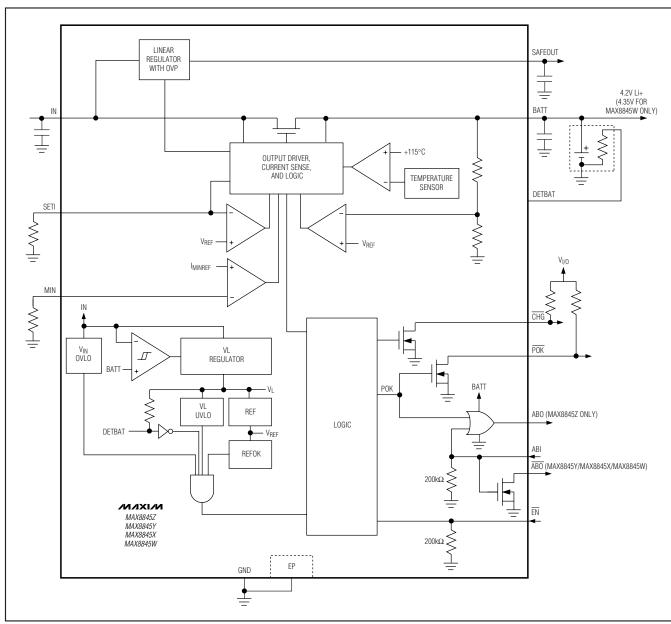


Figure 1. MAX8845Z/MAX8845Y/MAX8845X/MAX8845W Functional Diagram

Detailed Description

The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W chargers use voltage, current, and thermal-control loops to charge a single Li+ cell and protect the battery (Figure 1). When a Li+ battery with a cell voltage below 2.5V is inserted, the MAX8845Z/MAX8845Y/MAX8845X/MAX8845W chargers enter a prequalification stage where it precharges that cell with 10% of the user-pro-

grammed fast-charge current (Figure 2). The CHG indicator is driven low to indicate entry into the prequalification state. When the battery voltage exceeds 2.5V, the charger soft-starts as it enters the fast-charge stage. The fast-charge current level is programmed through a resistor from SETI to GND. As the battery voltage approaches 4.2V, the charging current is reduced. If the battery current drops to less than the

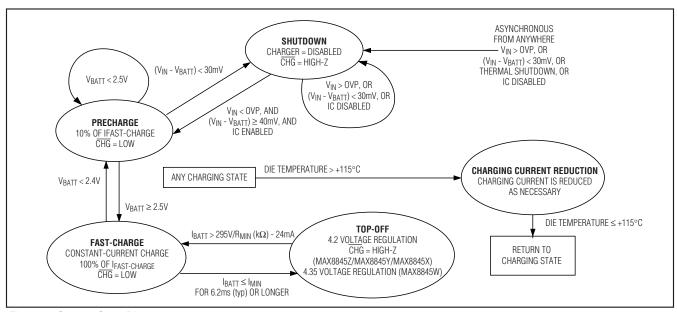


Figure 2. Charge-State Diagram

top-off current threshold set by R_{MIN} , the charger enters top-off mode and the \overline{CHG} indicator goes high impedance signaling that the battery is fully charged.

Overvoltage-Protected Output (SAFEOUT) SAFEOUT is a linear regulator that provides an output voltage of 4.7V (MAX8845Z/MAX8845Y/MAX8845W) or 4.85V (MAX8845X) and can be used to supply low voltage- rated USB systems. The SAFEOUT linear regulator turns on when $V_{IN} \ge 4.25V$ regardless of \overline{EN} and is disabled when V_{IN} is greater than the overvoltage threshold (7.5V typ) (8.0V typ for MAX8845X only).

Battery-Pack Detection Input (DETBAT) DETBAT is a battery-pack ID resistor detector that enables the battery charger if pulled low through a resistor that is less than $51k\Omega$. If DETBAT is left unconnected or the pulldown resistor is $51k\Omega$ or greater the battery charger is disabled. If DETBAT is not used connect DETBAT to GND for normal operation.

POK Output

The open-drain \overline{POK} output asserts low when 2.35V \leq V_{IN} \leq 7V (7.5V for MAX8845X only) and (V_{IN} - V_{BATT}) \geq 40mV (typ V_{IN} rising). \overline{POK} is high impedance during shutdown. When interfacing with a microprocessor logic input, a pullup resistor to the microprocessor's I/O voltage may be required.

Autobooting Assistant

The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W contain autobooting assistant circuits that generate an enable signal for system booting (ABO—MAX8845Z, ABO—MAX8845Y/MAX8845X/MAX8845W). For the MAX8845Z, the booting assistant functions as an internal OR gate (Figure 1). The first input is dependant on the input voltage (V_{IN}) and DETBAT while the second input is an external signal applied to ABI. The first input (POK) is driven high once DETBAT is pulled low through a resistor less than $51k\Omega$, $2.35V \le V_{IN} \le 7V$ (7.5V for only MAX8845X), and (V_{IN} - V_{BATT}) ≥ 40 mV (typ V_{IN} rising).

The second input signal (ABI) is driven by an external source (Table 1). ABI enables an autoboot signal when a battery is connected at BATT and is independent of $\overline{\text{POK}}$. If $\overline{\text{POK}}$ is pulled low, the booting assistant always drives ABO high regardless of ABI. ABI is pulled to GND through an internal 200k Ω resistor. If ABI is supplied from an outside exposed pin, a RC filter (Figure 4) is required for ESD protection and noise filtering. If ABI is supplied by a system's internal GPIO, or logic, the RC filter is not required. For the MAX8845Y/MAX8845X/ MAX8845W, the output $\overline{\text{ABO}}$ is only dependent on the state of ABI (Table 1).

CHG Charge Indicator Output

CHG is an open-drain output that indicates charge status. Table 2 describes the state of CHG during different stages of operation. CHG is suitable for driving a charge indication LED. If the MAX8845Z/MAX8845Y/MAX8845X/

8 /N/XI/M

Table 1. Autobooting Output States

ABI	BATT	РОК	CHARGER STATE	ABO (MAX8845Z)	ABO (MAX8845Y/ MAX8845X/MAX8845W)
Low	Present	High-Z	Shutdown	Low	High-Z
High	Present	High-Z	Shutdown	High	Low
Low	Not present	Low	CC/CV mode	High	High-Z
Low	Present	Low	Fast-charge/top-off	High	High-Z
High	Present	Low	Fast-charge/top-off	High	Low

Note: Present indicates that $V_{BATT} \ge 2V$ and Not Present indicates that the battery is not connected.

Table 2. CHG States

ĒN	V _{IN}	V _{BATT}	IBATT	CHG	STATE
High	X	Χ	0	High-Z	Disabled
Low	> 7.5V	X	0	Lligh 7	Chutdown
Low	X	> V _{IN} - 30mV	U	High-Z	Shutdown
Low	4.25V (4.4V for MAX8845W only) ≤ V _{IN} ≤ 7.5V	< 2.4V	10% of IFAST-CHARGE*	Low	Precharge
Low	4.25V (4.4V for MAX8845W only) ≤ V _{IN} ≤ 7.5V	≥ 2.5V	100% of IFAST-CHARGE*	Low	Fast-charge
Low	4.25V (4.4V for MAX8845W only) ≤ V _{IN} ≤ 7.5V	4.2V (4.35V for MAX8845W only)	< I _{MIN}	High-Z	Top-off

X = Don't care.

MAX8845W are used in conjunction with a microprocessor, a pullup resistor to the logic I/O voltage allows CHG to indicate charge status to the microprocessor instead of driving an LED.

Thermal Regulation

The thermal-regulation loop limits the MAX8845Z/MAX8845Y/MAX8845X/MAX8845W die temperature to +115°C by reducing the charge current as necessary. This feature not only protects the IC from overheating, but also allows a higher charge current without risking damage to the system.

Charger Enable Input

The MAX8845Z/MAX8845Y/MAX8845W contain active-low logic input $\overline{(EN)}$ used to enable the chargers. Drive \overline{EN} low, leave unconnected, or connect to GND to enable the charge-control circuitry. Drive \overline{EN} high to disable the charger-control circuitry. \overline{EN} has an internal 200k Ω pulldown resistor.

Soft-Start

The soft-start algorithm activates when entering fast-charge mode. When the prequalification state is complete (VBATT exceeds +2.5V), the charging current ramps up in 250µs to the full charging current. This reduces the inrush current demand on the input supply.

_Applications Information

Fast Charge-Current Setting

The maximum charging current is programmed by an external resistor connected from SETI to GND (R_{SETI}). Use the following equation to determine the fast-charge current (IFAST_CHARGE):

$$I_{\text{FAST_CHARGE}} = \frac{1400 \text{V}}{R_{\text{SETI}}}$$

where IFAST_CHARGE is in amps and RSETI is in ohms. RSETI must always be $1.4k\Omega$ or higher due to the continuous charging current limit of 1ARMS.

Top-Off Current Threshold Setting

The top-off current threshold is programmed by an external resistor connected from MIN to GND (R_{MIN}). Use the following equation to determine the top-off current (I_{MIN}):

$$I_{MIN}$$
 (falling) = 148V/R_{MIN} (k Ω) + 22mA
 I_{MIN} (rising) = 295V/R_{MIN} (k Ω) - 24mA

where I_{MIN} is in mA and R_{MIN} is in k Ω . Use R_{MIN} \leq 2.2k Ω .

^{*}IFAST-CHARGE is reduced as necessary to prevent the die temperature from exceeding +115°C.

Capacitor Selection

Connect a ceramic capacitor from BATT to GND for proper stability. Use a 2.2 μ F ceramic capacitor for most applications. Connect a 1 μ F ceramic capacitor from IN to GND. A larger input capacitor can be used for high charging current to reduce input voltage ripple.

Connect a $1\mu F$ ceramic capacitor from SAFEOUT to GND. A larger bypass capacitor for SAFEOUT can be used for optimum noise immunity. Ceramic capacitors with X5R or X7R dielectric are highly recommended due to their small size, low ESR, and small temperature coefficients.

Thermal Considerations

The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W are available in thermally enhanced Thin QFN packages with exposed pads. Connect the exposed pad to a large copper ground plane to provide a thermal contact between the device and the circuit board for increased power dissipation. The exposed pad transfers heat away from the device, allowing the IC to charge the battery with maximum current, while minimizing the increase in die temperature.

DC Input Sources

The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W operate from well-regulated DC sources. The full charging input voltage range is 4.25V (4.4V for MAX8845W only) to 7.5V (8V for MAX8845X only). The device can withstand up to 28V on the input without damage to the IC. If VIN is greater than 7.5V (8V for MAX8845X only), the internal overvoltage-protection circuitry disables charging until the input falls below 7.5V (8V for MAX8845X only). An appropriate power supply must provide at least 4.25V at the desired peak charging current and stay below 7V when unloaded.

Typical Application Circuits

AC Adapter Application

Figure 3 shows the MAX8845Z as a Li+ battery charger with an AC adapter. The MAX8845Z detects the presence of an input supply and DETBET, resulting in POK pulled low. Once POK is pulled low, the autobooting assistant drives ABO high (MAX8845Z) and enables the power

supplies of the system to boot up. The MAX8845Z begins charging the battery when $\overline{\text{EN}}$ is low or unconnected. By monitoring $\overline{\text{CHG}}$, the system can detect the top-off threshold and terminate the charge through $\overline{\text{EN}}$. The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W also provide an overvoltage-protected SAFEOUT to the system.

Factory System Interface Connector Application

Figure 4 shows the MAX8845Z as an autoboot assistor with the factory system interface connector. The MAX8845Z detects the ABI input even though there is no input voltage available and generates an ABO signal to turn on power supplies to boot up the system. The configuration in Figure 4 is used for system development, testing, and calibrations in production or design stage.

USB-Powered Li-Ion Charger

The universal serial bus (USB) provides a high-speed serial communication port as well as power for the remote device. The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W can be configured to charge batteries at the highest current possible from the host port. Figure 5 shows the MAX8845Z as a USB battery charger. To make the circuit compatible with either 100mA or 500mA USB ports, the circuit initializes at 100mA charging current. The microprocessor then enumerates the host to determine its current capability. If the host port is capable, the charging current is increased to 450mA to avoid exceeding the 500mA USB specification through GPIO control. The MAX8845Z/MAX8845Y/MAX8845X/MAX8845W also provide an overvoltage-protected SAFEOUT to the system.

Recommended PCB Layout and Routing

Place all bypass capacitors for IN, BATT, and SAFEOUT as close as possible to the device. Connect the battery to BATT as close as possible to the device to provide accurate battery voltage sensing. Provide a large copper ground plane to allow the exposed pad to sink heat away from the device. Make all high-current traces short and wide to minimize voltage drops. A sample layout is available in the MAX8845Z Evaluation Kit to speed designs.

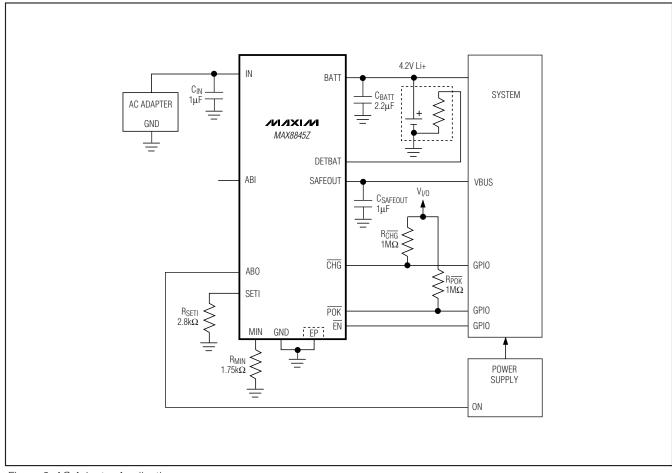


Figure 3. AC Adapter Application

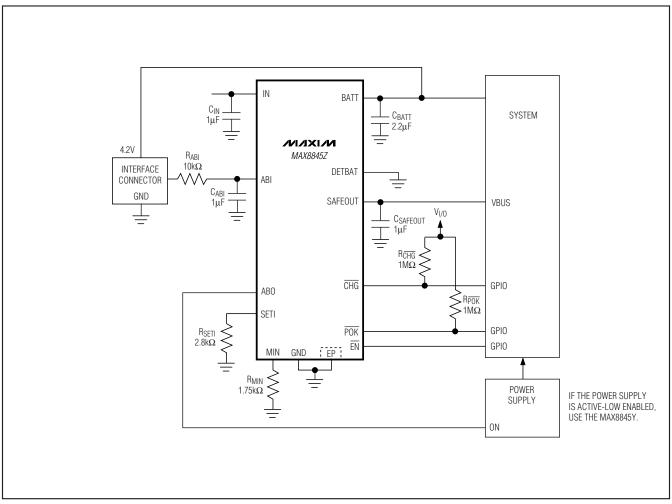


Figure 4. Factory System Interface Connector Application

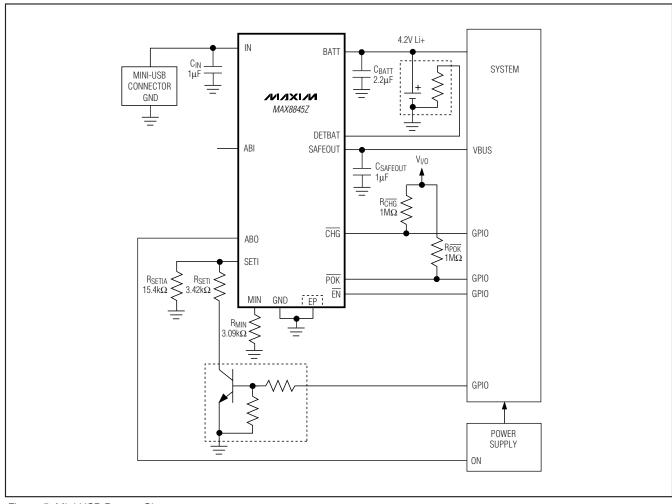


Figure 5. Mini USB Battery Charger

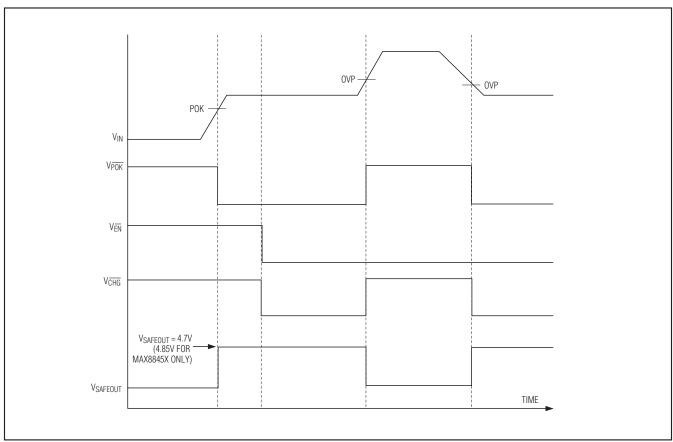
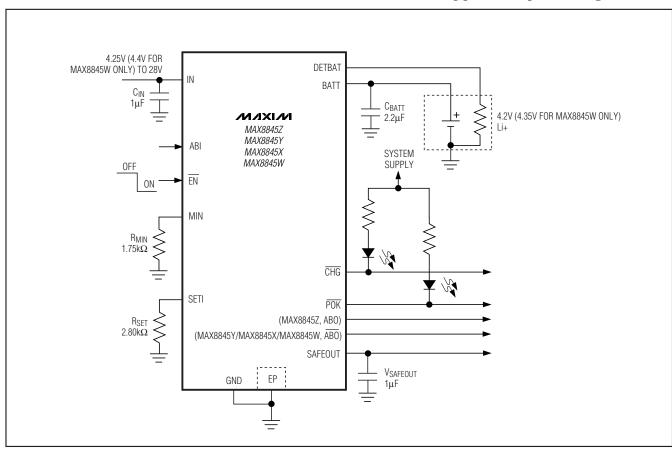


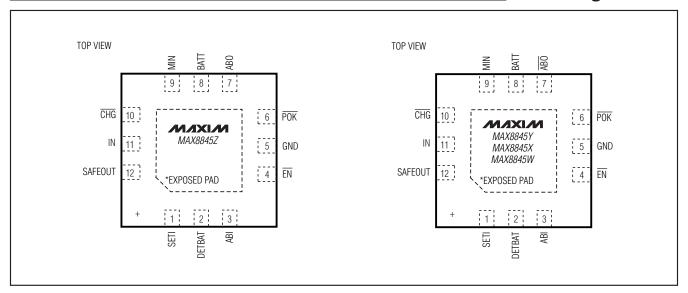
Figure 6. Timing Diagram

14 ______ *NIXIN*

Typical Operating Circuit



Pin Configurations



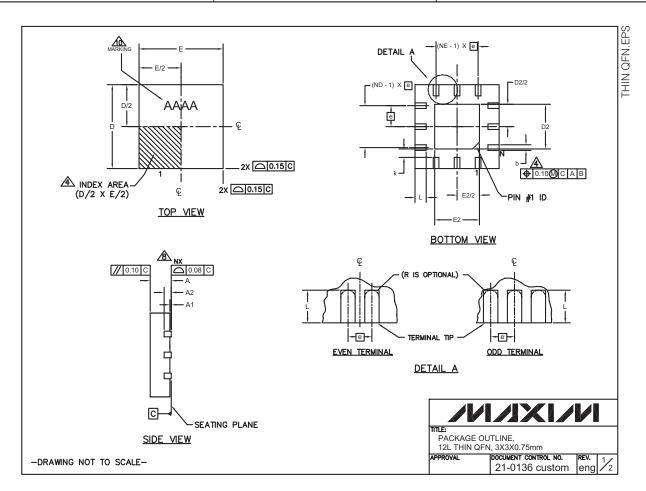
_____Chip Information

PROCESS: BiCMOS

Package Information

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
12 Thin QFN-EP (3mm x 3mm)	T1233-4	_



Package Information (continued)

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages.

PKG	1	3	
REF.	MIN.	NOM.	MAX.
Α	0.70	0.75	0.80
b	0.20	0.25	0.30
D	2.90	3.00	3.10
Ε	2.90	3.00	3.10
е	0	.50 BS	C.
L	0.45	0.55	0.65
N		12	
ND		3	
NE			
A1	0	0.02	0.05
A2	C	F	
k	0.25	-	-

EXPOSED PAD VARIATIONS								
PKG.	D2			E2			PIN ID	JEDEC
CODES	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	PINID	JEDEC
T1233-1	0.95	1.10	1.25	0.95	1.10	1.25	0.35 x 45°	WEED-1
T1233-3	0.95	1.10	1.25	0.95	1.10	1.25	0.35 x 45°	WEED-1
T1233-4	0.95	1.10	1.25	0.95	1.10	1.25	0.35 x 45°	WEED-1

- 1. DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994
- 2. ALL DIMENSIONS ARE IN MILLIMETERS. ANGLES ARE IN DEGREES.
- N IS THE TOTAL NUMBER OF TERMINALS.
- 1 THE TERMINAL #1 IDENTIFIER AND TERMINAL NUMBERING CONVENTION SHALL CONFORM TO JESD 95-1 SPP-012. DETAILS OF TERMINAL #1 IDENTIFIER ARE OPTIONAL, BUT MUST BE LOCATED WITHIN THE ZONE INDICATED. THE TERMINAL #1 IDENTIFIER MAY BE EITHER A MOLD OR
- 🟡 DIMENSION b APPLIES TO METALLIZED TERMINAL AND IS MEASURED BETWEEN 0.20 mm AND 0.25 mm FROM TERMINAL TIP.
- 6 ND AND NE REFER TO THE NUMBER OF TERMINALS ON EACH D AND E SIDE RESPECTIVELY.
- DEPOPULATION IS POSSIBLE IN A SYMMETRICAL FASHION.
- ⚠ COPLANARITY APPLIES TO THE EXPOSED HEAT SINK SLUG AS WELL AS THE TERMINALS. DRAWING CONFORMS TO JEDEC MO220 REVISION C
- 9. Drawing conforms to jedec mozzo revision c.

 MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.
- 11. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
- 12. WARPAGE NOT TO EXCEED 0.10mm.

PACKAGE OUTLINE 12L THIN QFN, 3X3X0.75mm DOCUMENT CONTROL NO.

21-0136 custom

-DRAWING NOT TO SCALE-

MIXIM

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	3/09	Initial release	_
1	9/09	Corrected the \overline{POK} Output section description and added MAX8845X and MAX8845W to data sheet	1–19

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