# M1MA141KT1G, M1MA142KT1G

# Single Silicon Switching Diode

This Silicon Epitaxial Planar Diode is designed for use in ultra high speed switching applications. This device is housed in the SC-70 package which is designed for low power surface mount applications.

#### **Features**

- Fast  $t_{rr}$ , < 3.0 ns
- Low  $C_D$ , < 2.0 pF
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

# **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit	
Reverse Voltage	M1MA141KT1 M1MA142KT1	V <sub>R</sub>	40 80	Vdc
Peak Reverse Voltage	M1MA141KT1 M1MA142KT1	V <sub>RM</sub>	40 80	Vdc
Forward Current		IF	100	mAdc
Peak Forward Current		I <sub>FM</sub>	225	mAdc
Peak Forward Surge Cur	rent	I <sub>FSM</sub> (Note 1)	500	mAdc

# THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation	P <sub>D</sub>	150	mW
Junction Temperature	TJ	150	°C
Storage Temperature	T <sub>stg</sub>	-55 ~ +150	°C

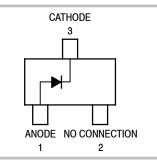
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. t = 1 sec



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SC-70 (SOT-323) CASE 419 STYLE 2

#### **MARKING DIAGRAM**



Mx = Device Code x = H for 141 I for 142

M = Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)
\*Date Code orientation may vary depending upon manufacturing location.

# **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
M1MA141KT1G	SC-70 (Pb-Free)	3000/Tape & Reel
M1MA142KT1G	SC-70 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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# M1MA141KT1G, M1MA142KT1G

# **ELECTRICAL CHARACTERISTICS** $(T_A = 25^{\circ}C)$

Characteristic		Condition	Symbol	Min	Max	Unit
Reverse Voltage Leakage Current	M1MA141KT1 M1MA142KT1	V <sub>R</sub> = 35 V V <sub>R</sub> = 75 V	I <sub>R</sub>	-	0.1	μAdc
Forward Voltage		I <sub>F</sub> = 100 mA	V <sub>F</sub>	-	1.2	Vdc
Reverse Breakdown Voltage	M1MA141KT1 M1MA142KT1	I <sub>R</sub> = 100 μA	V <sub>R</sub>	40 80	-	Vdc
Diode Capacitance		V <sub>R</sub> = 0, f = 1.0 MHz	C <sub>D</sub>	-	2.0	pF
Reverse Recovery Time (Figure 1)		$I_F$ = 10 mA, $V_R$ = 6.0 V, $R_L$ = 100 $\Omega$ , $I_{rr}$ = 0.1 $I_R$	t <sub>rr</sub> (Note 2)	1	3.0	ns

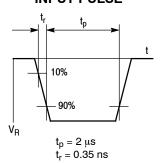
<sup>2.</sup> t<sub>rr</sub> Test Circuit

# M1MA141KT1G, M1MA142KT1G

# RECOVERY TIME EQUIVALENT TEST CIRCUIT

# A RL

# **INPUT PULSE**



# **OUTPUT PULSE**

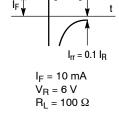
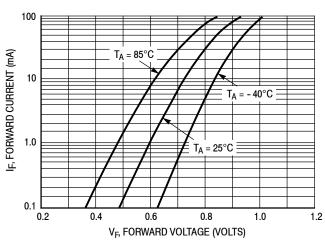


Figure 1. Recovery Time Equivalent Test Circuit



1.0

T<sub>A</sub> = 150°C

T<sub>A</sub> = 125°C

1.0

T<sub>A</sub> = 85°C

T<sub>A</sub> = 85°C

0.01

T<sub>A</sub> = 55°C

0.001

T<sub>A</sub> = 25°C

V<sub>B</sub>, REVERSE VOLTAGE (VOLTS)

Figure 2. Forward Voltage

Figure 3. Reverse Current

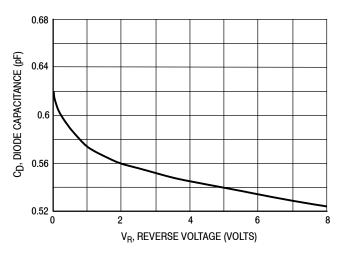


Figure 4. Diode Capacitance





SC-70 (SOT-323) **CASE 419** ISSUE R

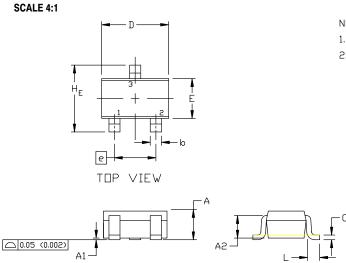
END VIEW

**DATE 11 OCT 2022** 

#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH

	MILLIMETERS				INCHES	
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.70 REF			0.028 BSC		
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.00	2.20	0.071	0.080	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
е	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC				0.026 BS	C
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095



# **GENERIC MARKING DIAGRAM**

SIDE VIEW

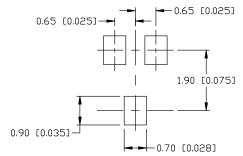


= Specific Device Code XX

Μ = Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.



For additional information on our Pb-Free strategy and soldering details, please download the ID Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

SOLDERING FOOTPRINT

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6: PIN 1. EMITTER	STYLE 7: PIN 1. BASE	STYLE 8: PIN 1. GATE	STYLE 9: PIN 1. ANODE	STYLE 10: PIN 1. CATHODE	STYLE 11: PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	2. CATHODE
<ol><li>COLLECTOR</li></ol>	<ol><li>COLLECTOR</li></ol>	3. DRAIN	<ol><li>CATHODE-ANODE</li></ol>	3. ANODE-CATHODE	<ol><li>CATHODE</li></ol>

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