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Vishay Siliconix

Automotive N-Channel 150 V (D-S) 175 °C MOSFET



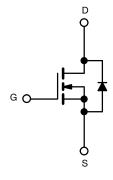
PRODUCT SUMMARY					
V _{DS} (V)	150				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.085				
I _D (A)	19				
Configuration	Single				
Package	SO-8				

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



ROHS COMPLIANT HALOGEN FREE



N-Channel MOSFET

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4080EY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATING	12 (16 =1 2, 0			11117
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	150	V
Gate-source voltage		V _{GS}	± 20	V
Continuous drain current	T _C = 25 °C	- I _D	18	
	T _C = 125 °C		10	
Continuous source current (diode conduction)		I _S	6.5	Α
Pulsed drain current ^a		I _{DM}	72	
Single pulse avalanche current	L = 0.1 mH	I _{AS}	20	
Single pulse avalanche energy	L = U. I IIII	E _{AS}	20	mJ
Maximum power dissipation ^a	T _C = 25 °C	P _D	7.1	W
	T _C = 125 °C	rD	2.4	VV
Operating junction and storage temperature range		T _J , T _{stq}	-55 to +175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction to ambient	PCB mount b	R_{thJA}	85	°C/W	
Junction to case (drain)		R_{thJF}	25	C/VV	

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		150	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		3	4	V
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		V _{GS} = 0 V	V _{DS} = 150 V	-	-	1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 150 V, T _J = 125 °C	-	-	50	μΑ
		V _{GS} = 0 V	V _{DS} = 150 V, T _J = 175 °C	-	-	150	1
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	30	-	-	Α
		V _{GS} = 10 V	I _D = 10 A	-	0.070	0.085	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 10 A, T _C = 125 °C	-	-	0.167	
		V _{GS} = 10 V	I _D = 10 A, T _C = 175 °C	-	-	0.220	
Forward Transconductance b	9 _{fs}	V _{DS}	V _{DS} = 15 V, I _D = 10 A		51	-	S
Dynamic ^b							
Input Capacitance	C _{iss}			-	1100	1590	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = 75 \text{ V}, f = 1 \text{ MHz}$	-	75	130	рF
Reverse Transfer Capacitance	C _{rss}			-	33	55	
Total Gate Charge ^c	Qg			-	25	33	
Gate-Source Charge c	Q_{gs}	$V_{GS} = 10 \text{ V}$	$V_{DS} = 75 \text{ V}, I_{D} = 5 \text{ A}$	-	5.2	-	nC
Gate-Drain Charge ^c	Q_{gd}			-	8.7	-	
Gate Resistance	R_g	f = 1 MHz		0.2	0.4	1	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	11.4	15	
Rise Time ^c	t _r	$V_{DD} = 20 \text{ V}, \text{ R}_L = 20 \Omega$ $I_D \cong 1 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_g = 6 \Omega$		-	3.2	4.5	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	19.1	25	
Fall Time ^c	t _f			-	2.5	3.2	
Source-Drain Diode Ratings and Chara	octeristics ^b						
Pulsed Current ^a	I _{SM}			-	-	72	Α
Forward Voltage	V _{SD}	$I_F = 3.5 \text{ A}, V_{GS} = 0$		-	0.75	1.1	V

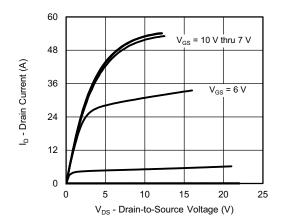
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,\,duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

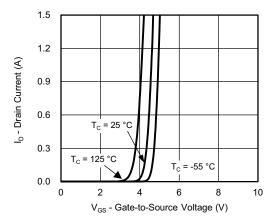
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



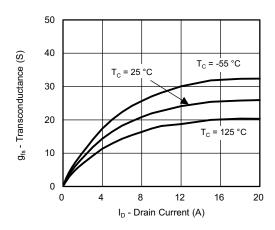
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



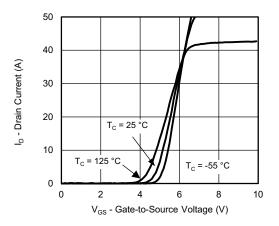
Output Characteristics



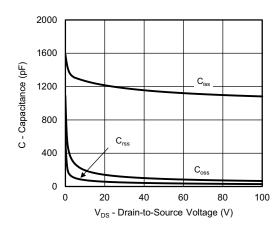
Transfer Characteristics



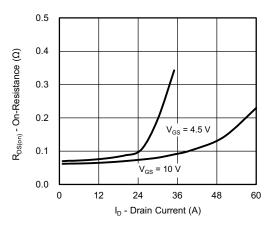
Transconductance



Transfer Characteristics



Capacitance

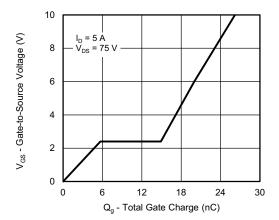


On-Resistance vs. Drain Current

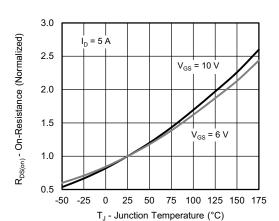
For technical questions, contact: automostech



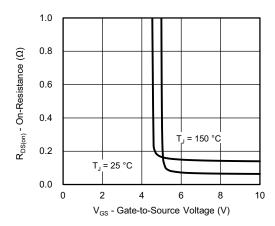
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



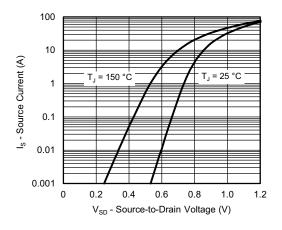
Gate Charge



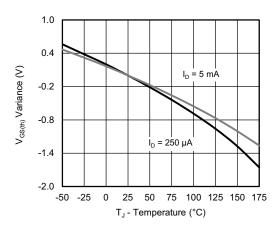
On-Resistance vs. Junction Temperature



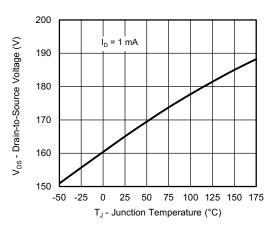
On-Resistance vs. Gate-to-Source Voltage



Source Drain Diode Forward Voltage

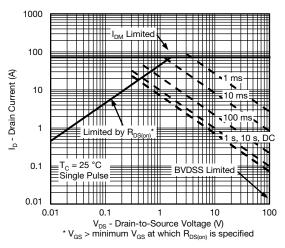


Threshold Voltage

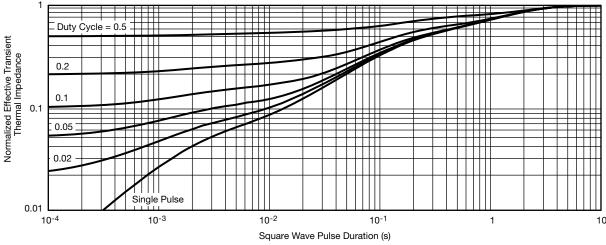


Drain Source Breakdown vs. Junction Temperature

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

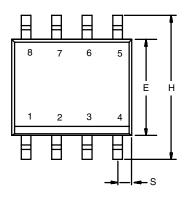
Note

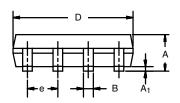
- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

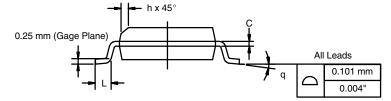
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71158.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







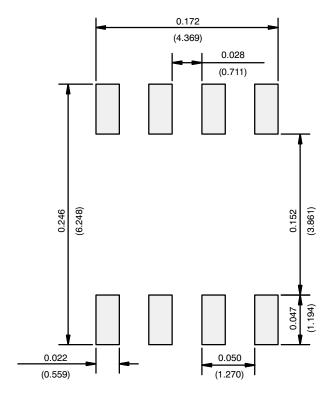
	MILLIM	IETERS	INC	HES	
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
Е	3.80	4.00	0.150	0.157	
е	1.27	BSC	0.050) BSC	
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I. 11-Sep-06					

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

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