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NTE573, NTE573-1, NTE573-2 Schottky Barrier Rectifier DO-201AD Type Package

Features:

- Schottky Barrier Chip
- Guard Ring for Transient and ESD Protection
- Surge Overload Rating to 150A Peak
- Low power Loss, High Efficiency
- Ideally Suited for Use in High Frequency SMPS, Inverters, and as Free Wheeling Diodes

Maximum Ratings and Electrical Characteristics: ($T_A = +25^\circ\text{C}$ unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)
Peak Repetitive Reverse Voltage, V_{RRM}

| | |
|----------------|------|
| NTE573 | 60V |
| NTE573-1 | 100V |
| NTE573-2 | 200V |

Working Peak Reverse Voltage, V_{RWM}

| | |
|----------------|------|
| NTE573 | 60V |
| NTE573-1 | 100V |
| NTE573-2 | 200V |

DC Blocking Voltage, V_R

| | |
|----------------|------|
| NTE573 | 60V |
| NTE573-1 | 100V |
| NTE573-2 | 200V |

RMS Reverse Voltage, $V_{R(RMS)}$

| | |
|----------------|------|
| NTE573 | 42V |
| NTE573-1 | 70V |
| NTE573-2 | 140V |

Average Forward Rectified Current (Note 1), I_O

Non-Repetitive Peak Forward Surge Current, I_{FSM}
(8.3ms Single half Sine-Wave Superimposed on Rated Load)

150A

Forward Voltage ($I_F = 5\text{A}$), V_{FM}

| | |
|----------------|-------|
| NTE573 | 0.7V |
| NTE573-1 | 0.85V |
| NTE573-2 | 0.9V |



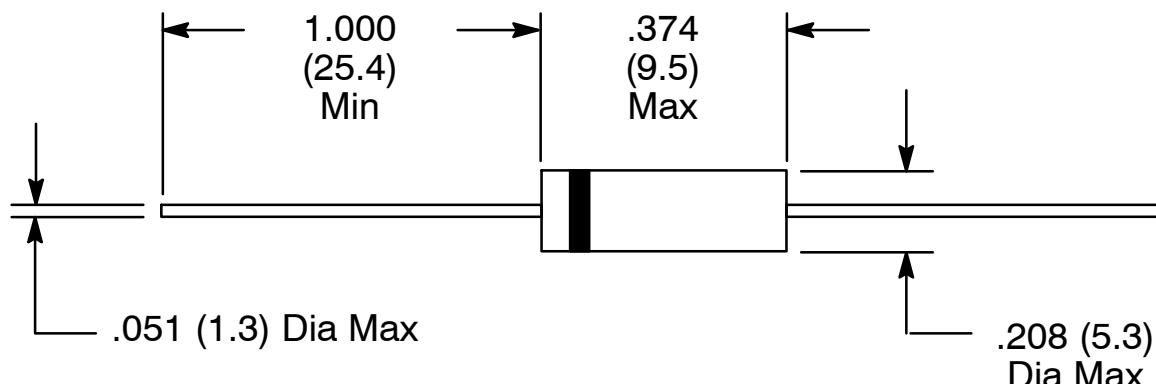
Maximum Ratings and Electrical Characteristics (Cont'd): ($T_A = +25^\circ\text{C}$ unless otherwise specified.
Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)
Peak Reverse Current (At rated DC Blocking Voltage), I_{RM}

| | | |
|--|-------|----------------|
| $T_J = +25^\circ\text{C}$ | | |
| NTE573 | | 5mA |
| NTE573-1 | | 0.5mA |
| NTE573-2 | | 0.2mA |
| $T_J = +100^\circ\text{C}$ | | |
| NTE573 | | 50mA |
| NTE573-1 | | 20mA |
| NTE573-2 | | 5mA |
| Typical Junction Capacitance (Note 2), C_J | | |
| NTE573 | | 400pF |
| NTE573-1 | | 380pF |
| NTE573-2 | | 120pF |
| Thermal Resistance, Junction-to-Ambient (Note 3), R_{thJA} | | 25°C/W |
| Thermal Resistance, Junction-to-Lead (Note 3), R_{thJL} | | 8.0°C/W |
| Operating Junction Temperature Range, T_J | | -65° to +150°C |
| Storage Temperature Range, T_{stg} | | -65° to +150°C |

Note 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case.

Note 2. Measured at 1.0MHz and applied reverse voltage of 4.0VDC.

Note 3. Vertical PCB mounting with 12.7mm lead length on 63.5mm x 63.5mm copper pad.



Color Band Denotes Cathode