

#### 30V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> max	I <sub>D</sub> max T <sub>A</sub> = 25°C (Notes 4 & 7)		
201/	120mΩ @ V <sub>GS</sub> = 10V	3.7A		
30V	180mΩ @ V <sub>GS</sub> = 4.5V	3.0A		

### **Description and Applications**

This MOSFET has been designed to minimize the on-state resistance  $(R_{DS(on)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power management functions
- Disconnect switches
- Portable applications

#### **Features and Benefits**

- Low profile package, for thin applications
- Low R<sub>0JA</sub>, thermally efficient package
- 6mm<sup>2</sup> footprint, 50% smaller than TSOP6 and SOT23-6
- Low on-resistance
- Fast switching speed
- "Lead-Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

**D1** 

**S1** 

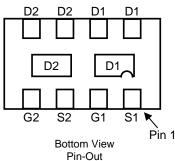
#### **Mechanical Data**

- Case: DFN3020B-8
- Terminals: Pre-Plated NiPdAu leadframe
- Nominal package height: 0.8mm
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Solderable per MIL-STD-202, Method 208
- Weight: 0.013 grams (approximate)

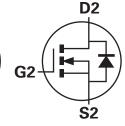


Top View

Bottom View







Equivalent Circuit

### **Ordering Information (Note 3)**

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN3AMCTA	DNB	7	8	3000

Notes:

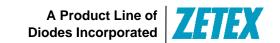
- 1. No purposefully added lead
- Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

### **Marking Information**



DNB = Product Type Marking Code Top View, Dot Denotes Pin 1





## 

Characteristic			Symbol	Value	Unit	
Drain-Source Voltage			$V_{DSS}$	30		
Gate-Source Voltage			$V_{GSS}$	±20	V	
		(Notes 4 & 7)		3.7		
Continuous Drain Current	$V_{GS} = 10V$	T <sub>A</sub> = 70°C (Notes 4 & 7)	I <sub>D</sub>	3.0		
		(Notes 3 & 7)		2.9	•	
Pulsed Drain Current	$V_{GS} = 10V$	(Notes 6 & 7)	I <sub>DM</sub>	13	А	
Continuous Source Current (Body diode) (Notes 4 & 7)		(Notes 4 & 7)	I <sub>S</sub>	3.2		
Pulse Source Current (Body diode) (Notes 6 & 7)		I <sub>SM</sub>	13			

### **Thermal Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	:	Symbol	Value	Unit
Power Dissipation Linear Derating Factor	(Notes 3 & 7)		1.50 12	
	(Notes 4 & 7)		2.45 19.6	W
	(Notes 5 & 7)	P <sub>D</sub>	1.13 9	mW/°C
	(Notes 5 & 8)		1.70 13.6	
Thermal Resistance, Junction to Ambient	(Notes 3 & 7)		83.3	
	(Notes 4 & 7)		51.0	1
	(Notes 5 & 7)	R <sub>0JA</sub>	111	°C/W
	(Notes 5 & 8)		73.5	
Thermal Resistance, Junction to Lead	(Notes 7 & 9)	$R_{\theta JL}$	17.1	1
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

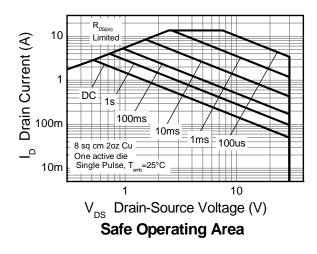
- 3. For a device surface mounted on 28mm x 28mm (8cm²) FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition. The heatsink is split in half with the exposed drain pads connected to each half.
- 4. Same as note (3) except the device is measured at t < 5 sec.
- 5. Same as note (3), except the device is surface mounted on 31mm x 31mm (10cm²) FR4 PCB with high coverage of single sided 1oz copper.
- 6. Same as note (3), except the device is pulsed with D = 0.02 and pulse width 300µs. The pulse current is limited by the maximum junction temperature. 7. For a dual device with one active die.

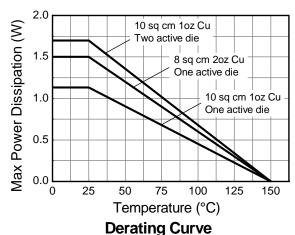
- 8. For dual device with 2 active die running at equal power.

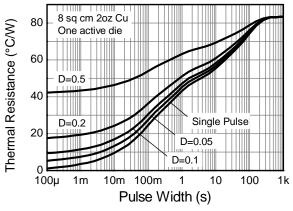
  9. Thermal resistance from junction to solder-point (at the end of the drain lead).

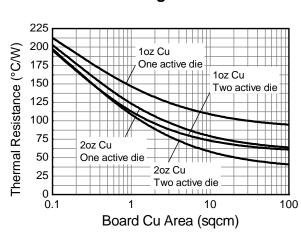


#### **Thermal Characteristics**

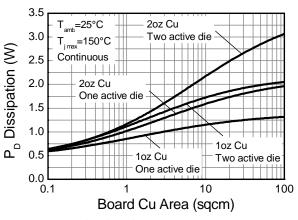








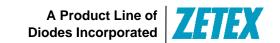
**Transient Thermal Impedance** 



Thermal Resistance v Board Area

Power Dissipation v Board Area





### Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test C	Condition
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	-	-	V	$I_D = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	0.5	μΑ	$V_{DS} = 30V, V_{G}$	s = 0V
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 20V, V$	DS = 0V
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	-	3.0	V	$I_D = 250 \mu A, V_D$	s = V <sub>G</sub> s
Static Drain-Source On-Resistance (Note 10)	В		0.100	0.120	Ω	$V_{GS} = 10V, I_D = 2.5A$	
Static Drain-Source On-Resistance (Note 10)	R <sub>DS(ON)</sub>	-	0.140	0.180		$V_{GS} = 4.5V, I_D$	= 2.0A
Forward Transconductance (Note 10 & 11)	9 <sub>fs</sub>	-	3.5	-	S	$V_{DS} = 10V, I_{D} = 10V$	= 2.5A
Diode Forward Voltage (Note 10)	V <sub>SD</sub>	-	0.85	0.95	V	I <sub>S</sub> = 1.7A, V <sub>GS</sub> = 0V	
Reverse Recover Time (Note 11)	t <sub>rr</sub>	-	17.7	-	ns	$I_S = 2.5A$ , di/dt = 100A/ $\mu$ s	
Reverse Recover Charge (Note 11)	Q <sub>rr</sub>	-	13.0	-	nC		
DYNAMIC CHARACTERISTICS (Note 11)							
Input Capacitance	C <sub>iss</sub>	-	190	-	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	Coss	-	38	-	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	20	-	pF		
Total Gate Charge (Note 12)	Qg	-	2.3	-	nC	$V_{GS} = 4.5V$	
Total Gate Charge (Note 12)	Qq	-	3.9	-	nC	$V_{\rm DS} = 15V$ $V_{\rm DS} = 15V$ $V_{\rm DS} = 2.5A$	
Gate-Source Charge (Note 12)	$Q_{gs}$	-	0.6	-	nC		
Gate-Drain Charge (Note 12)	Q <sub>gd</sub>	-	0.9	-	nC		
Turn-On Delay Time (Note 12)	t <sub>D(on)</sub>	-	1.7	-	ns	$V_{DS} = 15V, I_{D} = 2.5A$ $V_{GS} = 10V, R_{G} = 6\Omega$	
Turn-On Rise Time (Note 12)	t <sub>r</sub>	-	2.3	-	ns		
Turn-Off Delay Time (Note 12)	t <sub>D(off)</sub>	-	6.6	-	ns		
Turn-Off Fall Time (Note 12)	t <sub>f</sub>	-	2.9	-	ns		

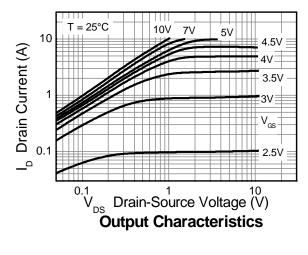
Notes:

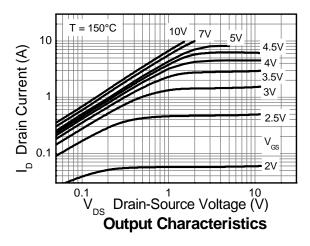
- 10. Measured under pulsed conditions. Width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.
- 11. For design aid only, not subject to production testing.

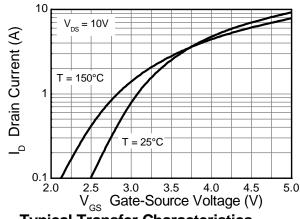
  12. Switching characteristics are independent of operating junction temperature.

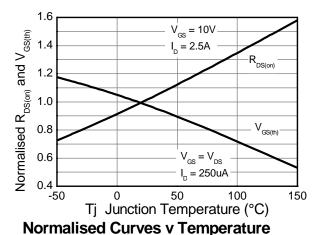


### **Typical Electrical Characteristics**

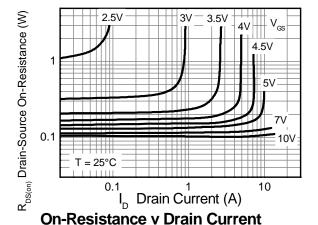








**Typical Transfer Characteristics** 



Reverse Drain Voltage (V)

T = 150°C

T = 150°C

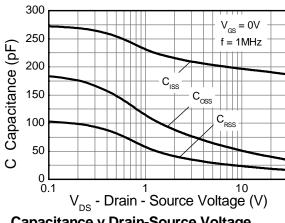
T = 25°C

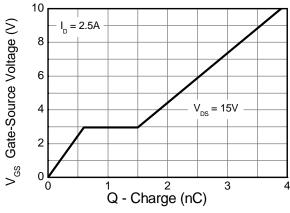
V<sub>SD</sub> Source-Drain Voltage (V)

Source-Drain Diode Forward Voltage



### **Typical Electrical Characteristics - Continued**

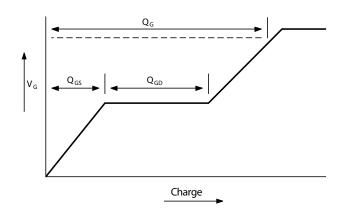




Capacitance v Drain-Source Voltage

Gate-Source Voltage v Gate Charge

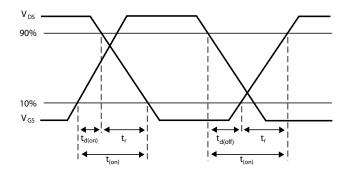
### **Test Circuits**

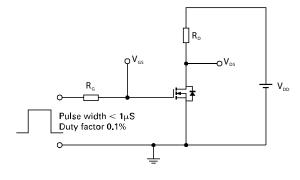


Current regulator (1) Ic J**⊑**ir d.U.T

Basic gate charge waveform

Gate charge test circuit

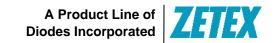




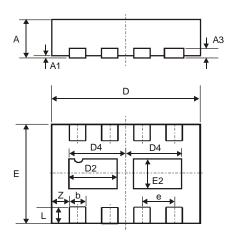
Switching time waveforms

Switching time test circuit



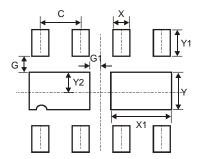


# Package Outline Dimensions



DFN3020B-8						
Dim	Min	Max	Тур			
Α	0.77	0.83	0.80			
A1	0	0.05	0.02			
A3	-	-	0.15			
b	0.25	0.35	0.30			
D	2.95	3.075	3.00			
D2	0.82	1.02	0.92			
D4	1.01	1.21	1.11			
е	-	-	0.65			
Е	1.95	2.075	2.00			
E2	0.43	0.63	0.53			
L	0.25	0.35	0.30			
Z	-	-	0.375			
All Dimensions in mm						

### **Suggested Pad Layout**



Dimensions	Value (in mm)
С	0.650
G	0.285
G1	0.090
Х	0.400
X1	1.120
Y	0.730
Y1	0.500
Y2	0.365





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