



# OCXO Model: OS400-2005-015

## Issue 2; 6th May 2022

#### **Features**

- Temperature stability to ±20ppb
- Low phase noise
- Frequency 20MHz
- Industry standard package
- The flexible nature of the design means that variations to suit almost any application can be developed to meet individual customer requirements



- Temperature stability: ±20ppb over (-40 to +70)°C
- Output: Sinewave +7dBm 5.0V Voltage: Warm up current: 440mA Quiescent current: 220mA



F0<sub>0</sub>+10Hz -120 dBc/Hz F0<sub>0</sub>+100Hz -145 dBc/Hz F0<sub>0</sub>+1KHz -155 dBc/Hz F<sub>00</sub>+10KHz -165 dBc/Hz F0<sub>0</sub>+100KHz -168 dBc/Hz

## Voltage / Load change

- ±5% supply voltage change: ±2ppb
- ±10% load change: ±10ppb

#### Ageing

- Per day: ±0.15ppb max.
- Per year: ±60ppb max.
- Warm up time: 5 minutes to within 1 ppm

#### Voltage Trim

- ±0.5ppm minimum
- Trim impedance 50KΩ

# **Reference Options**

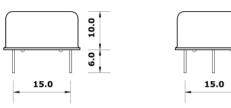
4.5V for 5.0V supply

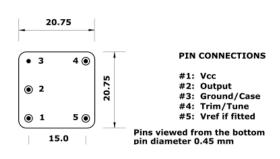
## Environmental

- Electrostatic-Sensitive Device (ESD)
- Storage Temperature Range: (-40 to +125)°C
- Mechanical shock: MIL standard 202F, method 213, condition J
- Thermal shock: MIL standard 202F, method 107, condition A
- Vibration: MIL standard 202F, method 204, condition B

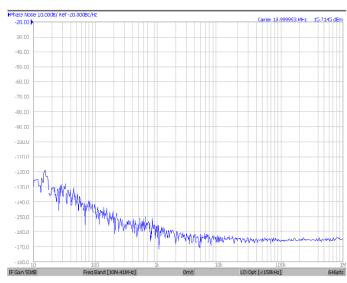


# Dimensions (mm)





## Phase Noise Plot



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- Solderability: 5 seconds maximum at 230°C
- 3 seconds maximum at 350°C

## Compliance

- RoHS Status (2011/65/EU) Compliant
- REACH Status Compliant

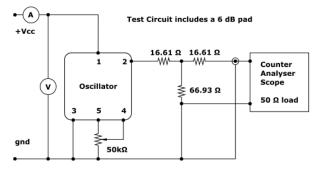
# **Packaging**

Pack Style: Bulk

## **Ordering Information**

- Unique customer part number and custom specification issued with each application
- OCXO Model: 0S400-2005-015
- Frequency: 20MHz
- Stability/Output/Voltage: Option C
- Supply voltage code: V2 = +5.0Vd.c. supply
- Add suffix (R) for Vref output on pin #5

## **Test Circuit - Sinewave**



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