

**Micro Commercial Components** 



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### Features

- Lead Free Finish/RoHS Compliant (NOTE 1)("P" Suffix designates RoHS Compliant. See ordering information)
- International standard package
- Heat transfer through aluminum oxide DBC ceramic isolated metal baseplate
- Glass passivated chip
- Simple Mounting

### **Applications**

- Power Converters
- Lighting Control
- DC Motor Control and Drives
- Heat and temperature control





MT25CB08T1 **MT25CB12T1** MT25CB16T1 MT25CB18T1 25 Amp **THYRISTOR/DIODE** MODULE 800~1800 Volts T1 M5 C‡ В D₫ LÎKŧ DIMENSIONS INCHES MM DIM NOTE MIN MAX MIN MAX А 0.776 0.799 19.50 20.50 В 1.169 1.193 29.50 30.50 С 0.343 0.366 8.50 9.50 D 0.323 0.346 8.00 9.00 Е 0.602 0.622 15.10 16.00 F 0.224 0.248 5.50 6.50 G 0.539 0.563 13.50 14.50 Н 0.657 0.681 16.50 17.50 79.50 80.50 Т 3.138 3.161 3.650 3.673 92.50 93.50 J Κ 0.256 6.50 Ø

0.421 0.445

0.11X0.032

0.839

0.602

0.815

0.579

Τ

Μ

0

Ρ

10.50

20.50

14.50

11.50

21.50

15.50

2.8X0.8

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#### Module Type

ТҮРЕ	Vrrm	Vrsm
MT25CB08T1	800V	900V
MT25CB12T1	1200V	1300V
MT25CB16T1	1600V	1700V
MT25CB18T1	1800V	1900V

#### ♦ Diode

#### Maximum Ratings

Symbol	ltem	Conditions	Values	Units
ld	Output Current(D.C.)	<b>Tc=85</b> ℃	25	А
IFSM	Surge forward current	t=10mS Tvj =45℃	550	А
i²t	Circuit Fusing Consideration		1500	A <sup>2</sup> s
Visol	Isolation Breakdown Voltage(R.M.S)	a.c.50HZ;r.m.s.;1min	3000	V
Tvj	Operating Junction Temperature		-40 to +125	°C
Tstg	Storage Temperature		-40 to +125	°C
Mt	Mounting Torque	To terminals(M5)	3±15%	Nm
Ms		To heatsink(M6)	5±15%	Nm
Weight	Module (Approximately)		100	g

#### **Thermal Characteristics**

Symbol	Item	Conditions	Values	Units
Rth(j-c)	Thermal Impedance, max.	Junction to Case	0.45	°C/W
Rth(c-s)	Thermal Impedance, max.	Case to Heatsink	0.10	°C/W

#### **Electrical Characteristics**

Symbol	ltem	Conditions	Values		Units	
Cymbol	nem	Conditions	Min.	Тур.	Max.	Onits
VFM	Forward Voltage Drop, max.	T=25℃ IF =75A			1.80	V
Irrm	Repetitive Peak Reverse Current, max.	Tvj =25℃ VrD=Vrrm Tvj =125℃ VrD=Vrrm		≤0.5 ≤6		mA mA

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#### ♦Thyristor Maximum Ratings

Symbol	Item	Conditions	Values	Units
I <sub>TAV</sub>	Average On-State Current	Sine 180°;Tc=85℃	25	А
I <sub>TSM</sub>	Surge On-State Current	T <sub>VJ</sub> =45℃ t=10ms, sine T <sub>VJ</sub> =125℃ t=10ms, sine	550 480	A
i²t	Circuit Fusing Consideration	T <sub>VJ</sub> =45℃ t=10ms, sine T <sub>VJ</sub> =125℃ t=10ms, sine	1500 1150	A <sup>2</sup> s
Visol	Isolation Breakdown Voltage(R.M.S)	a.c.50HZ;r.m.s.;1min	3000	V
Tvj	Operating Junction Temperature		-40 to +125	°C
Tstg	Storage Temperature		-40 to +125	°C
Mt	Mounting Torque	To terminals(M5)	3±15%	Nm
Ms		To heatsink(M6)	5±15%	Nm
di/dt	Critical Rate of Rise of On-State Current	$T_{VJ}\text{=}~T_{VJM}$ , 2/3V_{DRM} ,I_G =500mA Tr<0.5us,tp>6us	150	A/us
dv/dt	Critical Rate of Rise of Off-State Voltage, min.	$T_J = T_{VJM}$ ,2/3 $V_{DRM}$ linear voltage rise	1000	V/us
а	Maximum allowable acceleration		50	m/s <sup>2</sup>

#### **Thermal Characteristics**

Symbol	Item	Conditions	Values	Units
Rth(j-c)	Thermal Impedance, max.	Junction to Case	0.90	°C/W
Rth(c-s)	Thermal Impedance, max.	Case to Heatsink	0.20	°C/W

#### **Electrical Characteristics**

Cumhal	ltow	Conditions	Value	s	
Symbol	Item	Conditions			Units
V <sub>TM</sub>	Peak On-State Voltage, max.	T=25℃ I <sub>T</sub> =75A		1.80	V
I <sub>RRM</sub> /I <sub>DRM</sub>	Repetitive Peak Reverse Current, max. / Repetitive Peak Off-State Current, max.	T <sub>VJ</sub> =T <sub>VJM</sub> ,V <sub>R</sub> =V <sub>RRM</sub> ,V <sub>D</sub> = V <sub>DRM</sub>		10	mA
V <sub>TO</sub>	On state threshold voltage	For power-loss calculations only (T <sub>VJ</sub> =125℃)		0.9	V
r <sub>⊤</sub>	Value of on-state slope resistance. max	T <sub>VJ</sub> =T <sub>VJM</sub>		12	mΩ
V <sub>GT</sub>	Gate Trigger Voltage, max.	$T_{VJ}$ =25 $^\circ C$ , $V_D$ =6V		2.5	V
I <sub>GT</sub>	Gate Trigger Current, max.	$T_{VJ}$ =25 $^{\circ}\mathrm{C}$ , $V_{D}$ =6V		150	mA
V <sub>GD</sub>	Non-triggering gate voltage, max.	T <sub>VJ</sub> =125℃,V <sub>D</sub> =2/3V <sub>DRM</sub>		0.25	V
I <sub>GD</sub>	Non-triggering gate current, max.	T <sub>VJ</sub> =125℃, V <sub>D</sub> =2/3V <sub>DRM</sub>		5	mA
١ <sub>L</sub>	Latching current, max.	$T_{VJ}$ =25 $^{\circ}$ C , $R_{G}$ = 33 $\Omega$	250	400	mA
Ι <sub>Η</sub>	Holding current, max.	T <sub>VJ</sub> =25℃, V <sub>D</sub> =6V	100	200	mA
tgd	Gate controlled delay time	TVJ=25℃, IG=1A, diG/dt=1A/us	1		us
tq	Circuit commutated turn-off time	$T_{VJ} = T_{VJM}$	80		us

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#### **Performance Curves**



Fig4. Max Non-Repetitive Forward Surge Current



#### **Performance Curves**







Fig6. Gate trigger Characteristics

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#### **Revision:** A



#### **Ordering Information :**

Device	Packing
Part Number-BP	Bulk: 10PCS/BOX ;100PCS/CTN

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