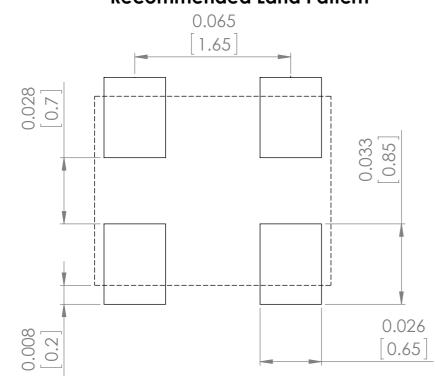


Recommended Land Pattern



Note: Recommended using an approximately 0.01 uf bypass capacitor between PIN 2 and 4.

No	Pin Terminal Standby#				
1					
2	GND				
3	Output				
4	VDD				



UNLESS OTHERWISE SPECIFIED: FINIS DIMENSIONS ARE IN INCH(MM) SURFACE FINISH:		FINISH:				DEBUR AND BREAK SHARP EDGES		DO NOT SCALE DRAWING		REVISION	-	
TOLERANC LINE ANGUL	CES: AR:						EDGES				BRAC	
	NAME	NAME SIGI		DATE				CORPORAT			MOL	
DRAWN	N SAAZVAT 20222 Canaranza, Danaha Canta margarita									arita California	, California 92688	
CHK'D	30332 Esperanza, Rancho Santa margarita, Califo											
APPV'D								TITLE:	ULTRA MINIATURE P	LASTIC PA	ACKAGE PUR	?E
MFG									SILICON CLOCK OS	LOCK OSCILLATOR		
Q.A					MATERIAL	:		DWG NO.	ASI	7//		A3
									, (02)		· ·	
					WEIGHT:			SCALE:25:1		SHEET 1 OF 1		