
# NXV75UP

## 20 V, P-channel Trench MOSFET

19 October 2020 Product data sheet

## 1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology

## 3. Applications

- Relay driver
- High-speed line driver
- High-side load switch
- Switching circuits

## 4. Quick reference data

**Table 1. Quick reference data**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $V_{DS}$ | drain-source voltage | $T_j$ = 25 °C | | - | - | -20 | V |
| $V_{GS}$ | gate-source voltage | | | -8 | - | 8 | V |
| $I_D$ | drain current | $V_{GS}$ = -4.5 V; $T_{amb}$ = 25 °C | [1] | - | - | -1.8 | A |
| **Static characteristics** | | | | | | | |
| $R_{DSon}$ | drain-source on-state resistance | $V_{GS}$ = -4.5 V; $I_D$ = -1.8 A; $T_j$ = 25 °C | | - | 75 | 90 | mΩ |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 $cm^2$.

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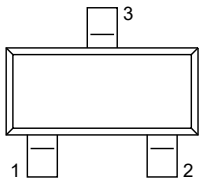
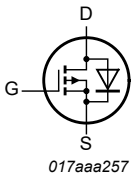
## 5. Pinning information

**Table 2. Pinning information**

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|---|---|---|---|---|
| 1 | G | gate |  SOT23 |  017aaa257 |
| 2 | S | source | | |
| 3 | D | drain | | |

## 6. Ordering information

**Table 3. Ordering information**

| Type number | Package | | |
|---|---|---|---|
| | Name | Description | Version |
| NXV75UP | SOT23 | plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body | SOT23 |

## 7. Marking

**Table 4. Marking codes**

| Type number | Marking code[1] |
|---|---|
| NXV75UP | %5M |

[1] % = placeholder for manufacturing site code

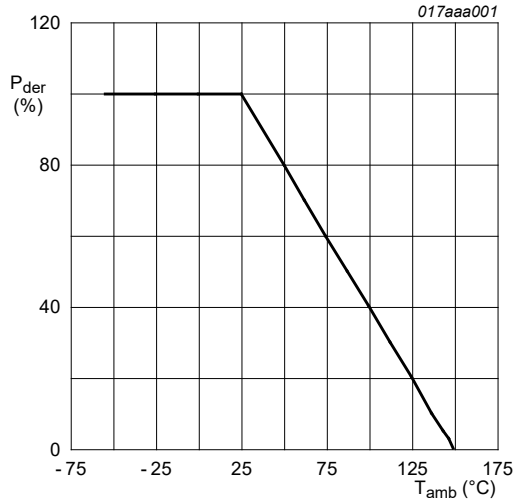
## 8. Limiting values

**Table 5. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

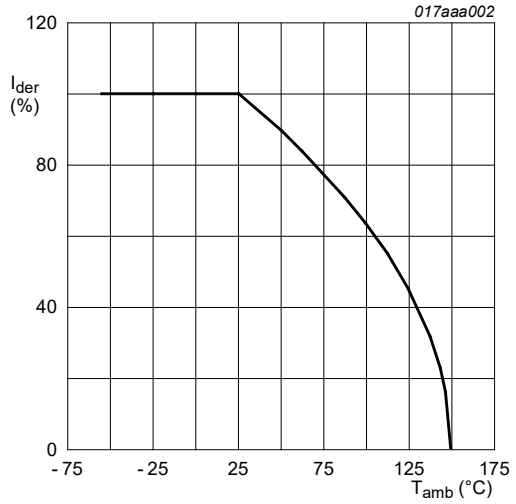
| Symbol | Parameter | Conditions | | Min | Max | Unit |
|---|---|---|---|---|---|---|
| $V_{DS}$ | drain-source voltage | $T_j$ = 25 °C | | - | -20 | V |
| $V_{GS}$ | gate-source voltage | | | -8 | 8 | V |
| $I_D$ | drain current | $V_{GS}$ = -4.5 V; $T_{amb}$ = 25 °C | [1] | - | -1.8 | A |
| | | $V_{GS}$ = -4.5 V; $T_{amb}$ = 100 °C | [1] | - | -1.1 | A |
| $I_{DM}$ | peak drain current | $T_{amb}$ = 25 °C; single pulse; $t_p \leq$ 10 µs | | - | -7.2 | A |
| $P_{tot}$ | total power dissipation | $T_{amb}$ = 25 °C | [2] | - | 340 | mW |
| | | | [1] | - | 480 | mW |
| | | $T_{sp}$ = 25 °C | | - | 2.1 | W |
| $T_j$ | junction temperature | | | -55 | 150 | °C |
| $T_{amb}$ | ambient temperature | | | -55 | 150 | °C |
| $T_{stg}$ | storage temperature | | | -65 | 150 | °C |
| **Source-drain diode** | | | | | | |
| $I_S$ | source current | $T_{amb}$ = 25 °C | [1] | - | -0.4 | A |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 $cm^2$.
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.




$$P_{der} = \frac{P_{tot}}{P_{tot(25°C)}} \times 100\ \%$$

**Fig. 1. Normalized total power dissipation as a function of ambient temperature**




$$I_{der} = \frac{I_D}{I_{D(25°C)}} \times 100\ \%$$

**Fig. 2. Normalized continuous drain current as a function of ambient temperature**

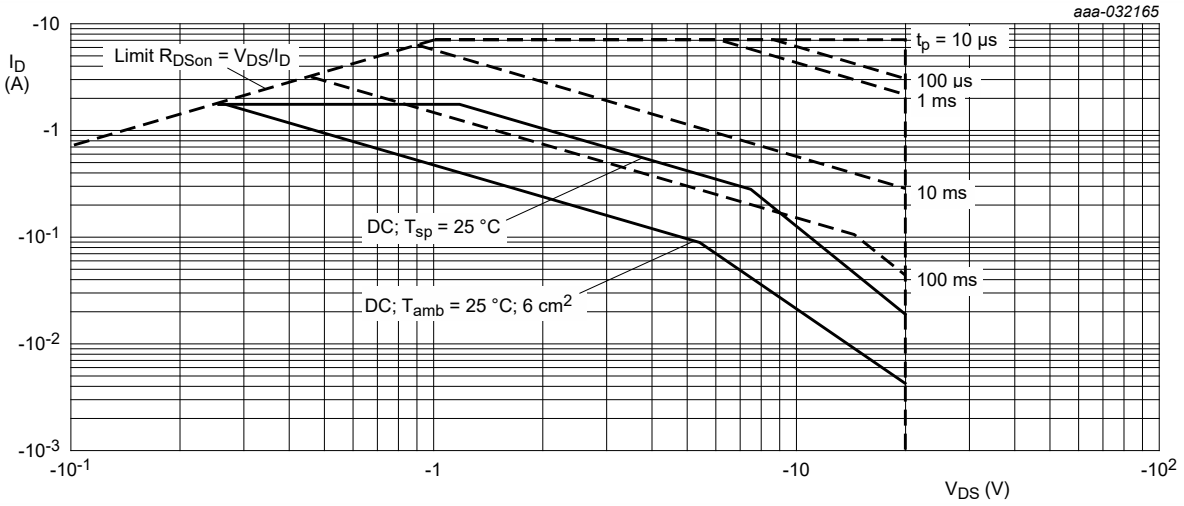


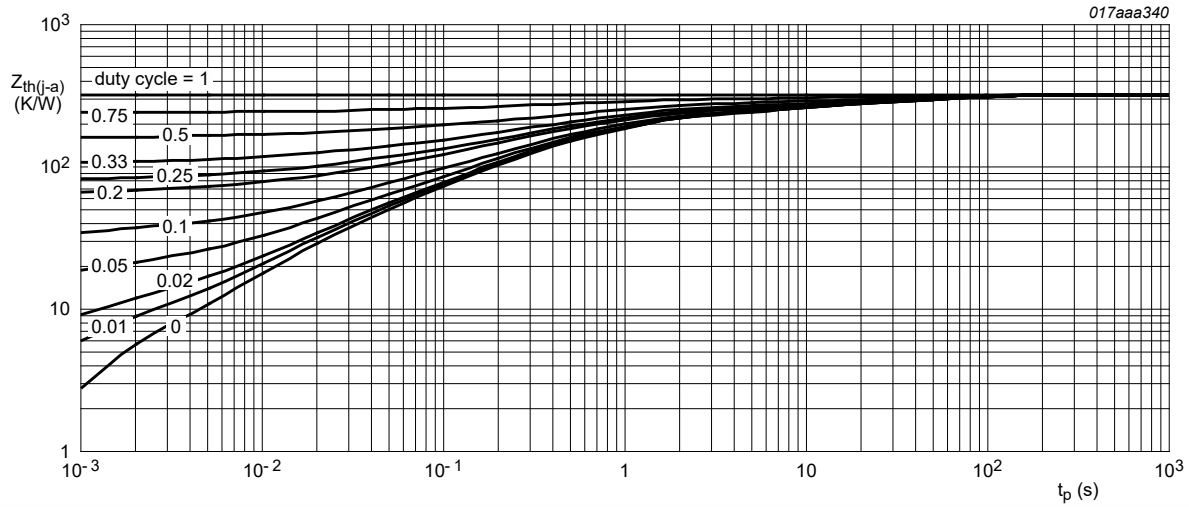

**Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage**

# 9. Thermal characteristics

**Table 6. Thermal characteristics**

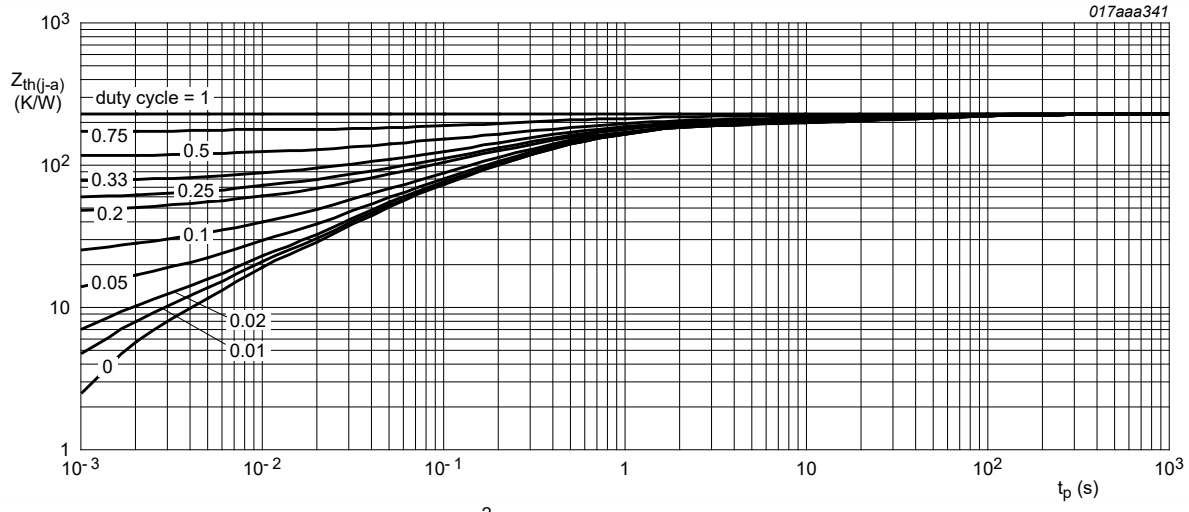
| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | [1] | - | 325 | 370 | K/W |
| | | | [2] | - | 230 | 260 | K/W |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | | - | 50 | 60 | K/W |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 $cm^2$.




FR4 PCB, standard footprint

**Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**



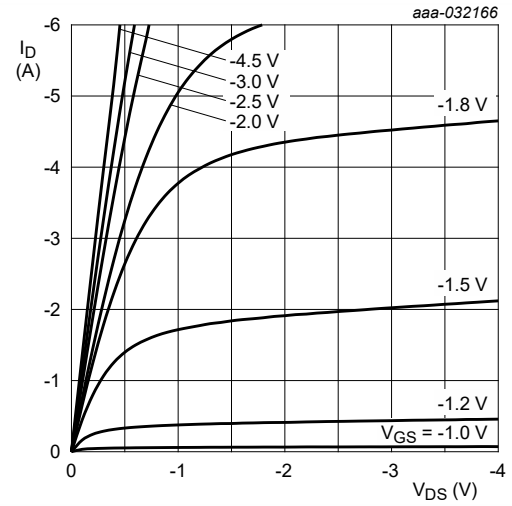

FR4 PCB, mounting pad for drain 6 $cm^2$

**Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**

## 10. Characteristics

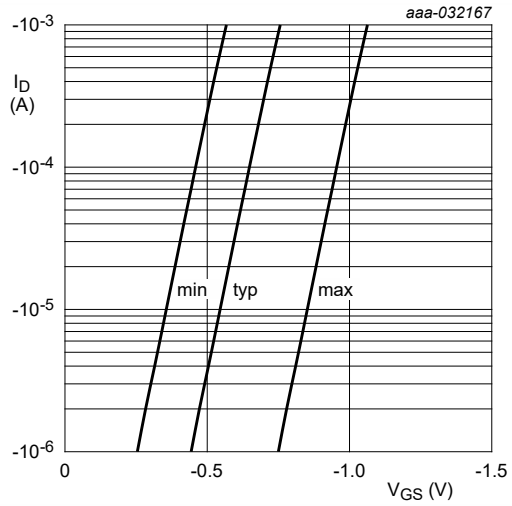
**Table 7. Characteristics**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| **Static characteristics** | | | | | | | |
| $V_{(BR)DSS}$ | drain-source breakdown voltage | $I_D$ = -250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C | | -20 | - | - | V |
| $V_{GSth}$ | gate-source threshold voltage | $I_D$ = -250 µA; $V_{DS}$=$V_{GS}$; $T_j$ = 25 °C | | -0.5 | -0.7 | -1 | V |
| $I_{DSS}$ | drain leakage current | $V_{DS}$ = -20 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C | | - | - | -1 | µA |
| $I_{GSS}$ | gate leakage current | $V_{GS}$ = -8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C | | - | - | -100 | nA |
| | | $V_{GS}$ = 8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C | | - | - | 100 | nA |
| $R_{DSon}$ | drain-source on-state resistance | $V_{GS}$ = -4.5 V; $I_D$ = -1.8 A; $T_j$ = 25 °C | | - | 75 | 90 | mΩ |
| | | $V_{GS}$ = -4.5 V; $I_D$ = -1.8 A; $T_j$ = 150 °C | | - | 120 | 140 | mΩ |
| | | $V_{GS}$ = -2.5 V; $I_D$ = -1.5 A; $T_j$ = 25 °C | | - | 103 | 130 | mΩ |
| $g_{fs}$ | forward transconductance | $V_{DS}$ = -5 V; $I_D$ = -1.8 A; $T_j$ = 25 °C | | - | 5 | - | S |
| $R_G$ | gate resistance | f = 1 MHz | | - | 9 | - | Ω |
| **Dynamic characteristics** | | | | | | | |
| $Q_{G(tot)}$ | total gate charge | $V_{DS}$ = -10 V; $I_D$ = -1.8 A; $V_{GS}$ = -4.5 V; $T_j$ = 25 °C | | - | 4.2 | 6.3 | nC |
| $Q_{GS}$ | gate-source charge | | | - | 0.5 | - | nC |
| $Q_{GD}$ | gate-drain charge | | | - | 1.3 | - | nC |
| $C_{iss}$ | input capacitance | $V_{DS}$ = -10 V; f = 1 MHz; $V_{GS}$ = 0 V; $T_j$ = 25 °C | | - | 330 | - | pF |
| $C_{oss}$ | output capacitance | | | - | 51 | - | pF |
| $C_{rss}$ | reverse transfer capacitance | | | - | 44 | - | pF |
| $t_{d(on)}$ | turn-on delay time | $V_{DS}$ = -10 V; $I_D$ = -1.8 A; $V_{GS}$ = -4.5 V; $R_{G(ext)}$ = 6 Ω; $T_j$ = 25 °C | | - | 5 | - | ns |
| $t_r$ | rise time | | | - | 11 | - | ns |
| $t_{d(off)}$ | turn-off delay time | | | - | 36 | - | ns |
| $t_f$ | fall time | | | - | 14 | - | ns |
| **Source-drain diode** | | | | | | | |
| $V_{SD}$ | source-drain voltage | $I_S$ = -0.4 A; $V_{GS}$ = 0 V; $T_j$ = 25 °C | | - | -0.7 | -1.2 | V |



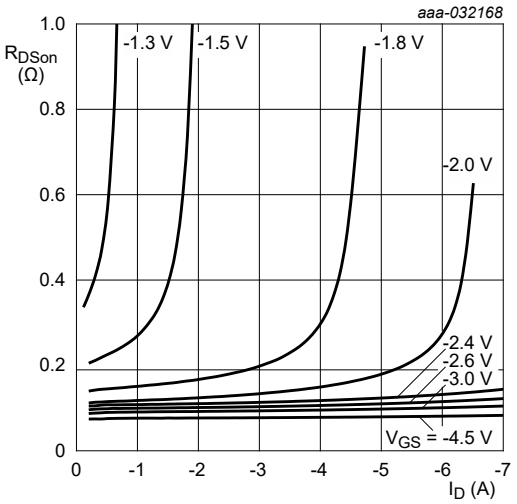

$T_j$ = 25 °C

**Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values**



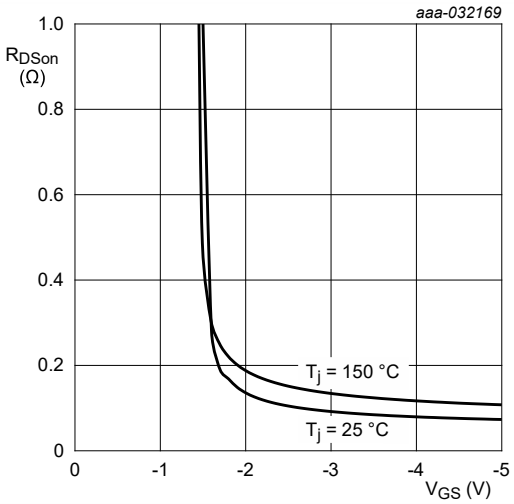

$T_j$ = 25 °C; $V_{DS}$ = -5 V

**Fig. 7. Sub-threshold drain current as a function of gate-source voltage**



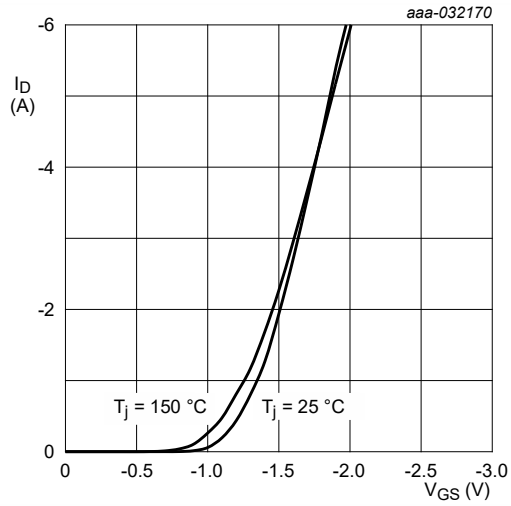

$T_j$ = 25 °C

**Fig. 8. Drain-source on-state resistance as a function of drain current; typical values**



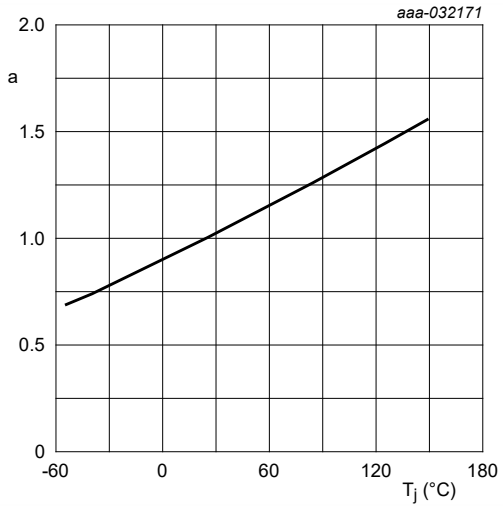

$I_D$ = -1.8 A

**Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values**



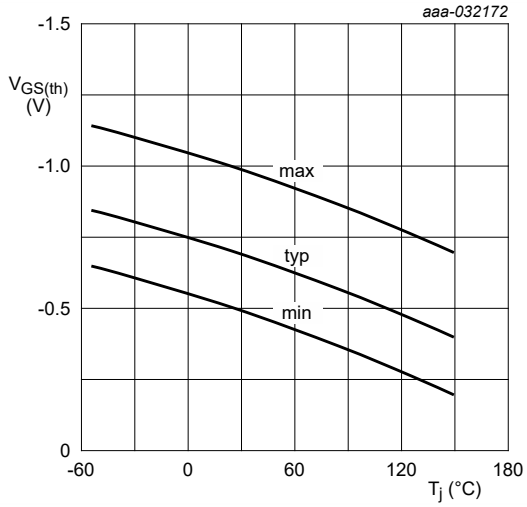

$V_{DS}$ = -5V

**Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values**



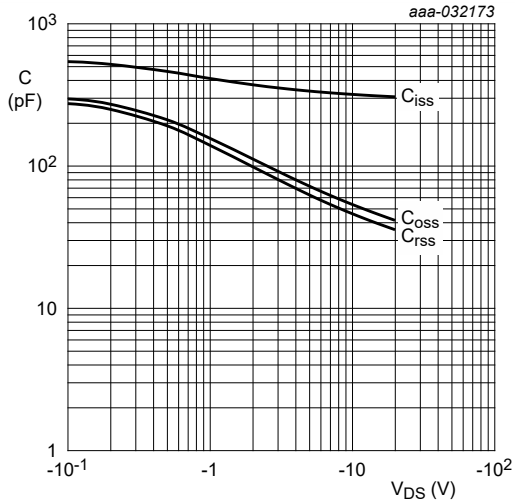

$$a = \frac{R_{DSon}}{R_{DSon(25°C)}}$$

**Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values**



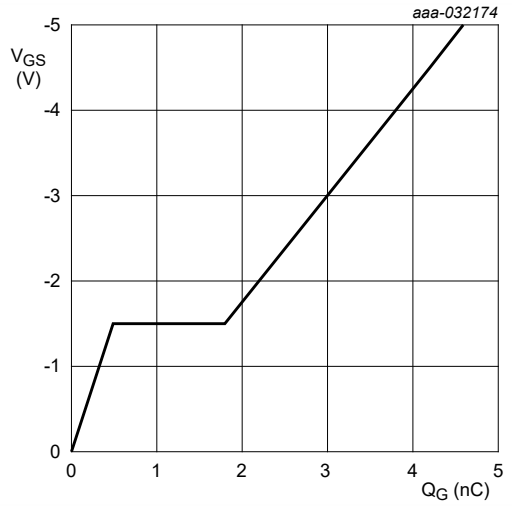

$I_D$ = -250 µA; $V_{DS}$ = $V_{GS}$

**Fig. 12. Gate-source threshold voltage as a function of junction temperature**




f = 1 MHz; $V_{GS}$ = 0 V

**Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values**




$V_{DS}$ = -10 V; $I_D$ = -1.8 A;$T_j$ = 25 °C

**Fig. 14. Gate-source voltage as a function of gate charge; typical values**

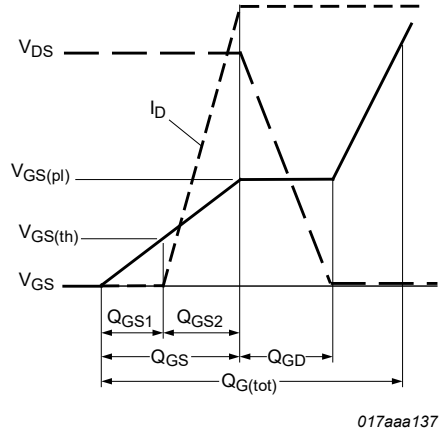
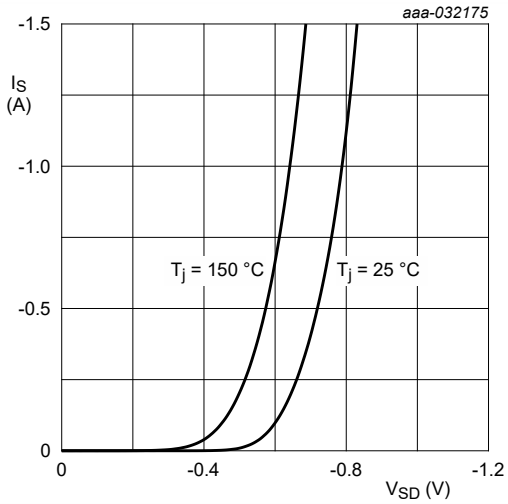



**Fig. 15. Gate charge waveform definitions**




$V_{GS}$ = 0 V

**Fig. 16. Source current as a function of source-drain voltage; typical values**

## 11. Test information

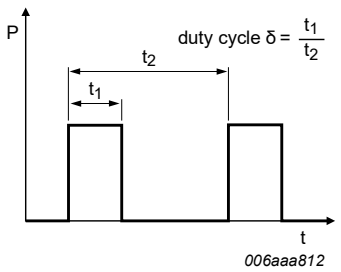
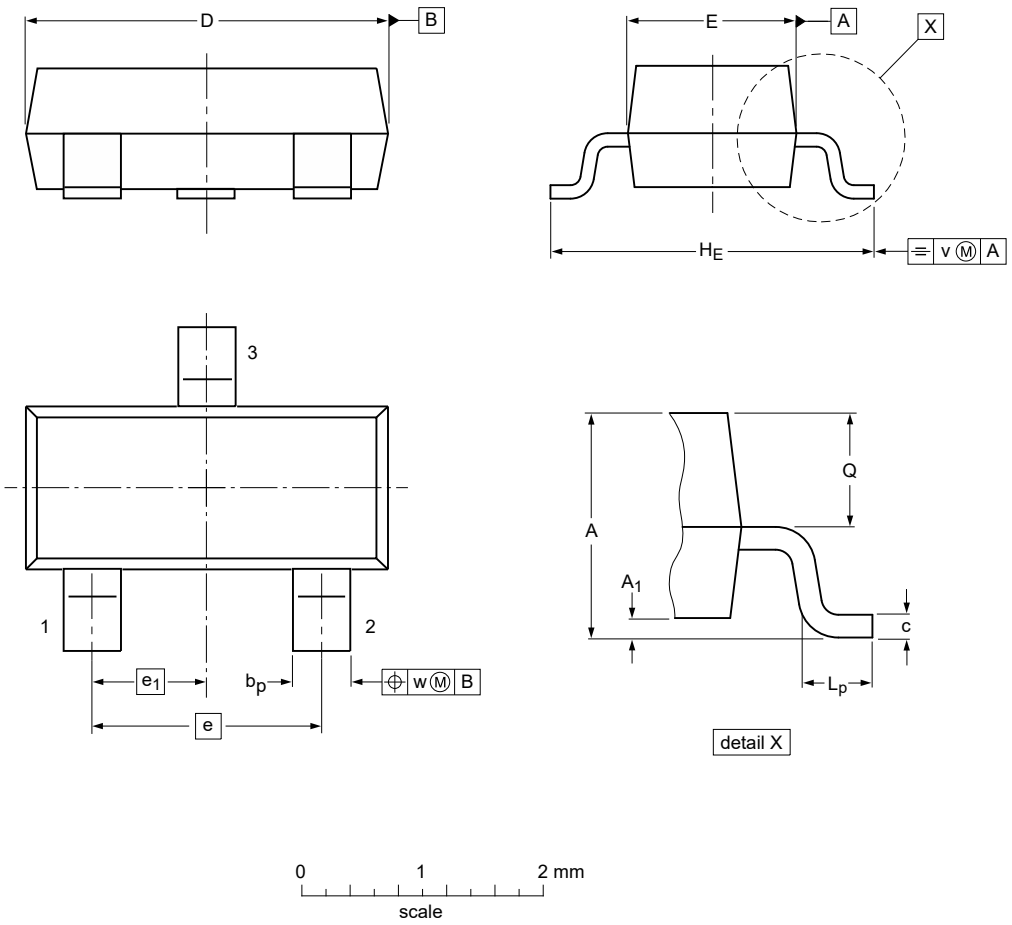



**Fig. 17. Duty cycle definition**

## 12. Package outline

**Plastic surface-mounted package; 3 leads** **SOT23**




Dimensions (mm are the original dimensions)

| Unit | | A | $A_1$ | $b_p$ | c | D | E | e | $e_1$ | $H_E$ | $L_p$ | Q | v | w |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| mm | max | 1.1 | 0.1 | 0.48 | 0.15 | 3.0 | 1.4 | | | 2.5 | 0.45 | 0.55 | | |
| | nom | | | | | | | 1.9 | 0.95 | | | | 0.2 | 0.1 |
| | min | 0.9 | | 0.38 | 0.09 | 2.8 | 1.2 | | | 2.1 | 0.15 | 0.45 | | |

*sot023_po*

| Outline version | References | | | | European projection | Issue date |
|---|---|---|---|---|---|---|
| | IEC | JEDEC | JEITA | | | |
| SOT23 | | TO-236AB | | | | ~~14-06-19~~ 14-09-22 |

**Fig. 18. Package outline SOT23**

## 13. Soldering

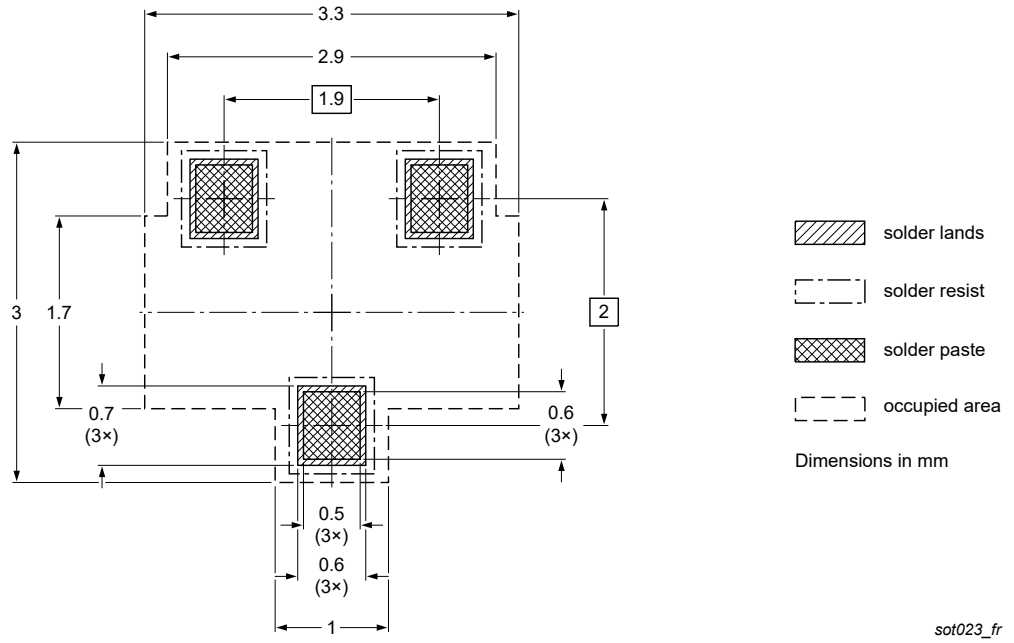



**Fig. 19. Reflow soldering footprint for SOT23**

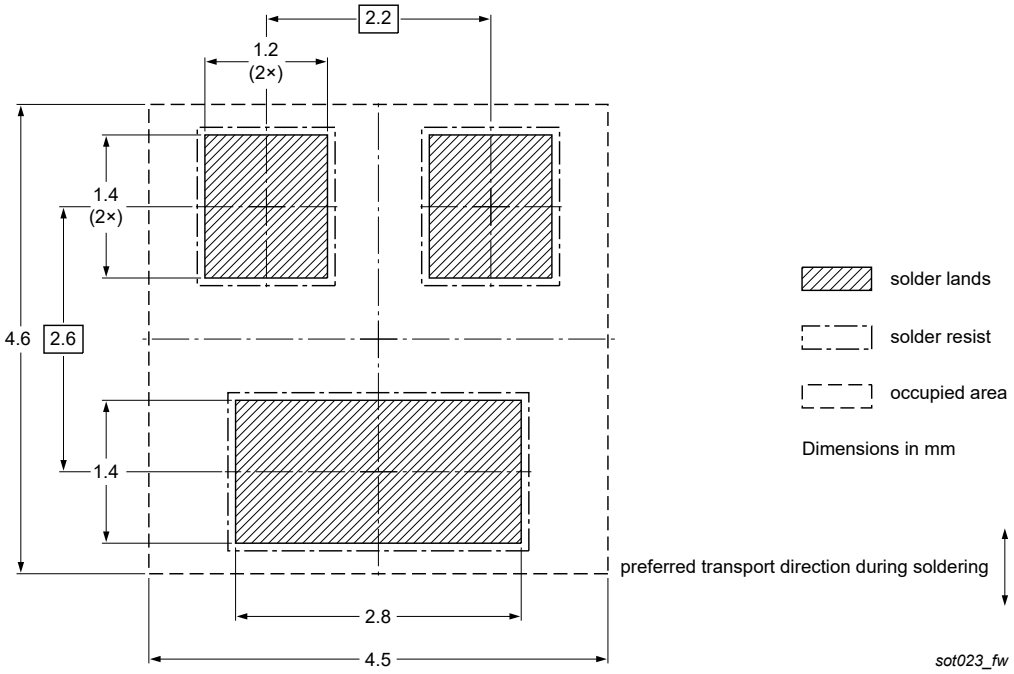



**Fig. 20. Wave soldering footprint for SOT23**

## 14. Revision history

**Table 8. Revision history**

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|---|---|---|---|---|
| NXV75UP v.1 | 20201019 | Product | - | - |

# 15. Legal information

## Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|---|---|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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**Date of release: 19 October 2020**