

### Features

- Ceramic Surface Mount Package
- Extended-Industrial Temperature Ranges
- Fundamental and 3<sup>rd</sup> Overtone Crystal Designs
- Frequency Range 1.25 156.25MHz \*
- +1.8V, +2.5V, +3.3V Operation; +5.0V Limited Availability
- Output Enable Standard
- Tape and Reel Packaging, EIA-481

### Applications

- Industrial IoT [IIoT]
- M2M Communication
- Industrial Controls
- Energy Industry



RoHS

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Part Dimensions: 5.0 × 3.2 × 1.3mm • 60.7734mg

Standard Frequencies

\* See Page 6 for common frequencies. Check with factory for availability of frequencies not listed and for +5.0V operation.

- Commercial Military & Aerospace
- Test and Measurement

### Description

CTS Model CHT50 is a low cost, small size, Clock Oscillator [XO] that operates over extended-industrial temperature ranges. CHT50 has an HCMOS/TTL compatible output, offers excellent stability and low jitter/phase noise performance.

### **Ordering Information**



Notes:

1] The CHT50 platform is qualified and manufactured to commercial and industrial standards.

2] Refer to document 016-1454-0, Frequency Code Tables. 3-digits for frequencies <100MHz, 4-digits for frequencies 100MHz or greater.

3] Temperature Only. All other stabilities are "all inclusive". See Electrical Specifications.

#### Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

This product is specified for use only in standard commercial applications. Supplier disclaims all express and implied warranties and liability in connection with any use of this product in any non-commercial applications or in any application that may expose the product to conditions that are outside of the tolerances provided in its specification.

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# **Electrical Specifications**

#### **Operating Conditions**

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT	
Maximum Supply Voltage	Vcc	V <sub>CC</sub> +1.8V to +3.3V	-0.5	-	4.0	V	
			1.710	1.8	1.890		
Cupply Valtage		1 5 0/	2.375	2.5	2.665	14	
Supply Voltage	V <sub>CC</sub>	±5%	3.135	3.3	3.465	V	
			4.750	5.0	5.250		
	Туріс	cal @ Nominal Vcc, CL = 15 pF, TA = +2	25°C				
		@ +1.8V	-	15	25	mA	
Supply Current	lcc	@ +2.5V	-	20	30		
		@ +3.3V	-	35	40		
		@ +5.0V	-	35	55		
Output Load	CL	-	-	-	15	pF	
On exerting Terms exerture	<u> </u>		-55	-25	+105	°C	
Operating Temperature	T <sub>A</sub>	-	-55	+25	+125	-(	
Storage Temperature	T <sub>STG</sub>	-	-55	-	+125	°C	

#### Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Frequency Range	fo	Temperature Range -55°C to +105°C	1.25 - 156.25			MHz
		Temperature Range -55°C to +125°C	40 - 135			
Frequency Stability [Note 1]	∆f/fo	-		50, 75 or 100		±ppm
Aging	$\Delta f/f_{25}$	First Year @ +25°C, nominal V <sub>CC</sub>	-5	-	5	ppm

1.] Inclusive of initial tolerance at time of shipment, changes in supply voltage, load, temperature and 1st year aging. Except ±50ppm which is temperature only.

#### **Output Parameters**

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT	
Output Type	-	-		HCMO	5	-	
Output Valtaga Lavala	V <sub>OH</sub>	Logic '1' Level, CMOS Load	0.9V <sub>CC</sub>	-	-	V	
Output Voltage Levels	Vol	Logic '0' Level, CMOS Load			$0.1V_{CC}$	V	
Quita ut Current Louis	I <sub>OH</sub>	V <sub>OH</sub> = 90%V <sub>CC</sub> [1.8V, 2.5V, 3.3V, 5.0V]	-	-	-4, -4, -8, -16		
Output Current Levels	I <sub>OL</sub>	V <sub>OL</sub> = 10%V <sub>CC</sub> [1.8V, 2.5V, 3.3V, 5.0V]	-	- +4, +4, +8, +1		mA 6	
Output Duty Cycle	SYM	@ 50% Level	45	-	55	%	
		@ 10%/90% Levels, Nominal V <sub>CC</sub> , C <sub>L</sub> = 15pF					
		@ +1.8V	-	4	5		
Rise and Fall Time [Note 2]	T <sub>R</sub> , T <sub>F</sub>	@ +2.5V	-	4	5	ns	
[		@ +3.3V	-	7	10		
		@ +5.0V	-	7	10		
Start Up Time	Ts	Application of V <sub>CC</sub>	-	2	5	ms	

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# **Electrical Specifications**

#### **Output Parameters**

SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT	
Sta						
VIH	Pin 1 Logic '1', Output Enabled	$0.7V_{CC}$	-	-	V	
VIL	Pin 1 Logic '0', Output Standby	-	-	0.3V <sub>CC</sub>	V	
I <sub>STB</sub>	Pin 1 Logic '0', Output Standby	-	-	10	μΑ	
T <sub>PLZ</sub>	Pin 1 Logic '1'	-	-	5	ms	
tjrms	Bandwidth 12kHz - 20MHz	-	0.5	<1	ps	
	Sta VIH VIL ISTB TPLZ	Standby V <sub>IH</sub> Pin 1 Logic '1', Output Enabled V <sub>IL</sub> Pin 1 Logic '0', Output Standby I <sub>STB</sub> Pin 1 Logic '0', Output Standby T <sub>PLZ</sub> Pin 1 Logic '1'	StandbyVIHPin 1 Logic '1', Output Enabled0.7VccVILPin 1 Logic '0', Output Standby-ISTBPin 1 Logic '0', Output Standby-TPLZPin 1 Logic '1'-	StandbyVIHPin 1 Logic '1', Output Enabled0.7Vcc-VILPin 1 Logic '0', Output StandbyISTBPin 1 Logic '0', Output StandbyTPLZPin 1 Logic '1'	Standby        VIH      Pin 1 Logic '1', Output Enabled      0.7V <sub>CC</sub> -      -        VIL      Pin 1 Logic '0', Output Standby      -      -      0.3V <sub>CC</sub> I <sub>STB</sub> Pin 1 Logic '0', Output Standby      -      -      10        T <sub>PLZ</sub> Pin 1 Logic '1'      -      -      5	

#### **Enable Truth Table**

Pin 1	Pin 3
Logic '1'	Output Enabled
Open	Output Enabled
	Output Disabled,
Logic 'O'	High Impedance

#### Test Circuit



#### Output Waveform



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### **Mechanical Specifications**



#### Recommended Pad Layout



#### **Pin Assignments**

Pin	Symbol	Function
1	EOH	Enable
2	GND	Circuit & Package
3	Output	RF Output
4	V <sub>CC</sub>	Supply Voltage

#### Table I - Date Code, Beginning year 2021

		1	иолтн			FFB	MAD		MAN			ALIC	CED	0.07	NOV	DEC
	YE	AR			JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
2021	2025	2029	2033	2037	А	В	С	D	E	F	G	Н	J	К	L	Μ
2022	2026	2030	2034	2038	Ν	Р	Q	R	S	Т	U	V	W	Х	Y	Ζ
2023	2027	2031	2035	2039	а	b	С	d	е	f	g	h	j	k	I	m
2024	2028	2032	2036	2040	n	р	q	r	S	t	u	V	W	х	У	Z

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### Marking Information

- 1. \*\* Manufacturing Site Code.
- 2. D Date Code. See Table I for codes.
- 3. CHT50 CTS model.
- 4. ST Frequency stability/temperature code. [Refer to Ordering Information]
- V Voltage code; M = 1.8V, N = 2.5V, L = 3.3V, S = 5.0V.
- 6. – Pin 1 identifier.
- xxxx Frequency Code.
  3-digits for frequencies <100MHz</li>
  4-digits for frequencies 100MHz or greater
  [See document 016-1454-0, Frequency Code Tables.]
- 1. Termination pads (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
- Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
- 3. MSL = 1.



### Packaging - Tape and Reel



#### **Reel Drawing**



#### Notes

- 1. Device quantity is 1k pieces maximum per 180mm reel.
- 2. Complete CTS part number, frequency value and date code information must appear on reel and carton labels.



## Addendum

### Common Frequencies Available – MHz

FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE
4.000000	040	24.000000	240	40.000000	400		
8.000000	080	24.576000	24C	48.000000	480		
10.000000	100	25.000000	250	50.000000	500		
12.000000	120	26.000000	260	100.000000	1000		
12.288000	122	27.000000	270	125.000000	1250		
14.318180	143	30.000000	300	156.250000	1562		
14.745600	147	32.000000	320				
16.000000	160	33.333000	33E				
20.000000	200	37.400000	374				
22.118400	221	38.400000	384				