



Customer Information Notification

201305003I

Issue Date: 13-Jun-2013
Effective Date: 14-Jun-2013

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Management Summary

Datasheet update for the LD6805-series. No change of the actual product.

Change Category

<input type="checkbox"/> Wafer Fab process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab materials	<input type="checkbox"/> Assembly Materials	<input checked="" type="checkbox"/> Electrical spec./Test coverage	<input type="checkbox"/> Mechanical Specification
<input type="checkbox"/> Wafer Fab location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Test Location	<input type="checkbox"/> Packing/Shipping/Labeling

Datasheet update for the LD6805-series

Information Notification

The datasheet will be updated to "Rev. 4 - 7 June 2013" for minor changes and additional information. The product itself including the materials will be exactly the same. There is no change in form, fit or function. The updated datasheet can be found on NXP.com:
http://www.nxp.com/documents/data_sheet/LD6805_SER.pdf

Changes in the datasheet are:

- new parameter: limiting value pin EN (Min = -0.5V; Max = +6.0V) (table 5)
- new parameter: operating condition of pin OUT (MIN = -0.5V; MAX = $V_i + 0.3V$) (table 6)
- new parameter: C_ext(in) external load capacitance on pin IN (Min = 0.7 uF; Typical = 1.0 uF) (table 9)
- updated parameter: Vin, Vout, Iout redefined as Vi, Vo, Io
- updated parameter: C_L_ext minimum valued added (previously in footnote) (table 9)
- updated text: reference to current quality specification (chapter 11)
- additional information: diode added in block diagram (figure 2+3)

- additional information: example figure added to explain the marking (figure 8)
- additional information: available packing methods and orientation of the product in the carrier tape (chapter 14)
- additional information:: PSRR explanation updated (chapter 9.1)
- typo correction: PSRR-formular defined positiv (previously negative) (chapter 9.1)
- typo correction: PSRR defined as +75 dB (previously -75dB) (table 8)

Please also see chapter 18 "revision history" in the datasheet for a detailed list of the modifications.

Why do we issue this Information Notification

Datasheet update for the LD6805-series. No change of the actual product.

Identification of Affected Products

Product identification does not change

Impact

no impact to the product's functionality anticipated.

Data Sheet Revision

A new datasheet will be issued

Disposition of Old Products

N/A. No change on the actual product.

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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NXP Quality Management Team.

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