

Data Sheet January 2002

15A, 200V Ultrafast Diodes

The MUR1520 and RURP1520 are ultrafast diodes (t_{rr} < 30ns) with soft recovery characteristics. They have a low forward voltage drop and are of planar, silicon nitride passivated, ion-implanted, epitaxial construction.

These devices are intended for use as energy steering/ clamping diodes and rectifiers in a variety of switching power supplies and other power switching applications. Their low stored charge and ultrafast recovery with soft recovery characteristics minimizes ringing and electrical noise in many power switching circuits, thus reducing power loss in the switching transistor.

Formerly developmental type TA09926.

Ordering Information

PART NUMBER	PACKAGE	BRAND
MUR1520	TO-220AC	MUR1520
RURP1520	TO-220AC	RURP1520

NOTE: When ordering, use the entire part number.

Symbol



Features

•	Ultrafast with Soft Recovery <30ns
•	Operating Temperature
•	Reverse Voltage
•	Avalanche Energy Rated

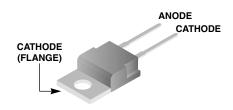
• Planar Construction

Applications

- · Switching Power Supply
- · Power Switching Circuits
- General Purpose

Packaging

JEDEC TO-220AC



Absolute Maximum Ratings T_C = 25°C, Unless Otherwise Specified

	MUR1520 RURP1520	UNITS
Peak Repetitive Reverse Voltage	200	V
Working Peak Reverse Voltage	200	V
DC Blocking Voltage V _R	200	V
Average Rectified Forward Current $I_{F(AV)}$ ($T_C = 157^{\circ}C$)	15	Α
Repetitive Peak Surge Current I _{FRM} (Square Wave 20kHz)	30	Α
Nonrepetitive Peak Surge CurrentIFSM (Halfwave 1 Phase 60Hz)	200	Α
Maximum Power Dissipation	100	W
Avalanche Energy (See Figures 7 and 8)	20	mJ
Operating and Storage Temperature	-55 to 175	°C

 $\textbf{Electrical Specifications} \hspace{0.5cm} \textbf{T}_{C} = 25^{o}\text{C, Unless Otherwise Specified}$

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V _F	I _F = 15A	-	-	1.05	V
	I _F = 15A, T _C = 150°C	-	-	0.85	V
I _R	V _R = 200V	-	-	100	μΑ
	$V_R = 200V, T_C = 150^{\circ}C$	-	-	500	μΑ
t _{rr}	$I_F = 1A$, $dI_F/dt = 100A/\mu s$	-	-	30	ns
	$I_F = 15A$, $dI_F/dt = 100A/\mu s$	-	-	35	ns
t _a	$I_F = 15A$, $dI_F/dt = 100A/\mu s$	-	20	-	ns
t _b	$I_F = 15A$, $dI_F/dt = 100A/\mu s$	-	10	-	ns
R _{θJC}		-	-	1.5	°C/W

DEFINITIONS

 V_F = Instantaneous forward voltage (pw = 300 μ s, D = 2%).

I_R = Instantaneous reverse current.

 t_{rr} = Reverse recovery time at dI_F/dt = 100A/ μ s (See Figure 6), summation of $t_a + t_b$.

 t_a = Time to reach peak reverse current at dI_F/dt = 100A/ μ s (See Figure 6).

t_b = Time from peak I_{RM} to projected zero crossing of I_{RM} based on a straight line from peak I_{RM} through 25% of I_{RM} (See Figure 6).

 $R_{\theta JC}$ = Thermal resistance junction to case.

pw = pulse width.

D = duty cycle.

Typical Performance Curves

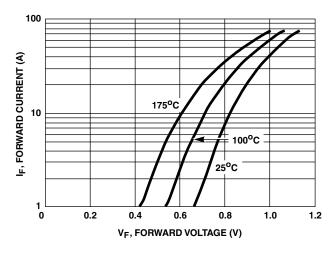


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

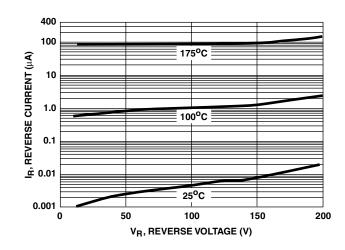


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

Typical Performance Curves (Continued)

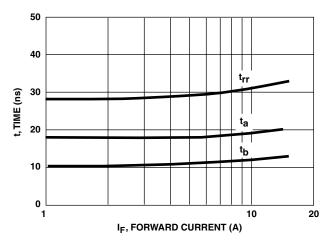


FIGURE 3. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

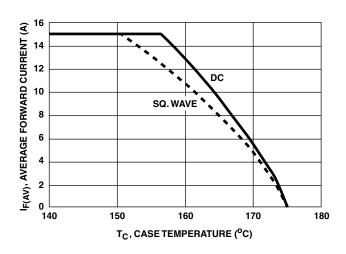


FIGURE 4. CURRENT DERATING CURVE

Test Circuits and Waveforms

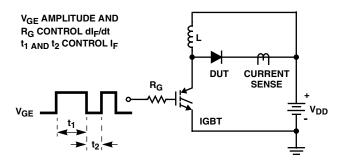


FIGURE 5. t_{rr} TEST CIRCUIT

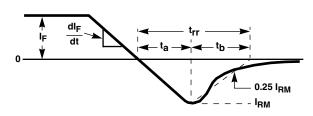


FIGURE 6. t_{rr} WAVEFORMS AND DEFINITIONS

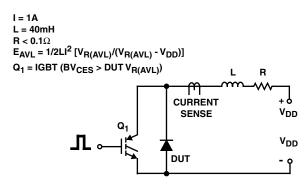


FIGURE 7. AVALANCHE ENERGY TEST CIRCUIT

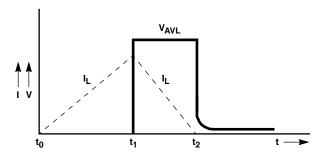


FIGURE 8. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

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