

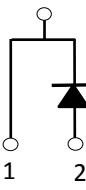
Description

United Silicon Carbide, Inc. offers the 3rd generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175°C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

CASE



CASE



Part Number	Package	Marking
UJ3D06510TS	TO-220-2L	UJ3D06510TS

Features

- ◆ 175°C maximum operating junction temperature
- ◆ Easy paralleling
- ◆ Extremely fast switching not dependent on temperature
- ◆ No reverse or forward recovery
- ◆ Enhanced surge current capability, MPS structure
- ◆ Excellent thermal performance, Ag sintered
- ◆ 100% UIS tested
- ◆ AEC-Q101 qualified

Typical Applications

- ◆ Power converters
- ◆ Industrial motor drives
- ◆ Switching-mode power supplies
- ◆ Power factor correction modules

Maximum Ratings

Parameter	Symbol	Test Conditions	Value	Units
DC blocking voltage	V _R		650	V
Repetitive peak reverse voltage, T _j =25°C	V _{RRM}		650	V
Surge peak reverse voltage	V _{RSM}		650	V
Maximum DC forward current	I _F	T _C = 152°C	10	A
Non-repetitive forward surge current sine halfwave	I _{FSM}	T _C = 25°C, t _p = 10ms	70	A
		T _C = 110°C, t _p = 10ms	60	
Repetitive forward surge current sine halfwave, D=0.1	I _{FRM}	T _C = 25°C, t _p = 10ms	45.9	A
		T _C = 110°C, t _p = 10ms	28.7	
Non-repetitive peak forward current	I _{F,max}	T _C = 25°C, t _p = 10μs	455	A
		T _C = 110°C, t _p = 10μs	455	
i ² t value	∫ i ² dt	T _C = 25°C, t _p = 10ms	24.5	A ² s
		T _C = 110°C, t _p = 10ms	18	
Power dissipation	P _{Tot}	T _C = 25°C	136.4	W
		T _C = 152°C	20.9	
Maximum junction temperature	T _{J,max}		175	°C
Operating and storage temperature	T _J , T _{STG}		-55 to 175	°C
Soldering temperatures, wavesoldering only allowed at leads	T _{sold}	1.6mm from case for 10s	260	°C

Electrical Characteristics

$T_J = +25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test Conditions	Value			Units
			Min	Typ	Max	
Forward voltage	V_F	$I_F = 10\text{A}, T_J = 25^\circ\text{C}$	-	1.5	1.7	V
		$I_F = 10\text{A}, T_J = 150^\circ\text{C}$	-	1.68	2.0	
		$I_F = 10\text{A}, T_J = 175^\circ\text{C}$	-	1.75	2.1	
Reverse current	I_R	$V_R=650\text{V}, T_J=25^\circ\text{C}$	-	10	60	μA
		$V_R=650\text{V}, T_J=175^\circ\text{C}$	-	150	-	
Total capacitive charge ⁽¹⁾	Q_C	$V_R=400\text{V}$	-	23	-	nC
Total capacitance	C	$V_R=1\text{V}, f=1\text{MHz}$	-	327	-	pF
		$V_R=300\text{V}, f=1\text{MHz}$	-	38	-	
		$V_R=600\text{V}, f=1\text{MHz}$	-	34	-	
Capacitance stored energy	E_C	$V_R=400\text{V}$	-	3.4	-	μJ

(1) Q_C is independent on T_J , di_F/dt , and I_F as shown in the application note USCI_AN0011.

Thermal characteristics

Parameter	symbol	Test Conditions	Value			Units
			Min	Typ	Max	
Thermal resistance, junction - case	R_{0JC}		-	0.82	1.1	$^\circ\text{C/W}$

Typical Performance

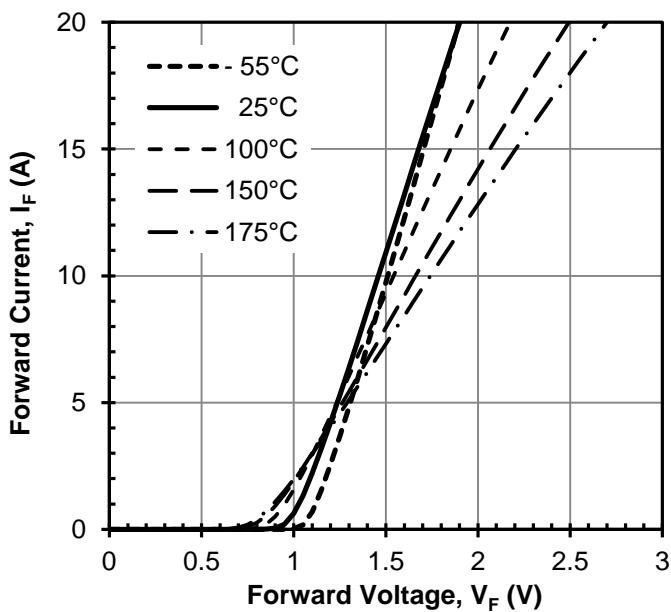


Figure 1 Typical forward characteristics

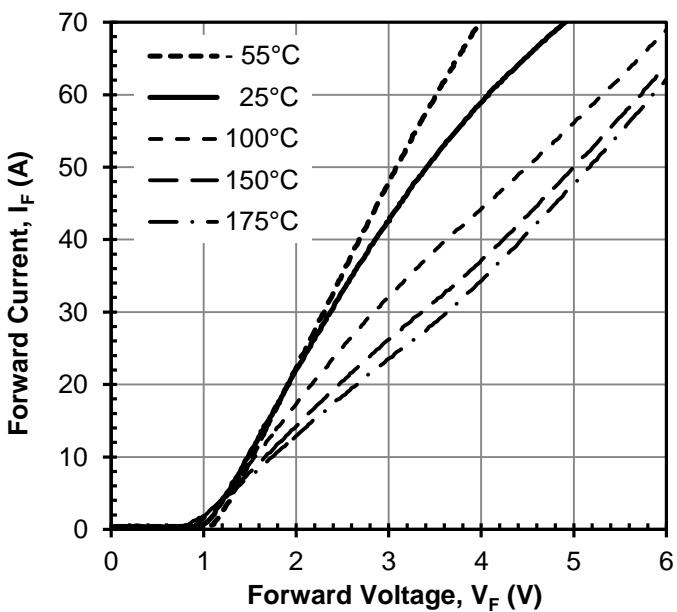


Figure 2 Typical forward characteristics in surge current

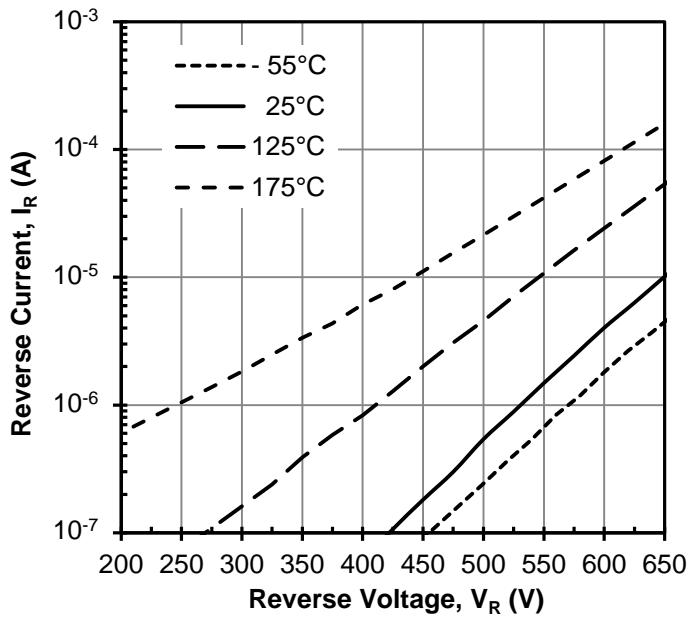


Figure 3 Typical reverse characteristics

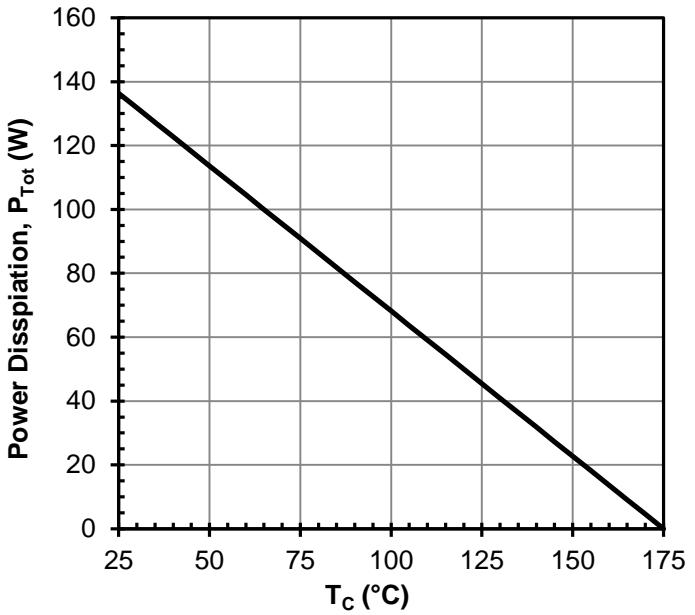


Figure 4 Power dissipation

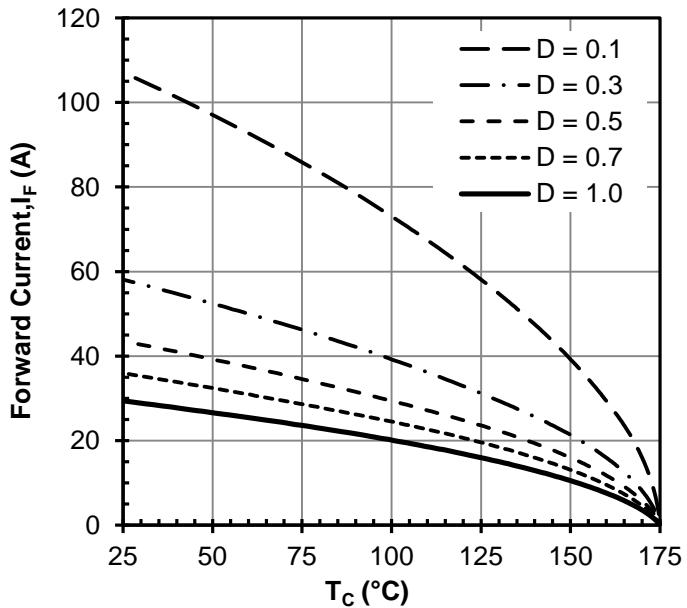


Figure 5 Diode forward current

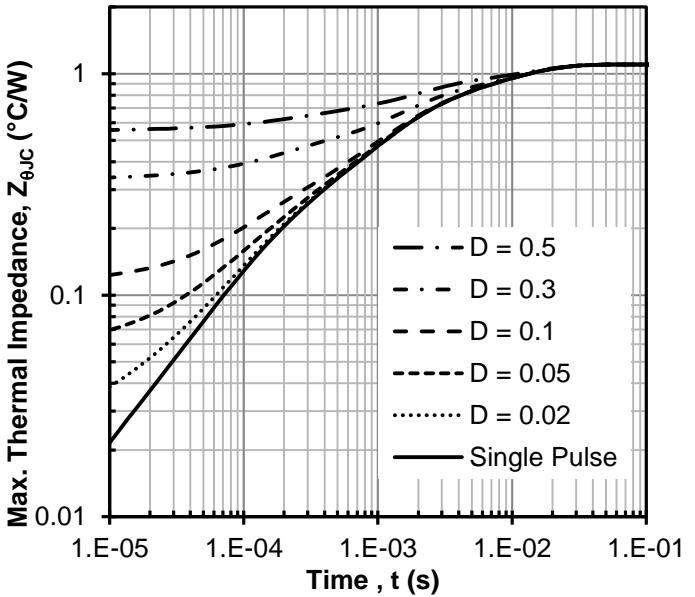


Figure 6 Maximum transient thermal impedance

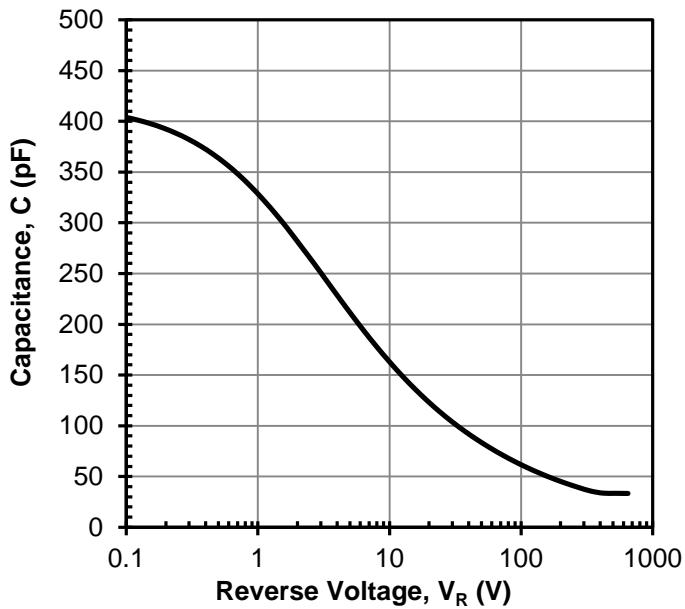


Figure 7 Capacitance vs. reverse voltage at 1MHz

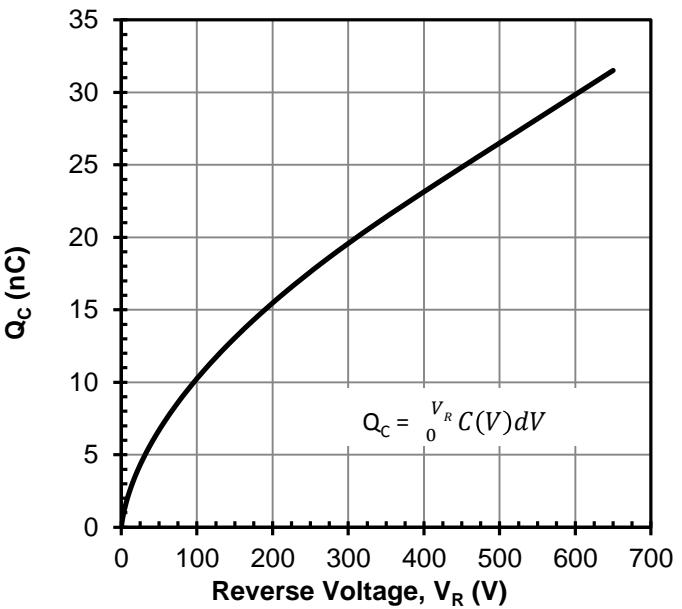


Figure 8 Typical capacitive charge vs. reverse voltage

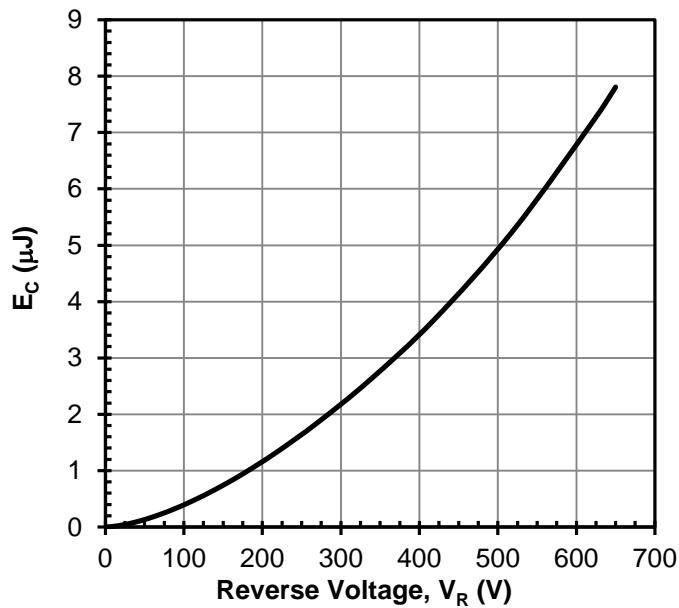


Figure 9 Typical capacitance stored energy vs. reverse voltage

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