

LOW-JITTER SAW OSCILLATOR (SPSO) **OUTPUT: LV-PECL, LVDS, HCSL**

EG-4121/4101CA

: 100 MHz to 700 MHz : 2.5 V ··· EG-4121CA 3.3 V ··· EG-4101CA : LV-PECL or LVDS or HCSL •Frequency range •Supply voltage Function Output enable (OE) •External dimensions : $7.0 \times 5.0 \times 1.2 \text{ mm}$

•Very low jitter and low phase noise by SAW unit.



Specifications (characteristics)

► Differential LV-PECL Output

lk	Symbol	EG-4121CA P	EG-4101CA P	Conditions	/ Romarka	
Item	Symbol	LV-PECL		Conditions	Conditions / Remarks	
Output frequency range	fo	100 MHz to 700 MHz		Please contact us about available frequencies.		
Supply voltage	Vcc	2.5 V ±0.125 V 3.3 V ±0.33 V				
Storage temperature	T_stg	-55 °C to +125 °C		Storage as single product.		
Operating temperature	T_use	W:-40 °C to +85 °C				
Frequency tolerance	f_tol	G: ± 50 × 10 ⁻⁶				
Current consumption	Icc	60 mA Max.		OE=Vcc, L_ECL =50 Ω		
Disable current	I_dis	2 mA Max.		OE=GND		
Symmetry	SYM	45 % to 55 %		at outputs crossing point		
	Vон	1.55 V Typ.	2.35 V Typ.			
Output voltage	VOH	Vcc-1.025 V to Vcc-0.88 V		DC characteristics		
	Vol	0.8 V Typ.	1.6 V Typ.	DC Characteristics		
	VOL	Vcc-1.81 V to Vcc-1.62 V				
Output load condition (ECL)	L_ECL	50 Ω		Terminated to Vcc -2.0 V		
Input voltage	ViH	70 % Vcc Min.		OE terminal		
, ,	VIL	30 % Vcc Max.				
Rise time / Fall time	tr / tf	400 ps Max.		Between 20 % and 80 % of (VoH-VoL)		
Start-up time	t_str	10 ms Max.		Time at minimum supply voltage to be 0 s		
Phase Jitter		0.23 ps Max.		$100~MHz \leq f_0 < 150~MHz$		
		0.22 ps Max.		$150 \text{ MHz} \le f_0 < 200 \text{ MHz}$	Offset frequency: 12 kHz to 20 MHz	
		0.21 ps Max.		$200~MHz \leq f_0 < 300~MHz$		
	tpJ	0.18 ps Max.		$300~MHz \leq f_0 < 400~MHz$		
		0.16 ps Max.		$400~MHz \leq f_0 < 500~MHz$		
		0.14 ps Max.		$500 \text{ MHz} \le f_0 < 600 \text{ MHz}$		
		0.10 ps Max.		$600 \text{ MHz} \le f_0 \le 700 \text{ MHz}$		

LVDS Output

ltem	Cumbal	EG-4121CA L	EG-4101CA L	Condition	on / Damorka
	Symbol	LVDS		Conditions / Remarks	
Output frequency range	fo	100 MHz to 700 MHz		Please contact us about available frequencies.	
Supply voltage	Vcc	2.5 V ±0.125 V 3.3 V ±0.33 V			
Storage temperature	T_stg	-55 °C to +125 °C		Storage as single product.	
Operating temperature	T_use	W:-40 °C to +85 °C			
Frequency tolerance	f_tol	G: ± 50 × 10 ⁻⁶			
Current consumption	Icc	30 mA Max		OE=Vcc, L_LVDS=100 Ω	
Disable current	I_dis	15 mA Max		OE=GND	
Symmetry	SYM	45 % to 55 %		at outputs crossing point	
	Vod	350 mV Typ. 247 mV to 454 mV		Vod1, Vod2	
Output voltage	dVod	50 mV Max.		dVod = Vod1-Vod2	DC characteristics
	Vos	1.25 V Typ. 1.125 V to 1.375 V		Vos1, Vos2	
	dVos	150 mV Max.		dVos = Vos1-Vos2	
Output load condition (LVDS)	L_LVDS	100 Ω		Connected between OUT to OUT	
Innuit voltage	ViH	70 % Vcc Min.		OE terminal	
	VIL	30 % Vcc Max.			
Rise time / Fall time	tr / tf	400 ps Max.		Between 20 % and 80 % of Differential Output Peak to Peak voltage.	
Start-up time	t_str	10 ms Max.		Time at minimum supply voltage to be 0 s	
Phase Jitter		0.27 ps Max. 0.24 ps Max. 0.23 ps Max.		$100 \text{ MHz} \le f_0 < 150 \text{ MHz}$	
				$150~MHz \leq f_0 < 200~MHz$	Offset frequency: -12 kHz to 20 MHz
	tpJ			200 MHz ≤ f ₀ < 300 MHz	
		0.19 ps Max.		300 MHz ≤ fo < 400 MHz	
		0.16 ps Max.		400 MHz ≤ fo < 500 MHz	
		0.14 ps Max.		500 MHz ≤ fo < 600 MHz	
		0.10 ps Max.		$600 \text{ MHz} \le f_0 \le 700 \text{ MHz}$	

W -40 to +85°C



► HCSL Output

		EG-4121CA H	EG-4101CA H	0 1111	15	
Item	Symbol	HCSL		Condition	Conditions / Remarks	
Output frequency range	fo	100 MHz to 200 MHz		Please contact us about available frequencies.		
Supply voltage	Vcc	2.5 V ±0.125 V	3.3 V ±0.3 V			
Storage temperature	T_stg	-55 °C to +125 °C		Storage as single product.		
Operating temperature	T_use	W:-40 °C to +85 °C				
Frequency tolerance	f_tol	G: ± 50 × 10 ⁻⁶				
Current consumption	Icc	35 mA Max.		OE=Vcc,L_HCSL=50 Ω		
Disable current	I_dis	15 mA Max.		OE=GND		
Symmetry	SYM	45 % to 55 %		at outputs crossing point		
Output Voltage	Voн	0.75 V Typ. -0.3 V Typ.		DC characteristics	DC characteristics	
	Vol			DC characteristics		
Output load condition (HCSL)	L_HCSL	50 Ω		Terminated to GND		
Input voltage	ViH	70 % Vcc Min.		OE terminal	OF terminal	
	VIL	30 % Vcc Max.		OE terminal		
Rise time / Fall time	tr / tf	500 ps Max.		Between 0.175 V and 0.525 V of output		
Start-up time	t_str	10 ms Max.		Time at minimum supply voltage to be 0 s		
Phase Jitter	tpJ	0.3 ps Max. 0.4 ps Max.		fo≤ 160 MHz	Offset frequency:	
				160 MHz < f ₀ ≤ 175 MHz	12 kHz to 20 MHz	
		0.2 ps	s Max.	fo >175 MHz	12 KI IZ IO ZO IVITIZ	

Product Name

EG-4121 CA 250.000000MHz P G W A

(Standard form)

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②Package type ③Frequency ④Output(P:LV-PECL, L:LVDS, H: HCSL)

⑤Frequency tolerance ⑥Operating temperature

⑦Frequency aging (A*1: Frequency tolerance include aging)

*1 This includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, and aging(+25 °C,10 years).

Table 2 Jitter

Item	Symbol	Specifications	Remarks
	tDJ	0.3 ps Typ.	Deterministic Jitter
	trj	2 ps Typ.	Random Jitter
Jitter *	trms	2 ps Typ.	σ (RMS of total distribution)
	t _{p-p}	20 ps Typ.	Peak to Peak
	tacc	3 ps Typ.	Accumulated Jitter(σ) n=2 to 50000 cycles

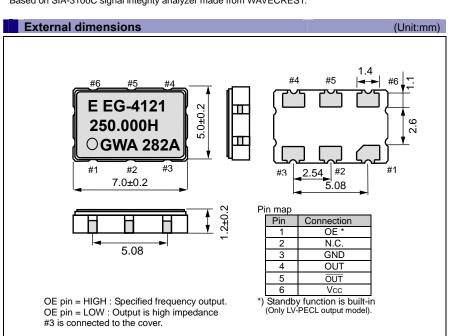
* Tested using a DTS-2075 Digital timing system made by WAVECREST with jitter analysis software VISI6. * Based on SIA-3100C signal integrity analyzer made from WAVECREST.

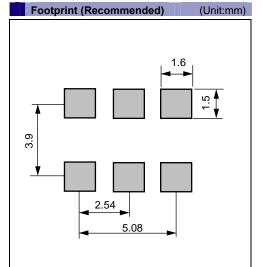
: LV-PECL, LVDS output

: HCSL output

⑤Frequency tolerance

±50 × 10⁻⁶





To maintain stable operation, provide a 0.01 μF to 0.1 µF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
 - *About the products without the Pb-free mark.

 Contains Pb in products exempted by EU RoHS directive.

 (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

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