RoHS

HALOGEN

FREE





Load Switch with Level-Shift

PRODUCT SUMMARY					
V _{DS2} (V) (V _{IN})	$R_{DS(on)}(\Omega)$	I _D (A)			
	0.060 at V _{IN} = 4.5 V	2.8			
1.8 to 12	0.095 at V _{IN} = 2.5 V	2.2			
	0.130 at V _{IN} = 1.8 V	1.9			

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- 60 mΩ Low R_{DS(on)} TrenchFET[®]
- 1.8 V to 12 V Input
- 1.5 V to 8 V Logic Level Control
- Low Profile, Small Footprint TSOP-6 Package
- 3000 V ESD Protection On Input Switch, VON/OFF
- Adjustable Slew-Rate
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

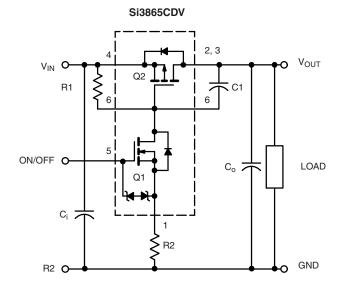
· Load Switching for Portable Devices

shift to drive the P-Channel load-switch. The N-Channel MOSFET has internal ESD protection and can be driven by logic signals as low as 1.5 V. The Si3865CDV operates on supply lines from 1.8 to 12 V, and can drive loads up to 2.8 A.

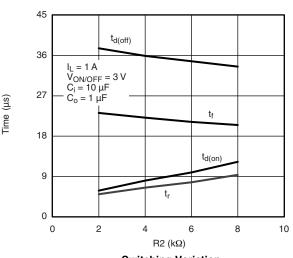
DESCRIPTION

The Si3865CDV includes a P- and N-Channel MOSFET in a single TSOP-6 package. The low on-resistance P-Channel TrenchFET is tailored for use as a load switch. The N-Channel, with an external resistor, can be used as a level-

APPLICATION CIRCUITS



COI	MPONENTS	
R1	Pull-Up Resistor	Typical 10 k Ω to 1 M Ω^*
R2	Optional Slew-Rate Control	Typical 0 to 100 k Ω^*
C1	Optional Slew-Rate Control	Typical 1000 pF



Switching Variation R2 at V_{IN} = 2.5 V, R1 = 20 k Ω

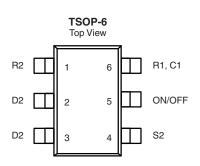
The Si3865CDV is ideally suited for high-side load switching in portable applications. The integrated N-Channel level-shift device saves space by reducing external components. The slew rate is set externally so that rise-times can be tailored to different load types.

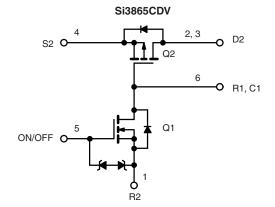
Note:

^{*} Minimum R1 value should be at least 10 x R2 to ensure Q1 turn-on at 1.8 V input.

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FUNCTIONAL BLOCK DIAGRAM





Ordering Information: Si3865CDV-T1-E3 (Lead (Pb)-free)

Si3865CDV-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	Γ _A = 25 °C, unle	ess otherwise r	noted)		
Parameter		Symbol	Limit	Unit	
Input Voltage		V _{IN} (V _{DS2})	12		
Gate-Source Voltage		V_{GS2}	8	V	
ON/OFF Voltage		V _{ON/OFF} (V _{GS1})	8		
Load Current	Continuous ^{a, b}	L	± 2.8		
Load Gurrent	Pulsed ^{b, c}	'L	± 6	Α	
Continuous Intrinsic Diode Conduction ^a	Is	-1			
Maximum Power Dissipation ^a		P _D	0.83	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	
ESD Rating, MIL-STD-883D Human Body Model (100 pF, 1500 Ω)		ESD	3	kV	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient (continuous current) ^a	R _{thJA}	130	150	°C/W	
Maximum Junction-to-Foot (Q2)	R _{thJF}	75	90	C/ VV	

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
OFF Characteristics								
Reverse Leakage Current	I _{FL}	V _{IN} = 12 V, V _{ON/OFF} =	= 0 V			1	μΑ	
Diode Forward Voltage	V_{SD}	I _S = - 1 A		- 0.77	- 1	V		
ON Characteristics								
Input Voltage Range	V _{IN} (V _{DS2})		1.8		12	V		
	R _{DS(on)}	V _{ON/OFF} = 1.5 V, I _D = 1 A	V _{IN} = 4.5 V		0.050	0.060	Ω	
On-Resistance (P-Channel) at 1 A			V _{IN} = 2.5 V		0.073	0.095		
			V _{IN} = 1.8 V		0.100	0.130		
On Chata (D. Channal) Dunin Courset	I _{D(on)}	$V_{IN-OUT} \le 0.2 \text{ V}, V_{IN} = 5 \text{ V}, V_{ON/OFF} = 1.5 \text{ V}$		1			۸	
On-State (P-Channel) Drain-Current		$V_{IN-OUT} \le 0.3 \text{ V}, V_{IN} = 3 \text{ V}, V_{ON/OFF} = 1.5 \text{ V}$		1			Α	

Notes:

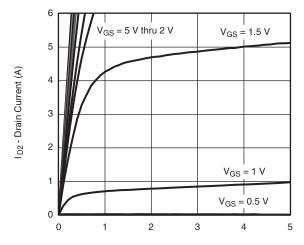
- a. Surface Mounted on FR4 board.
- b. V_{IN} = 8 V, $V_{ON/OFF}$ = 8 V, T_A = 25 °C. c. Pulse test: pulse width \leq 300 μ s, duty cycle \leq 2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



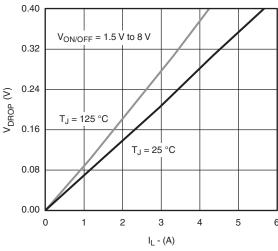


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

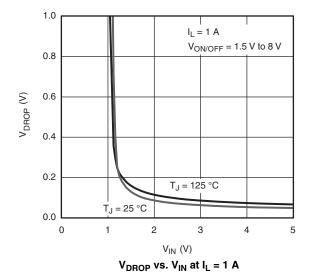


V_{DS2} - Drain-to-Source Voltage (V)



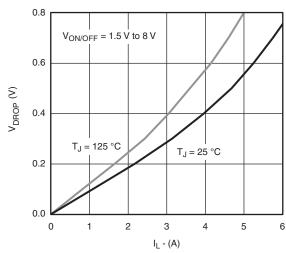


 V_{DROP} vs. I_L at V_{IN} = 2.5 V

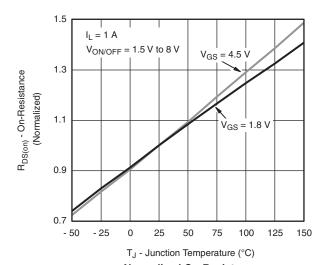


0.32 V_{ON/OFF} = 1.5 V to 8 V 0.24 E 0.00 0.00 0 1 2 3 4 5 6 I_L - (A)

 V_{DROP} vs. I_L at V_{IN} = 4.5 V



 V_{DROP} vs. I_L at V_{IN} = 1.8 V

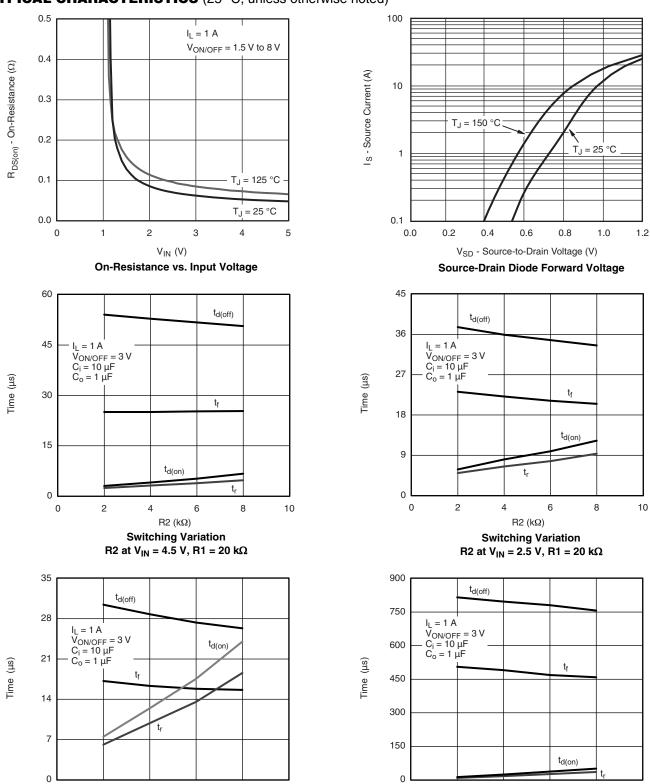


Normalized On-Resistance vs. Junction Temperature

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



0

20

40

60

R2 ($k\Omega$)

Switching Variation

R2 at V_{IN} = 4.5 V, R1 = 300 k Ω

2

0

6

R2 ($k\Omega$)

Switching Variation

R2 at $V_{IN} = 1.8 \text{ V}$, R1 = 20 k Ω

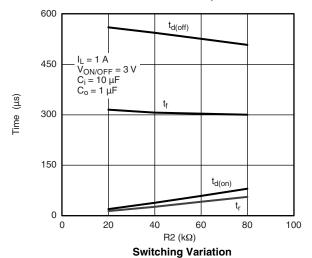
80

100





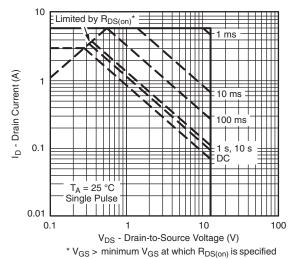
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



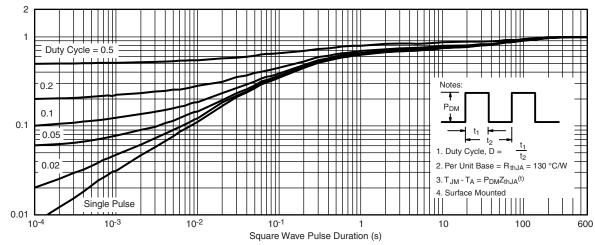
R2 at V_{IN} = 2.5 V, R1 = 300 k Ω

500 $t_{d(off)} \\$ 400 $I_L = 1 \text{ A}$ $V_{ON/OFF} = 3 \text{ V}$ $C_i = 10 \text{ }\mu\text{F}$ 300 Time (µs) $C_0 = 1 \mu F$ 200 100 t_{d(on)} 0 40 60 20 100 0 R2 $(k\Omega)$

Switching Variation R2 at $\rm V_{IN}$ = 1.8 V, R1 = 300 $k\Omega$







Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?69010.

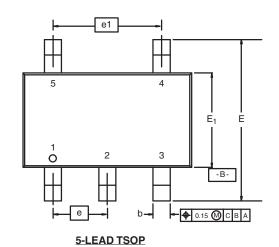
Normalized Effective Transient Thermal Impedance

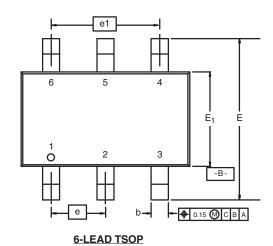


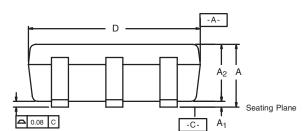


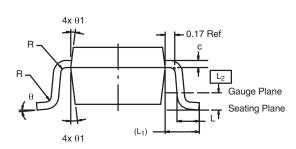
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C







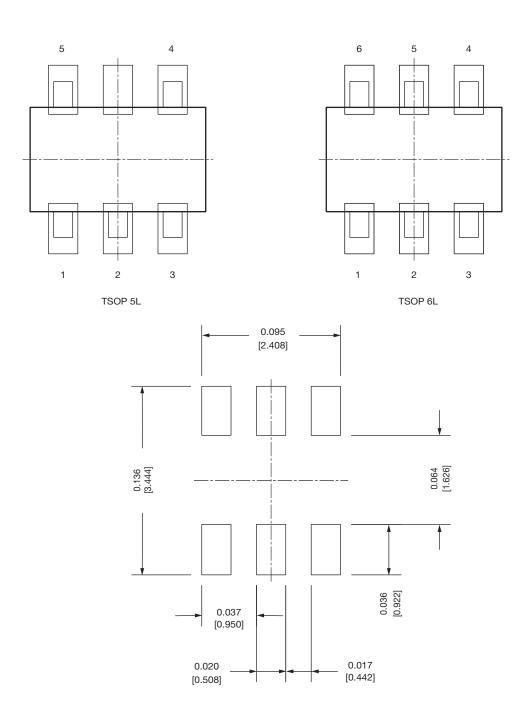


	MIL	LIMETER	RS	ı	NCHES	
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.91	-	1.10	0.036	-	0.043
A ₁	0.01	-	0.10	0.0004	-	0.004
A ₂	0.90	-	1.00	0.035	0.038	0.039
b	0.30	0.32	0.45	0.012	0.013	0.018
С	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E ₁	1.55	1.65	1.70	0.061	0.065	0.067
е		0.95 BSC		0.0374 BSC		
e ₁	1.80	1.90	2.00	0.071 0.075 0.0		
L	0.32	-	0.50	0.012	-	0.020
L ₁		0.60 Ref			0.024 Ref	
L ₂	0.25 BSC				0.010 BSC	
R	0.10	-	-	0.004	-	-
θ	0°	4°	8°	0°	4°	8°
θ_1		7° Nom			7° Nom	
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540						

Document Number: 71200 www.vishay.com 18-Dec-06



Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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