PCN Number:		20190809000			PCN Date:	Au	Aug. 26, 2019		
Title: Datasheet for TMS320x									
Customer Contact:		PCN Manager				D	ept:	Quality Services	
Change Type:									
Assembly Site			Design			Wafer	Bump Site		
Assembly Process		\square	Data Sheet			Wafer	Bump Material		
Assembly Materials			Part number change			Wafer	Bump Process		
Mechanical Specification			Test Site			Wafer	Fab Site		
Packing/Shipping/Labeling			Test Process			Wafer	Fab Materials		
							Wafer	Fab Process	
Notification Details									
Description of Change:									
Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.									
TMS320F2809, TMS320F2808, TMS320F2806, TMS320F2802, TMS320F2801 TMS320C2802, TMS320C2801, TMS320F28016, TMS320F28015 SPRS2300 - OCTOBER 2003-REVISED MARCH 2019									

Changes from May 31, 2012 to March 11, 2019 (from N Revision (May 2012) to O Revision)

Page

•	Global: Restructured document.	. 1
•	Global: Replaced "DSP/BIOS" with "SYS/BIOS".	
•	Global: Changed "CAN 2.0B" to "ISO11898-1 (CAN 2.0B)".	<u>1</u>
•	Global: Removed references to the Reliability Data for TMS320LF24xx and TMS320F28xx Devices Application Report (SPRA963).	. 1
•	Section 1 (Device Overview): Changed section title from "F280x, F2801x, C280x DSPs" to "Device Overview"	1
•	Section 1.1 (Features): Removed "Dynamic PLL Ratio Changes Supported" feature.	- 1
•	Section 1.1: Added "(AEC-Q100 Qualification for Automotive Applications)" to Q temperature option	1
•	Section 1.2 (Applications): Added section.	- <u>4</u>
•	Section 1.3 (Description): Added section.	- <u>2</u>
•	Section 1.4 (Functional Block Diagram): Added section.	<u>3</u>
•	Section 3 (Device Comparison): Added section.	<u>/</u>
•	Table 3-1 (Device Comparison (100-MHz Devices)): Changed title from "Hardware Features (100-MHz	_
	Devices)" to "Device Companison (100-MHz Devices)".	<u>(</u>
•	Table 3-1: Changed "PWM outputs" to "PWM channels".	<u>(</u>
•	Table 3-1: Changed "PWM outputs" to "PWM channels". Table 3-1: Changed "PWM outputs" to "PWM channels". Table 3-1: Added "(AEC-Q100 Qualification)" after Q temperature range. Table 3-1: Removed "Product status" row.	<u>(</u>
•	Table 3-1: Removed "Product status" row.	<u>7</u>
•	Table 3-2 (Device Comparison (60-MHz Devices)): Changed title from "Hardware Features (60-MHz Devices)" to "Device Comparison (60-MHz Devices)".	. 8
•	Table 3-2: Changed "PWM outputs" to "PWM channels".	. 8
	Table 3-2: Changed "PWM outputs" to "PWM channels". Table 3-2: Added "(AEC-Q100 Qualification)" after Q temperature range.	
	Table 3-2: Removed "Product status" row	8
	Table 3-2: Removed "Product status" row. Section 3.1 (Related Products): Added section.	9
	Section 4 (Terminal Configuration and Functions): Added section.	10
	Section 4.1 (Pin Diagrams): Changed section title from "Pin Assignments" to "Pin Diagrams".	
	Table 4-1 (Signal Descriptions): Updated DESCRIPTION of XRS.	15
	Section 5.2 (ESD Ratings – Automotive): Added section	22
	Section 5.2 (ESD Ratings – Automotive): Added section. Section 5.3 (ESD Ratings – Commercial): Added section.	22
•	Section 5.4 (Recommended Operating Conditions): Changed "Q version (Q100 Qualification)" to "Q version	
	(AEC-Q100 Qualification)".	<u>22</u>
•	Section 5.5 (Power Consumption Summary): Changed section title from "Current Consumption" to "Power Consumption Summary". Section 5.13 (Thermal Design Considerations): Added section.	23
•	Section 5.13 (Thermal Design Considerations): Added section.	33
•	Section 5.14 (Timing and Switching Characteristics): Added section. Section 5.14.2 (Power Sequencing): Updated "No voltage larger than a diode drop" paragraph.	34
•	Section 5.14.2 (Power Sequencing): Updated "No voltage larger than a diode drop" paragraph.	36
•	Section 5.14.2: Removed "Power Management and Supervisory Circuit Solutions" section.	36
•	Figure 5-12 (General-Purpose Input Timing): Changed XCLKOUT to SYSCLK.	44
•	Figure 5-16 (PWM Hi-Z Characteristics): Changed XCLKOUT to SYSCLK.	48
•	Table 5-24 (High-Resolution PWM Characteristics at SYSCLKOUT = 60-100 MHz): Updated footnote.	
•	Section 5.14.4.5.1 (SPI Master Mode Timing): Updated section.	
•	Section 5.14.4.5.2 (SPI Slave Mode Timing): Updated section.	55
•	Table 5-39 (Flash Parameters at 100-MHz SYSCLKOUT): Added MAX Program Time values and MAX Erase Time values. Updated and added footnotes.	58
	Table 5-41 (Flash Data Retention Duration): Added table.	
	Section 5.16.1 (Migration Issues): Added NOTE about ROM versions of F280x device not being accepted by TI	
-	anymore.	
•	Section 6 (Detailed Description): Changed section title from "Functional Overview" to "Detailed Description"	
•	Section 6.1.6 (ROM): Added NOTE.	<u>69</u>
•	Section 6.2.6 (Enhanced Analog-to-Digital Converter (ADC) Module): Updated equations by which the digital	
	value of the input analog voltage is derived.	<u>85</u>
•	Section 6.2.9 (Serial Peripheral Interface (SPI) Modules (SPI-A, SPI-B, SPI-C, SPI-D)): Updated "Rising edge	
	with phase delay" clockng scheme	<u>99</u>
•		
•	Table 6-28 (PIE Peripheral Interrupts): Added footnote about ADCINT.	122
• F	igure 6-30 (Watchdog Module): Updated figure. ection 7 (Applications, Implementation, and Layout): Added section.	130 132
-	Section & (Device and Decumentation Support): Added section	122
	Section 8 (Device and Documentation Support): Added section.	133
	Figure 8-1 (Example of TMS320x280x/2801x Device Nomenclature): Changed "(Q100 qualification)" to "(AEC-	405
	Q100 qualification)".	
	Section 8.3 (Tools and Software): Added section.	
	Section 8.4 (Documentation Support): Updated section.	
•	Section 8.5 (Related Links): Added section.	139

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The datasheet number	will be changing.					
Device Family	Cł	nange From:	Change To:			
TMS320x	SF	PRS230N	SPRS2300			
These changes may be	reviewed at the data	sheet links provided.				
http://www.ti.com/product/TMS320F2809						
Reason for Change:						
To accurately reflect de	vice characteristics.					
Anticipated impact of	n Fit, Form, Functio	on, Quality or Reliabil	ity (positive / negative):			
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.						
Changes to product identification resulting from this PCN:						
None.						
Product Affected:						
TMS320F28015PZA	TMS320F2801PZS	TMS320F2806P2	ZA TMS320F2808PZS			
TMS320F28015PZQ	TMS320F2801PZS-6	0 TMS320F2806P2	ZQ TMS320F2808ZGMA			
TMS320F28015PZS	TMS320F2801ZGM/	A TMS320F2806P2	ZS TMS320F2808ZGMS			
TMS320F28015PZSR	TMS320F2802PZA	TMS320F2806Z0	GMA TMS320F2809GGMA			
TMS320F28015ZGMA	TMS320F2802PZA-6	50 TMS320F2806Z0	GMS TMS320F2809GGMS			
TMS320F28016PZA	TMS320F2802PZQ	TMS320F2808G	GMA TMS320F2809PZA			
TMS320F28016PZQ TMS320F2802PZ		TMS320F2808G	GMS TMS320F2809PZQ			
TMS320F28016PZS	TMS320F2802PZS-6	0 TMS320F2808P2	ZA TMS320F2809PZS			
TMS320F2801PZA	TMS320F2802ZGM/	A TMS320F2808P2	ZAR TMS320F2809ZGMA			
TMS320F2801PZA-60	TMS320F2802ZGMS	5 TMS320F2808P2	ZQ TMS320F2809ZGMS			
TMS320F2801PZQ						

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