HEF4067B-Q100

16-channel analog multiplexer/demultiplexer

Rev. 3 — 6 January 2022

Product data sheet

1. General description

The HEF4067B-Q100 is a single-pole 16-throw analog switch (SP16T) suitable for use in analog or digital 16:1 multiplexer/demultiplexer applications. The switch features four digital select inputs (A0, A1, A2 and A3), sixteen independent inputs/outputs (Yn), a common input/output (Z) and a digital enable input (\overline{E}). When \overline{E} is HIGH, the switches are turned off. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{DD} .

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 3) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 3)
 - Specified from -40 °C to +85 °C
- Wide supply voltage range from 3.0 V to 15.0 V
- CMOS low power dissipation
- · High noise immunity
- · Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- · Standardized symmetrical output characteristics
- ESD protection:
 - MIL-STD-883, method 3015 exceeds 2000 V
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)
- Complies with JEDEC standard JESD 13-B

3. Applications

- · Analog multiplexing and demultiplexing
- Digital multiplexing and demultiplexing
- Signal gating

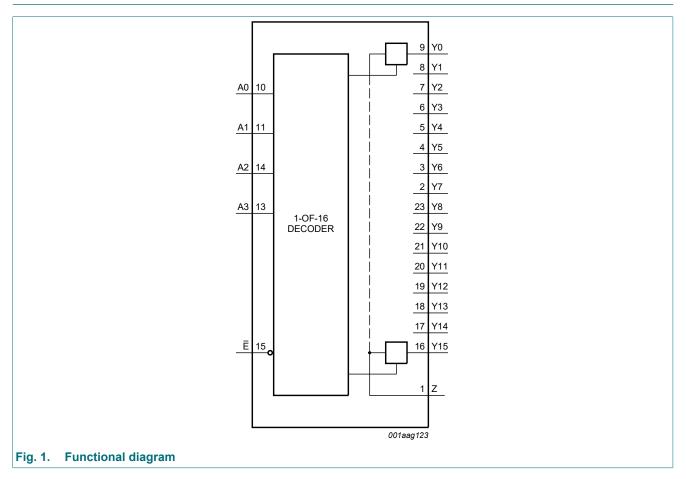
4. Ordering information

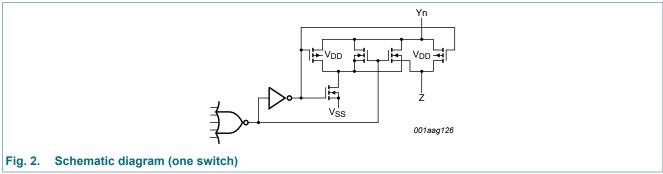
Table 1. Ordering information

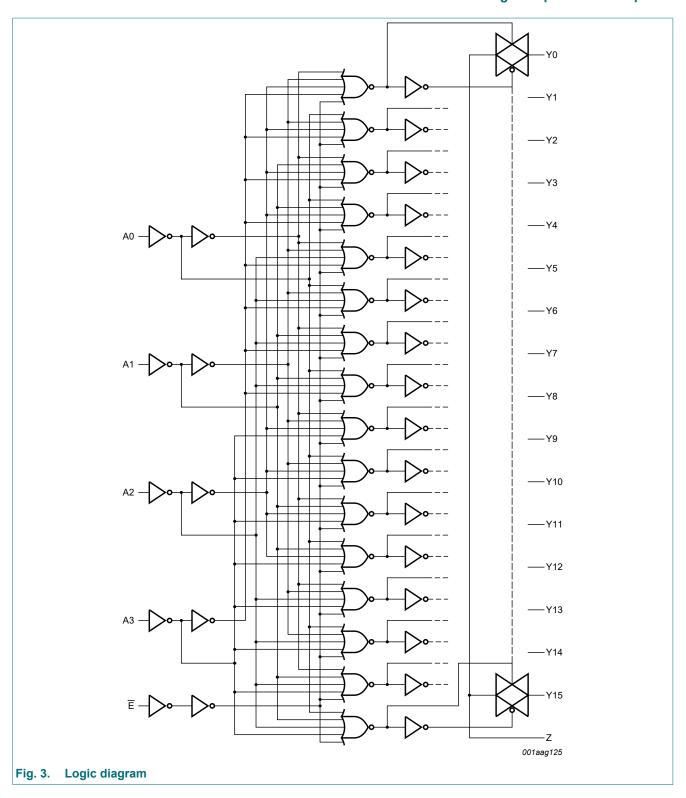
Type number	Package			
	Temperature range	Name	Description	Version
HEF4067BT-Q100	-40 °C to +85 °C	SO24	plastic small outline package; 24 leads; body width 7.5 mm	SOT137-1



5. Functional diagram



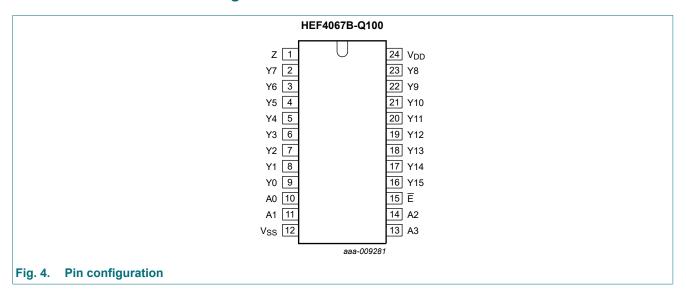




Product data sheet

6. Pinning information

6.1. Pinning



6.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
Z	1	common input/output
	9, 8, 7, 6, 5, 4, 3, 2, 23, 22, 21, 20, 19, 18, 17, 16	independent input/output
A0, A1, A2, A3	10, 11, 14, 13	address input
V _{SS}	12	ground (0 V)
E	15	enable input (active LOW)
V_{DD}	24	supply voltage

4/16

7. Functional description

Table 3. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level; \ X = don't \ care.$

Control	Address				Channel ON
Ē	A3	A2	A1	A0	-
L	L	L	L	L	Y0 = Z
L	L	L	L	Н	Y1 = Z
L	L	L	Н	L	Y2 = Z
L	L	L	Н	Н	Y3 = Z
L	L	Н	L	L	Y4 = Z
L	L	Н	L	Н	Y5 = Z
L	L	Н	Н	L	Y6 = Z
L	L	Н	Н	Н	Y7 = Z
L	Н	L	L	L	Y8 = Z
L	Н	L	L	Н	Y9 = Z
L	Н	L	Н	L	Y10 = Z
L	Н	L	Н	Н	Y11 = Z
L	Н	Н	L	L	Y12 = Z
L	Н	Н	L	Н	Y13 = Z
L	Н	Н	Н	L	Y14 = Z
L	Н	Н	Н	Н	Y15 = Z
Н	X	Х	X	X	none

8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to $V_{SS} = 0 \text{ V}$ (ground).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DD}	supply voltage		-0.5	+18	V
I _{IK}	input clamping current	pins An and \overline{E} ; $V_I < -0.5 \text{ V}$ or $V_I > V_{DD} + 0.5 \text{ V}$	-	±10	mA
VI	input voltage		-0.5	V _{DD} + 0.5	V
I _{I/O}	input/output current	[1]	-	±10	mΑ
I _{DD}	supply current		-	50	mΑ
T _{stg}	storage temperature		-65	+150	°C
T _{amb}	ambient temperature		-40	+85	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +85 °C	-	500	mW
Р	power dissipation	per output	-	100	mW

^[1] To avoid drawing V_{DD} current out of terminal Z, when switch current flows into terminals Yn, the voltage drop across the bidirectional switch must not exceed 0.4 V. If the switch current flows into terminal Z, no V_{DD} current will flow out of terminals Yn, in this case there is no limit for the voltage drop across the switch, but the voltages at Y and Z may not exceed V_{DD} or V_{SS}.

9. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DD}	supply voltage		3	-	15	V
VI	input voltage		0	-	V_{DD}	V
T _{amb}	ambient temperature	in free air	-40	-	+85	°C
Δt/ΔV	input transition rise and fall rate	V _{DD} = 5 V	-	-	3.75	μs/V
		V _{DD} = 10 V	-	-	0.5	μs/V
		V _{DD} = 15 V	-	-	0.08	μs/V

10. Static characteristics

Table 6. Static characteristics

 $V_{SS} = 0 \ V$; $V_I = V_{SS}$ or V_{DD} ; unless otherwise specified.

Symbol	Parameter	Conditions	V_{DD}	T _{amb} =	-40 °C	T _{amb} =	+25 °C	T _{amb} = +85 °C		Unit
				Min	Max	Min	Max	Min	Max	
V _{IL}	LOW-level input	I _O < 1 μA								
	voltage	V _O = 0.5 V or 4.5 V	5 V	-	1	-	1	-	1	V
		V _O = 1.0 V or 9.0 V	10 V	-	2	-	2	-	2	V
		V _O = 1.5 V or 13.5 V	15 V	-	2.5	-	2.5	-	2.5	V
V _{IH}	HIGH-level input	I _O < 1 μA								
	voltage	V _O = 0.5 V or 4.5 V	5 V	4	-	4	-	4	-	V
		V _O = 1.0 V or 9.0 V	10 V	8	-	8	-	8	-	V
		V _O = 1.5 V or 13.5 V	15 V	12.5	-	12.5	-	12.5	-	V
I _I	input leakage current	V _I = 0 V or 15 V	15 V	-	±0.3	-	±0.3	-	±1.0	μΑ
I _{OZ}	OFF-state output	output at V _{DD}	15 V	-	1.6	-	1.6	-	12.0	μΑ
	current	output at V _{SS}	15 V	-	-1.6	-	-1.6	-	-12.0	μΑ
I _{S(OFF)}	OFF-state leakage current	Z port; all channels OFF; see <u>Fig. 5</u>	15 V	-	-	-	1000	-	-	nA
		Yn port; per channel; see Fig. 6	15 V	-	-	-	200	-	-	nA
I _{DD}	supply current	all valid input combinations;	5 V	-	20	-	20	-	150	μΑ
		I _O = 0 A	10 V	-	40	-	40	-	300	μΑ
				-	80	-	80	-	600	μΑ
Cı	input capacitance	digital inputs	15 V	-	-	-	7.5	-	-	pF

10.1. Test circuits

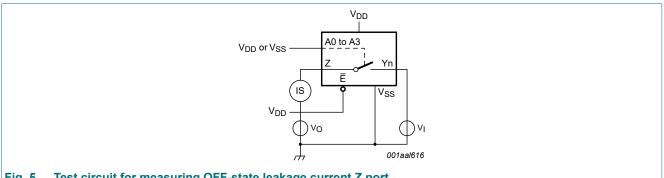
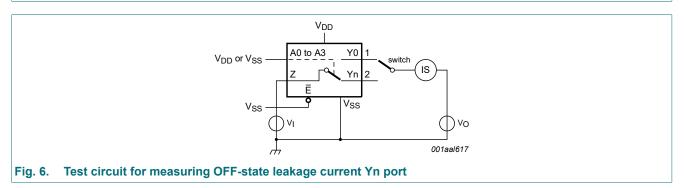


Fig. 5. Test circuit for measuring OFF-state leakage current Z port



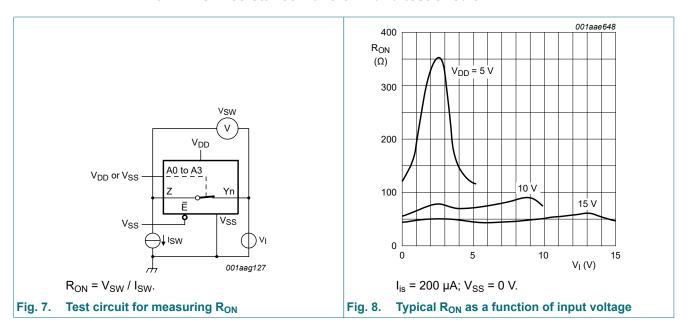
10.2. On resistance

Table 7. ON resistance

 $T_{amb} = 25 \, ^{\circ}C; \, I_{SW} = 200 \, \mu A; \, V_{SS} = 0 \, V.$

Symbol	Parameter	Conditions	V_{DD}	Тур	Max	Unit
R _{ON(peak)}	ON resistance (peak)	$V_I = 0 \text{ V to } V_{DD}$; see <u>Fig. 7</u> and <u>Fig. 8</u>	5 V	350	2500	Ω
			10 V	80	245	Ω
			15 V	60	175	Ω
R _{ON(rail)}	ON resistance (rail)	V _I = 0 V; see <u>Fig. 7</u> and <u>Fig. 8</u>	5 V	115	340	Ω
			10 V	50	160	Ω
			15 V	40	115	Ω
		V _I = V _{DD} ; see <u>Fig. 7</u> and <u>Fig. 8</u>	5 V	120	365	Ω
			10 V	65	200	Ω
			15 V	50	155	Ω
ΔR_{ON}	ON resistance mismatch	V _I = 0 V to V _{DD} ; see <u>Fig. 7</u>	5 V	25	-	Ω
	between channels		10 V	10	-	Ω
			15 V	5	-	Ω

10.2.1. On resistance waveform and test circuit



11. Dynamic characteristics

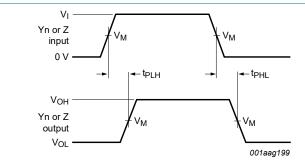
Table 8. Dynamic characteristics

 T_{amb} = 25 °C; V_{SS} = 0 V; for test circuit see Fig. 12.

Symbol	Parameter	Conditions	V_{DD}	Min	Тур	Max	Unit
t _{PHL}	HIGH to LOW propagation delay	Yn, Z to Z, Yn; see Fig. 9	5 V	-	30	60	ns
			10 V	-	15	25	ns
			15 V	-	10	20	ns
		An to Yn, Z; see Fig. 10	5 V	-	190	380	ns
			10 V	-	70	145	ns
			15 V	-	50	100	ns
t _{PLH}	LOW to HIGH propagation delay	Yn, Z to Z, Yn; see Fig. 9	5 V	-	25	50	ns
			10 V	-	10	20	ns
			15 V	-	10	20	ns
		An to Yn, Z; see Fig. 10	5 V	-	175	345	ns
			10 V	-	70	140	ns
			15 V	-	50	100	ns
t _{PHZ}	HIGH to OFF-state propagation delay	E to Yn, Z; see Fig. 11	5 V	-	195	385	ns
			10 V	-	140	280	ns
			15 V	-	130	260	ns
t _{PLZ}	LOW to OFF-state propagation delay	E to Yn, Z; see Fig. 11	5 V	-	215	435	ns
			10 V	-	180	355	ns
			15 V	-	170	340	ns
t _{PZH}	OFF-state to HIGH propagation delay	E to Yn, Z; see Fig. 11	5 V	-	155	315	ns
			10 V	-	70	135	ns
			15 V	-	50	100	ns

Symbol	Parameter	Conditions	V_{DD}	Min	Тур	Max	Unit
t_{PZL}	OFF-state to LOW propagation delay	E to Yn, Z; see Fig. 11	5 V	-	170	340	ns
			10 V	-	70	140	ns
			15 V	-	50	100	ns

11.1. Waveforms and test circuit



Measurement points are given in Table 9.

 $V_{\mbox{\scriptsize OL}}$ and $V_{\mbox{\scriptsize OH}}$ are typical output voltage levels that occur with the output load.

Fig. 9. Yn, Z to Z, Yn propagation delays

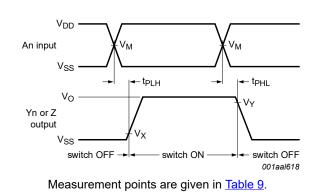
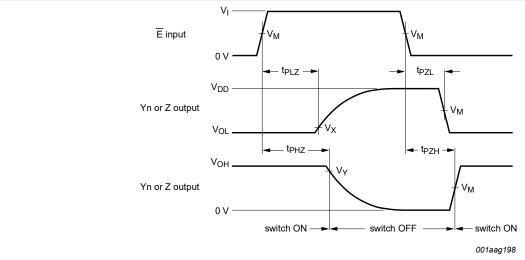


Fig. 10. An to Yn, Z propagation delays



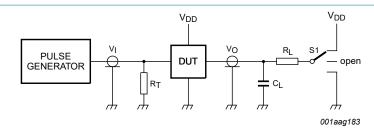
Measurement points are shown in Table 9.

 V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 11. Enable and disable times

Table 9. Measurement points

Supply voltage	Input		Output				
V _{CC}	V _M	V _I	V _M	V_X	V _Y		
5 V to 15 V	0.5 × V _{DD}	GND to V _{DD}	0.5 × V _{DD}	10%	90%		



Test data is given in Table 10.

Definitions test circuit:

R_T = termination resistance should be equal to output impedance Z₀ of the pulse generator;

 C_L = load capacitance including jig and probe capacitance;

R_L = load resistor;

S1 = test selection switch.

Fig. 12. Test circuit for measuring switching times

Table 10. Test data

Input				Load		S1 position				
Yn, Z	An and E	t _r , t _f	V _M	CL	R _L	t _{PHL} [1] t _{PLH} t _{PZH} , t _{PHZ} t _{PZL} , t _{PLZ}			other	
V_{DD} or V_{SS}	V_{DD} or V_{SS}	≤ 20 ns	$0.5 \times V_{DD}$	50 pF	10 kΩ	V_{DD} or V_{SS}	V_{SS}	V_{SS}	V_{DD}	V _{SS}

[1] For Yn to Z or Z to Yn propagation delays use V_{SS} . For An or to Yn or Z propagation delays use V_{DD} .

11.2. Additional dynamic parameters

Table 11. Additional dynamic characteristics

 V_{SS} = 0 V; T_{amb} = 25 °C.

Symbol	Parameter	Conditions	V	OD	Тур	Max	Unit
THD	total harmonic distortion		5 ۱	V	0.25	-	%
		channel ON; $V_I = 0.5 \times V_{DD}$ (p-p); $f_i = 1$ kHz	10) V	0.04	-	%
			15	5 V	0.04	-	%
f _(-3dB)	-3 dB frequency response		5 ۱	V	13	-	MHz
		channel ON; V _I = 0.5 × V _{DD} (p-p)	10) V	40	-	MHz
				5 V	70	-	MHz
$\alpha_{\rm iso}$	isolation (OFF-state)	see Fig. 15; f_i = 1 MHz; R_L = 1 $k\Omega$; C_L = 5 pF; [1] channel OFF; V_I = 0.5 × V_{DD} (p-p)	10) V	-50	-	dB
V _{ct}	crosstalk voltage	digital inputs to switch; see Fig. 16; $R_L = 10 \text{ k}\Omega$; $C_L = 15 \text{ pF}$; E or An = V_{DD} (square-wave)	10) V	50	-	mV
Xtalk	crosstalk	between switches; see Fig. 17; f_i = 1 MHz; [1] R_L = 1 k Ω ; V_I = 0.5 × V_{DD} (p-p)	10) V	-50	-	dB

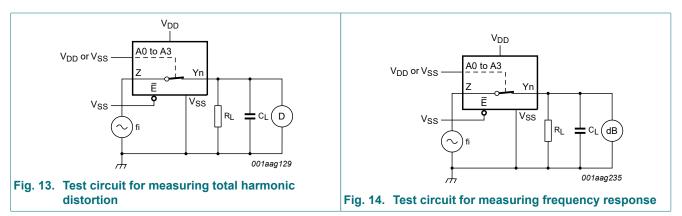
[1] f_i is biased at 0.5 × V_{DD} ; V_I = 0.5 × V_{DD} (p-p).

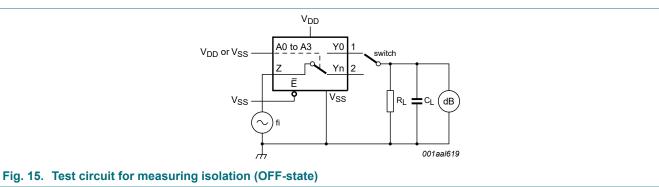
Table 12. Dynamic power dissipationP_D

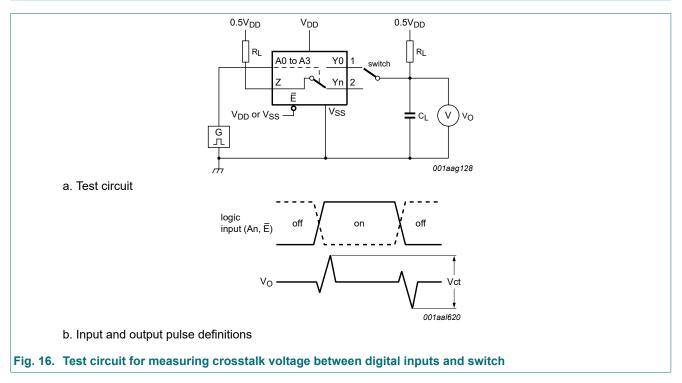
 P_D can be calculated from the formulas shown; V_{SS} = 0 V; t_r = t_f ≤ 20 ns; T_{amb} = 25 °C.

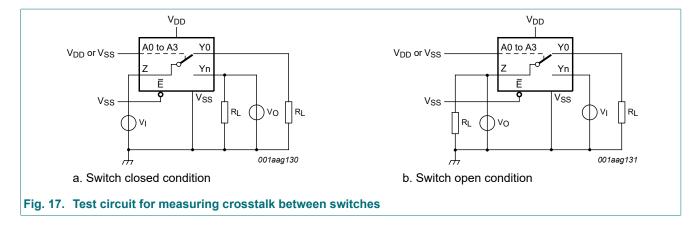
Symbol	Parameter	V_{DD}	Typical formula for P _D (μW)	where:
P _D	dynamic power dissipation	5 V	$P_{D} = 1000 \times f_{i} + \Sigma (f_{o} \times C_{L}) \times V_{DD}^{2}$	f _i = input frequency in MHz;
		10 V	$P_D = 5500 \times f_i + \Sigma (f_o \times C_L) \times V_{DD}^2$	f _o = output frequency in MHz; C ₁ = output load capacitance in pF;
		15 V	$P_D = 15000 \times f_i + \Sigma (f_o \times C_L) \times V_{DD}^2$	V_{DD} = supply voltage in V; $\Sigma(C_L \times f_0)$ = sum of the outputs.

11.2.1. Test circuits





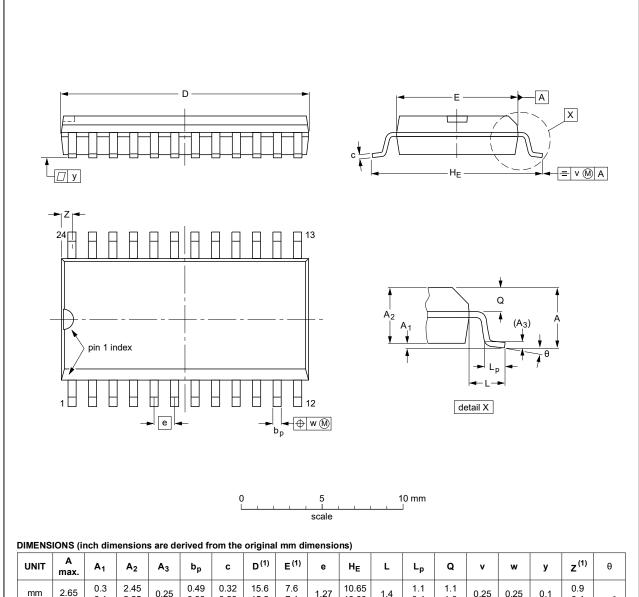




12. Package outline

SO24: plastic small outline package; 24 leads; body width 7.5 mm

SOT137-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Q	v	w	у	z ⁽¹⁾	θ
mm	2.65	0.3 0.1	2.45 2.25	0.25	0.49 0.36	0.32 0.23	15.6 15.2	7.6 7.4	1.27	10.65 10.00	1.4	1.1 0.4	1.1 1.0	0.25	0.25	0.1	0.9 0.4	8°
inches	0.1	0.012 0.004	0.096 0.089	0.01	0.019 0.014	0.013 0.009	0.61 0.60	0.30 0.29	0.05	0.419 0.394	0.055	0.043 0.016	0.043 0.039	0.01	0.01	0.004	0.035 0.016	0°

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT137-1	075E05	MS-013				99-12-27 03-02-19	

Fig. 18. Package outline SOT137-1 (SO24)

13. Abbreviations

Table 13. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MIL	Military
MM	Machine Model

14. Revision history

Table 14. Revision history

Table 14. Revision mistor	<i>3</i>			
Document ID	Release date	Data sheet status	Change notice	Supersedes
HEF4067B_Q100 v.3	20220110	Product data sheet	-	HEF4067B_Q100 v.2
Modifications:	Nexperia. • Legal texts ha	this data sheet has been redes ve been adapted to the new co Section 2 updated.		
HEF4067B_Q100 v.2	20140911	Product data sheet	-	HEF4067B_Q100 v.1
Modifications:	Fig. 16: Test circuit modified			
HEF4067B_Q100 v.1	20130924	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- 2] The term 'short data sheet' is explained in section "Definitions".
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15 / 16

Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Ordering information	1
5. Functional diagram	2
6. Pinning information	4
6.1. Pinning	4
6.2. Pin description	4
7. Functional description	5
8. Limiting values	5
9. Recommended operating conditions	6
10. Static characteristics	6
10.1. Test circuits	7
10.2. On resistance	7
10.2.1. On resistance waveform and test circuit	8
11. Dynamic characteristics	8
11.1. Waveforms and test circuit	9
11.2. Additional dynamic parameters	10
11.2.1. Test circuits	11
12. Package outline	13
13. Abbreviations	14
14. Revision history	14
15. Legal information	15

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