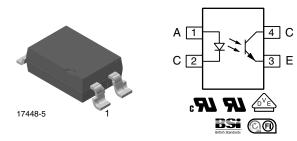


Optocoupler, Phototransistor Output, High Reliability, 5300 V_{RMS}

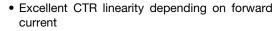


DESCRIPTION

The SFH615A-3X018T features a variety of transfer ratios. low coupling capacitance and high isolation voltage. This coupler has a GaAs infrared diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic SMD package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

FEATURES





- Isolation test voltage, 5300 V_{RMS}
- · Fast switching times
- · Low CTR degradation
- · Low coupling capacitance



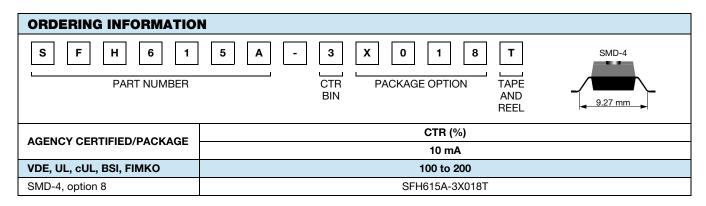
• Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Switchmode power supply
- Telecom
- Battery powered equipment

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- DIN EN 60747-5-5 (VDE 0884-5) available with option 1
- cUL tested to CSA 22.2 bulletin 5A
- BSI IEC 60950, IEC 60065
- FIMKO EN6005, EN60950-1





ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT			<u> </u>				
Reverse voltage		V_R	6	V			
DC forward current		I _F	60	mA			
Surge forward current	t _p ≤ 10 μs	I _{FSM}	2.5	А			
OUTPUT							
Collector emitter voltage		V _{CEO}	70	V			
Emitter collector voltage		V _{ECO}	7	V			
Collector current		I _C	50	mA			
Collector current	t _p ≤ 1 ms	I _C	100	mA			
COUPLER							
Isolation test voltage between emitter and detector	t = 1 s	V _{ISO}	5300	V_{RMS}			
Creepage distance			≥ 7	mm			
Clearance distance			≥ 7	mm			
Insulation thickness between emitter and detector			≥ 0.4	mm			
Comparative tracking index per DIN IEC112/VDE0303 part 1		СТІ	≥ 175				
Indiation maintains	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω			
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω			
Storage temperature range		T _{stg}	- 55 to + 150	°C			
Ambient temperature range		T _{amb}	- 55 to +100	°C			
Soldering temperature (1)	max. 10 s	T _{sld}	260	°C			

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD).

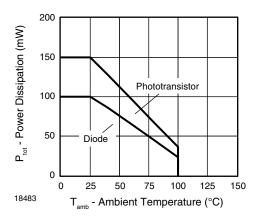


Fig. 1 - Permissible Power Dissipation vs. Ambient Temperature



THERMAL CHARACTERISTICS			
PARAMETER	SYMBOL	VALUE	UNIT
LED power dissipation	P _{diss}	100	mW
Output power dissipation	P _{diss}	150	mW
Maximum LED junction temperature	T _{jmax} .	125	°C
Maximum output die junction temperature	T _{jmax.}	125	°C
Thermal resistance, junction emitter to board	$\theta_{\sf EB}$	173	°C/W
Thermal resistance, junction emitter to case	$\theta_{\sf EC}$	149	°C/W
Thermal resistance, junction detector to board	θ_{DB}	111	°C/W
Thermal resistance, junction detector to case	θ_{DC}	127	°C/W
Thermal resistance, junction emitter to junction detector	θ_{ED}	95	°C/W
Thermal resistance, board to ambient (1)	θ_{BA}	195	°C/W
Thermal resistance, case to ambient (1)	$\theta_{\sf CA}$	3573	°C/W

Notes

The thermal model is represented in the thermal network below. Each resistance value given in this model can be used to calculate the
temperatures at each node for a given operating condition. The thermal resistance from board to ambient will be dependent on the type of
PCB, layout and thickness of copper traces. For a detailed explanation of the thermal model, please reference Vishay's thermal
characteristics of optocouplers application note.

(1) For 2 layer FR4 board (4" x 3" x 0.062")

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I _F = 60 mA	V_{F}		1.25	1.65	V	
Reverse current	V _R = 6 V	I _R		0.01	10	μA	
Capacitance	V _R = 0 V, f = 1 MHz	Co		13		pF	
OUTPUT							
Collector emitter capacitance	V _{CE} = 5 V, f = 1 MHz	C _{CE}		5.2		pF	
Collector emitter leakage current	V _{CE} = 10 V	I _{CEO}		5	100	nA	
COUPLER	•						
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 2.5 \text{ mA}$	V _{CEsat}		0.25	0.4	V	
Coupling capacitance		C _C		0.4		pF	

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Ic/I _F	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	CTR	100		200	%
	$I_F = 1 \text{ mA}, V_{CE} = 5 \text{ V}$	CTR	34	70		%



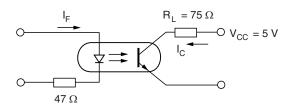
SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
NON-SATURATED							
Rise time	I_F = 10 mA, V_{CC} = 5 V, T_A = 25 °C, R_L = 75 Ω	t _r		2		μs	
Fall time	I_F = 10 mA, V_{CC} = 5 V, T_A = 25 °C, R_L = 75 Ω	t _f		2		μs	
Turn-on time	I_F = 10 mA, V_{CC} = 5 V, T_A = 25 °C, R_L = 75 Ω	t _{on}		3		μs	
Turn-off time	I_F = 10 mA, V_{CC} = 5 V, T_A = 25 °C, R_L = 75 Ω	t _{off}		2.3		μs	
Cut-off frequency	I_F = 10 mA, V_{CC} = 5 V, T_A = 25 °C, R_L = 75 Ω	f _{ctr}		250		kHz	
SATURATED							
Rise time	V_{CC} = 5 V, T_A = 25 °C, R_L = 1 k Ω , I_F = 10 mA	t _r		3		μs	
Fall time	V_{CC} = 5 V, T_A = 25 °C, R_L = 1 k Ω , I_F = 10 mA	t _f		14		μs	
Turn-on time	V_{CC} = 5 V, T_A = 25 °C, R_L = 1 k Ω , I_F = 10 mA	t _{on}		4.2		μs	
Turn-off time	V_{CC} = 5 V, T_A = 25 °C, R_L = 1 k Ω , I_F = 10 mA	t _{off}		23		μs	

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)				55/100/21		
Comparative tracking index		CTI	175		399	
V _{IOTM}		V _{IOTM}	10 000			V _{peak}
V _{IORM}		V_{IORM}	890			V _{peak}
P _{SO}		P _{SO}			400	mW
I_{SI}		I _{SI}			275	mA
T_{SI}		T _{SI}			175	°C
Creepage distance			7			mm
Clearance distance			7			mm
Insulation thickness, reinforced rated	per IEC 60950 2.10.5.1		0.4			mm

Note

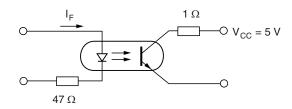
• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



isfh615a_01

Fig. 2 - Linear Operation (without Saturation)



isfh615a 02

Fig. 3 - Switching Operation (with Saturation)

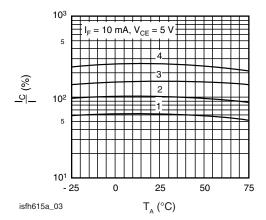


Fig. 4 - Current Transfer Ratio (Typ.) vs. Temperature

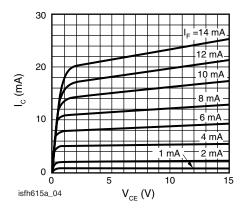


Fig. 5 - Output Characteristics (Typ.) Collector Current vs. Collector Emitter Voltage

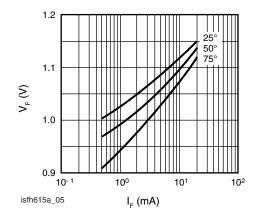


Fig. 6 - Diode Forward Voltage (Typ.) vs. Forward Current

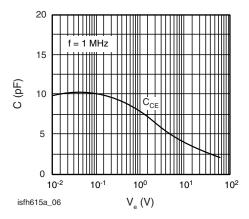


Fig. 7 - Transistor Capacitance (Typ.) vs. Collector Emitter Voltage



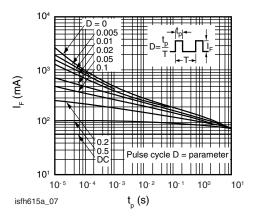
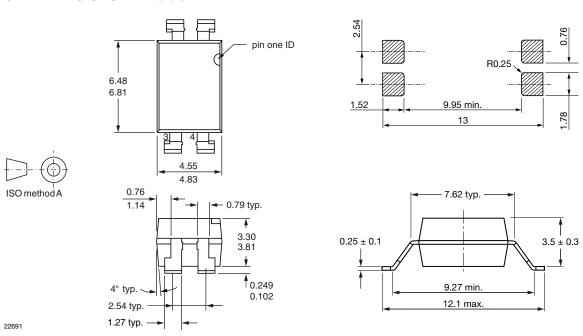


Fig. 8 - Permissible Pulse Handling Capability Forward Current vs. Pulse Width

PACKAGE DIMENSIONS millimeters



PACKAGE MARKING (example of SFH615A-3X018T)



Notes

- VDE logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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Vishay

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