Old Company Name in Catalogs and Other Documents

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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR 2SK2461

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The 2SK2461 is N-Channel MOS Field Effect Transistor designed for high speed switching applications.

FEATURES

• Low On-Resistance

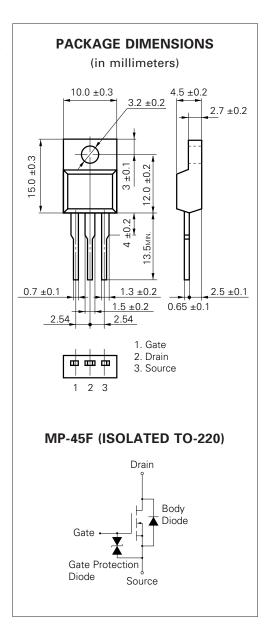
 $R_{DS(on)1} = 80 \text{ m}\Omega \text{ MAX.}$ (@ VGS = 10 V, ID = 10 A) $R_{DS(on)2} = 0.1 \Omega \text{ MAX.}$ (@ VGS = 4 V, ID = 10 A)

- Low Ciss Ciss = 1400 pF TYP.
- Built-in G-S Gate Protection Diodes
- High Avalanche Capability Ratings

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage	VDSS	100	V
Gate to Source Voltage	Vgss	±20	V
Drain Current (DC)	ID(DC)	±20	Α
Drain Current (pulse)*	D(pulse)	±80	Α
Total Power Dissipation ($T_c = 25$ °C)	P _{T1}	35	W
Total Power Dissipation (T _A = 25 °C)	P _{T2}	2.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Single Avalanche Current**	las	20	Α
Single Avalanche Energy**	Eas	40	mJ

- * PW \leq 10 μ s, Duty Cycle \leq 1 %
- ** Starting Tch = 25 °C, Rg = 25 Ω , Vgs = 20 V \rightarrow 0

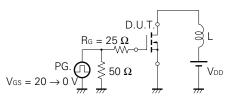


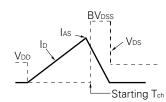


ELECTRICAL CHARACTERISTICS (TA = 25 °C)

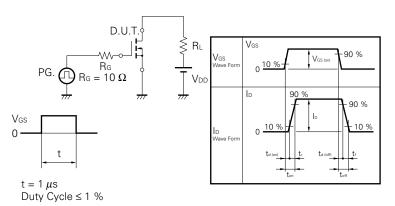
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-Resistance	RDS(on)1		58	80	mΩ	Vgs = 10 V, ID = 10 A
Drain to Source On-Resistance	RDS(on)2		70	100	$m\Omega$	Vgs = 4 V, ID = 10 A
Gate to Source Cutoff Voltage	V _{GS(off)}	1.0	1.7	2.0	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance	l yfs l	12	19		S	V _{DS} = 10 V, I _D = 10 A
Drain Leakage Current	IDSS			10	μΑ	V _{DS} = 100 V, V _{GS} = 0
Gate to Source Leakage Current	Igss			±10	μΑ	$V_{GS} = \pm 20 \text{ V, } V_{DS} = 0$
Input Capacitance	Ciss		1400		pF	V _{DS} = 10 V
Output Capacitance	Coss		470		pF	V _{GS} = 0
Reverse Transfer Capacitance	Crss		150		pF	f = 1 MHz
Turn-On Delay Time	td(on)		21		ns	ID = 10 A
Rise Time	tr		110		ns	V _{GS(on)} = 10 V
Turn-Off Delay Time	td(off)		140		ns	V _{DD} = 50 V
Fall Time	tf		110		ns	$R_G = 10 \Omega$
Total Gate Charge	QG		51		nC	ID = 20 A
Gate to Source Charge	Qgs		4.9		nC	VDD = 80 V
Gate to Drain Charge	Q _{GD}		15		nC	V _{GS} = 10 V
Body Diode Forward Voltage	V _F (S-D)		1.1		V	IF = 20 A, VGS = 0
Reverse Recovery Time	trr		170		ns	IF = 20 A, VGS = 0
Reverse Recovery Charge	Qrr		770		nC	di/dt = 100 A/μs

Test Circuit 1 Avalanche Capability

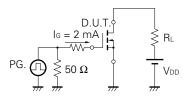




Test Circuit 2 Switching Time

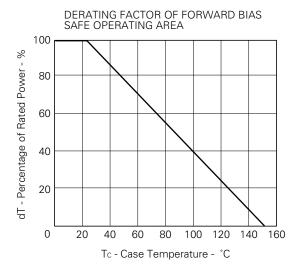


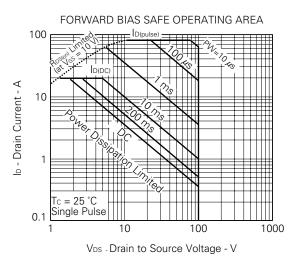
Test Circuit 3 Gate Charge

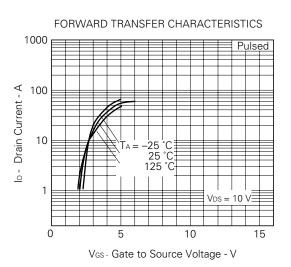


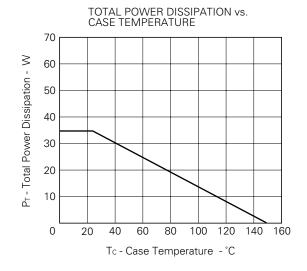
The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

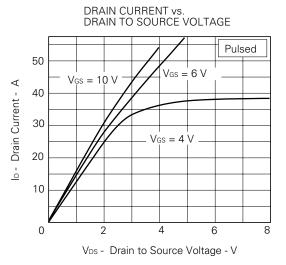
TYPICAL CHARACTERISTICS (TA = 25 °C)





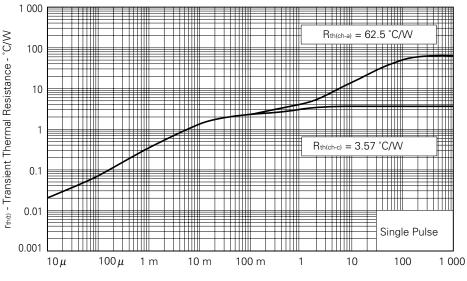






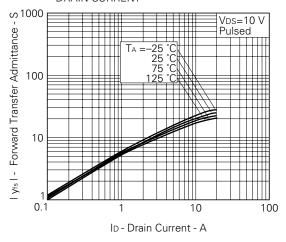
NEC

TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

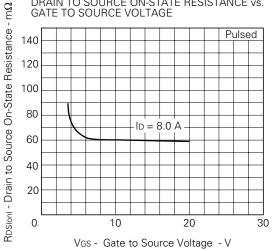


PW - Pulse Width - s

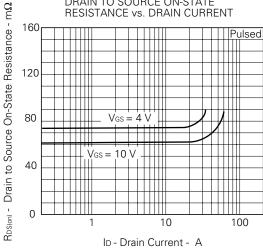
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



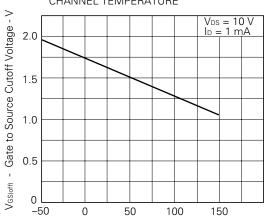
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



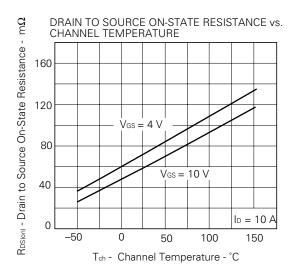
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

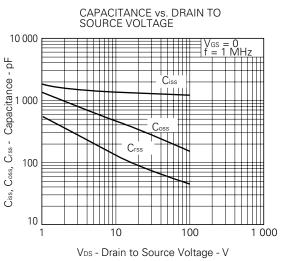


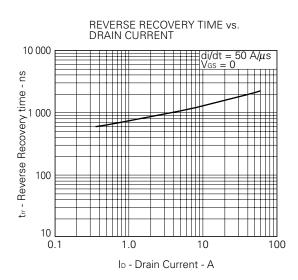
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE

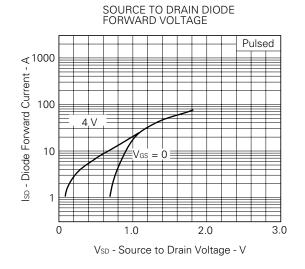


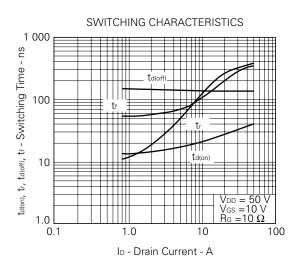
Tch - Channel Temperature - °C

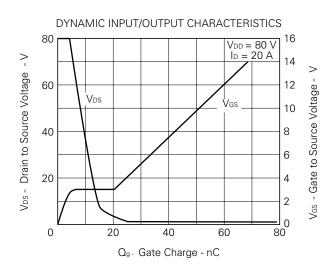




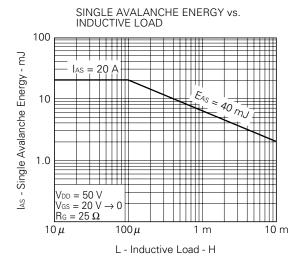


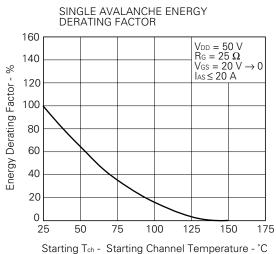














REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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