Overview

The purpose of this notification is to communicate that Xilinx is introducing an additional wafer fabrication site for all 28nm product families. Xilinx and TSMC are integrating the 28nm/HPL process as part of TSMC's 28nm super manufacturing platform (SMP) and business continuity strategy. As a result, Fab15 is being added as a qualified source for all Xilinx 28nm product family members.

This notification applies to all Artix®-7, Kintex®-7, Virtex®-7, and Zynq®-7000 (XC) Commercial (C) grade, Extended (E) grade and Industrial (I) grade products. Automotive (XA) and Hi-Rel (XQ) products are not impacted by this change as they will be qualified and released to production in TSMC SMP/Fab15.

Description

To ensure business continuity and enable high volume supply chain capabilities for 28nm products, Xilinx intends to include an additional wafer fabrication site, TSMC SMP/Fab 15, for all 28nm product families. Xilinx and TSMC are partnering to create a seamless synchronized process across wafer fabrication sites for the 28nm node with standardized HPL process manufacturing control systems. This super manufacturing platform (SMP) creates centralized alignment for manufacturing management process control.

TSMC SMP/Fab15 is being added as a qualified source for all Artix-7, Kintex-7, Virtex-7, and Zynq-7000 (XC) Commercial (C) grade, Extended (E) grade and Industrial (I) grade products. Automotive (XA) and Hi-Rel (XQ) products will be qualified and released to production in TSMC SMP/Fab15 and are not impacted by this change.

This addition allows the same performance, quality and reliability specifications that apply to all 28nm product families proven through characterization and testing. As a result, there is no change in form, fit, function, or reliability with this SMP/Fab15 addition.

Products Affected

This change affects all standard and specification control document (SCD) XC Commercial (C) grade, Extended (E) grade and Industrial (I) grade devices listed in Table 1.

Table 1: **Affected 28nm device families**

Device	Device	Device	Device	Device
XC7A100T	XC7K355T	XC7V2000T	XC7VX485T	XC7Z010
XC7A200T	XC7K410T	XC7VH580T	XC7VX550T	XC7Z020
XC7K70T	XC7K420T	XC7VH870T	XC7VX690T	XC7Z030
XC7K160T	XC7K480T	XC7VX330T	XC7VX980T	XC7Z045
XC7K325T	XC7V585T	XC7VX415T	XC7VX1140T	XC7Z100

Key Dates and Ordering Information

Xilinx will begin Fab 15 initial production device shipments to customers in the timelines indicated in Table 2 below.

Table 2: **Fab 15 Device Qualification Completion and Transition Schedule**

Device	Qualification Completion	Cross-Ship Date
XC7A100T	Dec-13	Apr-14
XC7A200T	Apr-14	Jul-14
XC7K70T	Jun-14	Sep-14
XC7K160T	May-14	Aug-14
XC7K325T	Dec-13	Apr-14
XC7K355T	Jun-14	Sep-14
XC7K410T	Jun-14	Sep-14
XC7K420T	May-14	Aug-14
XC7K480T	May-14	Aug-14
XC7V585T	Jul-14	Oct-14
XC7V2000T	Jun-14	Sep-14
XC7VH580T	Jul-14	Oct-14
XC7VH870T	Jul-14	Oct-14
XC7VX330T	Jul-14	Oct-14
XC7VX415T	Jun-14	Sep-14
XC7VX485T	Jan-14	Jul-14
XC7VX550T	Feb-14	May-14
XC7VX690T	Feb-14	July-14
XC7VX980T	Jun-14	Sep-14
XC7VX1140T	Jul-14	Oct-14
XC7Z010	Dec-13	Apr-14
XC7Z020	Dec-13	Apr-14
XC7Z030	Mar-14	Jun-14
XC7Z045	Mar-14	Jun-14
XC7Z100	Apr-14	Jul-14

Note: All subsequent updates to this table will be provided in [XTP262](XTP262).

Traceability

The package top mark will remain the same with only a single letter change to reflect the Fabrication location.

Figure 1: **Top Mark for TSMC Fab 12**

Figure 2: **Top Mark for TSMC Fab 15**

Qualification Data

Qualification data is available and provided upon request.

Response

No response is required. For additional information or questions, please contact Xilinx Technical Support.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (http://www.xilinx.com/support). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Answer Record 18683: http://www.xilinx.com/support/answers/18683.htm.

Additional Documentation

XTP262: Transition Schedule for Additional Wafer Fabrication Site for 28nm Product Families
https://secure.xilinx.com/webreg/clickthrough.do?cid=354251.

Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
12/16/2013	1.0	Initial release.
03/24/2014	1.1	Add new traceability section for top mark information. Update XC7VX690T cross-ship date from TBD to July, 2014 on Table 2.

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