

Features

- Epitaxial Planar Die Construction
 - Complementary NPN Types Available (DDTC)
 - Built-In Biasing Resistors, R1 only
 - **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
 - **Halogen and Antimony Free. "Green" Device (Note 3)**
 - **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.**
- <https://www.diodes.com/quality/product-definitions/>

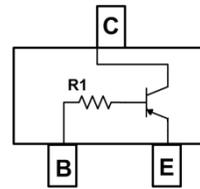
Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 Ⓔ³
- Weight: 0.006 grams (Approximate)

Part Number	R1 (NOM)
DDTA113TUA	1kΩ
DDTA123TUA	2.2kΩ
DDTA143TUA	4.7kΩ
DDTA114TUA	10kΩ
DDTA124TUA	22kΩ
DDTA144TUA	47kΩ
DDTA115TUA	100kΩ
DDTA125TUA	200kΩ



Top View



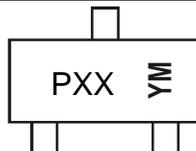
Device Schematic – Top View

Ordering Information (Note 4)

Part Number	Status	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DDTA113TUA-7-F	Active	Standard	P01	7	8	3,000
DDTA123TUA-7-F	Active	Standard	P03	7	8	3,000
DDTA143TUA-7-F	Active	Standard	P07	7	8	3,000
DDTA114TUA-7-F	Active	Standard	P12	7	8	3,000
DDTA124TUA-7-F	Active	Standard	P16	7	8	3,000
DDTA144TUA-7-F	Active	Standard	P19	7	8	3,000
DDTA115TUA-7-F	Active	Standard	P23	7	8	3,000
DDTA125TUA-7-F	Obsolete	Standard	P25	7	8	3,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



PXX = Product Type Marking Code (See Table above)
 YM = Date Code Marking
 Y = Year (ex: I = 2021)
 M = Month (ex: 9 = September)

Date Code Key

Year	2010	...	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	X	...	I	J	K	L	M	N	O	P	R	S

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CB0}	-50	V
Collector-Emitter Voltage	V _{CEO}	-50	V
Emitter-Base Voltage	V _{EBO}	-5	V
Collector Current	I _C (Max)	-100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	200	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CB0}	-50	—	—	V	I _C = -50μA
Collector-Emitter Breakdown Voltage	BV _{CEO}	-50	—	—	V	I _C = -1mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-5	—	—	V	I _E = -50μA
Collector Cutoff Current	I _{CB0}	—	—	-0.5	μA	V _{CB} = -50V
Emitter Cutoff Current	I _{EBO}	—	—	-0.5	μA	V _{EB} = -4V
Collector-Emitter Saturation Voltage	V _{CE(sat)}	—	—	-0.3	V	I _C /I _B = -10mA/-1mA DDTA113TUA I _C /I _B = -5mA/-0.5mA DDTA123TUA I _C /I _B = -2.5mA/-0.25mA DDTA143TUA I _C /I _B = -1mA/-0.1mA DDTA114TUA I _C /I _B = -5mA/-0.5mA DDTA124TUA I _C /I _B = -2.5mA/-0.25mA DDTA144TUA I _C /I _B = -1mA/-0.1mA DDTA115TUA I _C /I _B = -0.5mA/-0.05mA DDTA125TUA
DC Current Transfer Ratio	h _{FE}	100	250	600	—	I _C = -1mA, V _{CE} = -5V
Input Resistor (R ₁) Tolerance	ΔR ₁	-30	—	+30	%	—
Gain-Bandwidth Product (Note 6)	f _T	—	250	—	MHz	V _{CE} = -10V, I _E = -5mA, f = 100MHz

Notes: 5. Mounted on FR4 PC Board with minimum recommended pad layout.
6. Transistor - For Reference Only

Typical Characteristics – DDTA114TUA (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

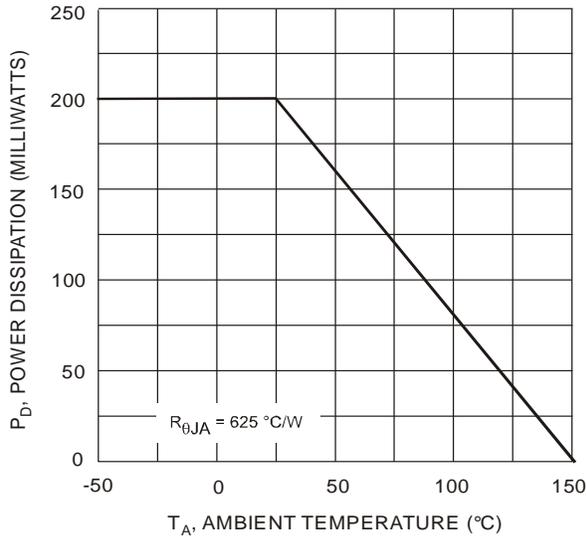


Fig. 1 Derating Curve

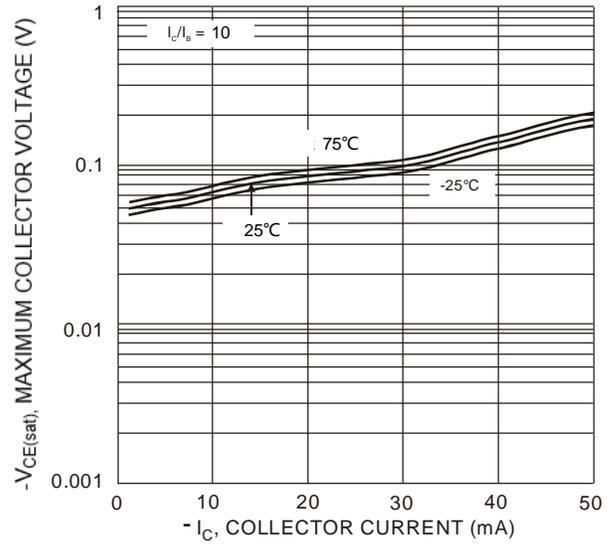


Fig. 2 $V_{CE(sat)}$ vs. I_C

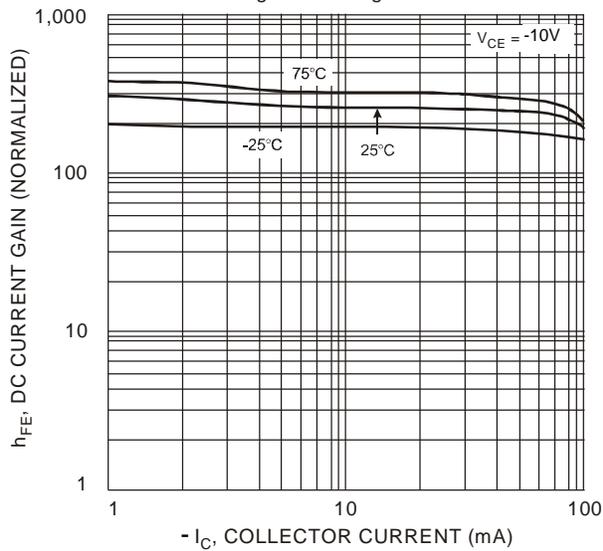


Fig. 3 DC Current Gain

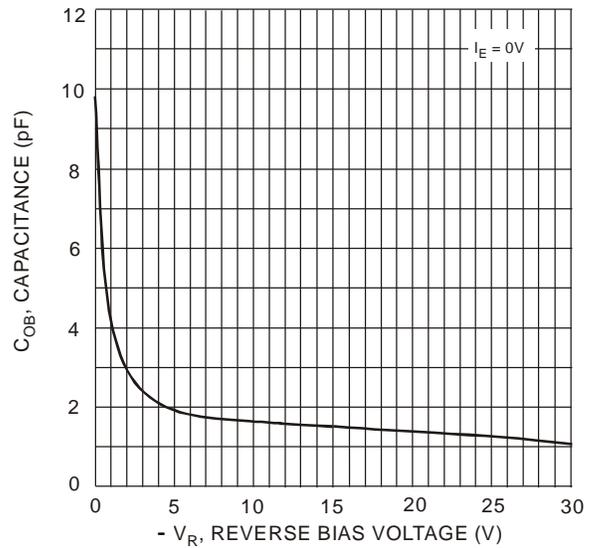


Fig. 4 Output Capacitance

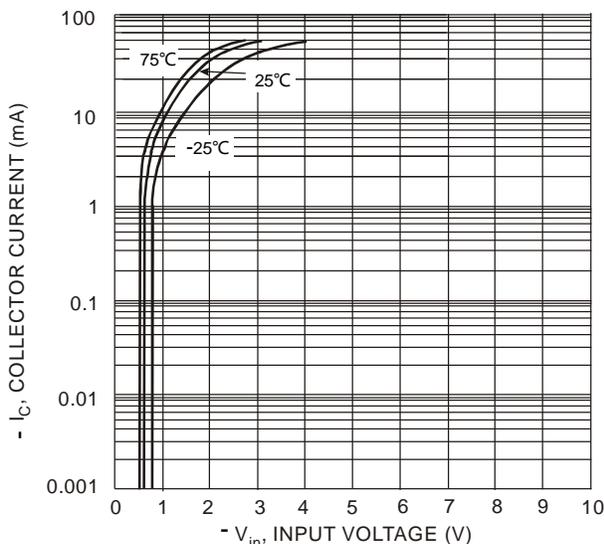


Fig. 5 Collector Current vs. Input Voltage

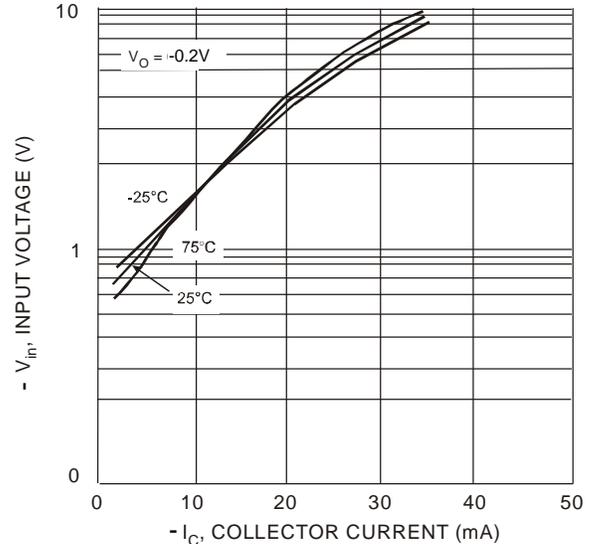
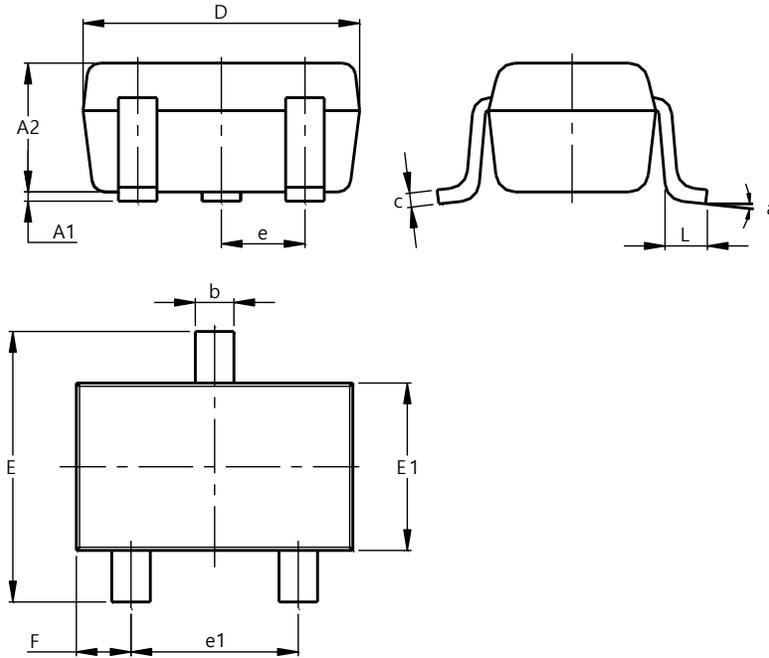


Fig. 6 Input Voltage vs. Collector Current

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT323

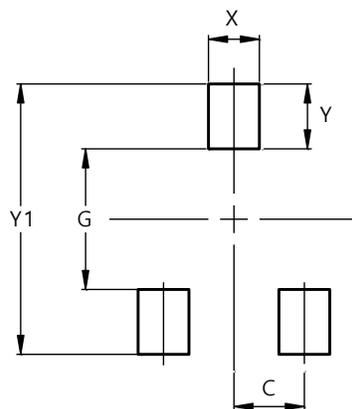


SOT323			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.25	0.40	0.30
c	0.10	0.18	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
e1	1.20	1.40	1.30
F	0.375	0.475	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT323



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500

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