

EMIF04-MMC02F2

4-line IPAD[™], EMI filter including ESD protection

Features

- EMI symmetrical (I/O) low-pass filter
- High efficiency in EMI filtering
- Lead-free package
- Very low PCB space occupation: 1.57 mm x 2.07 mm
- Very thin package: 0.65 mm
- High efficiency in ESD suppression
- High reliability offered by monolithic integration
- High reduction of parasitic elements through integration and wafer level packaging

Complies with the standards:

- IEC 61000-4-2 Level 4
 - 15 kV (air discharge)
 - 8 kV (contact discharge)

Application

Where EMI filtering in ECD sensitive equipment is required:

 MultiMediaGaid for mobile phones, personal digital assistant, digital camera, MP3 players...

Description

The EMIF04-MMC02 is a highly integrated device designed to suppress EMI/RFI noise for a MultiMediaCard port. The EMIF04 Flip Chip packaging means the package size is equal to the die size.

This filter includes ESD protection circuitry, which prevents damage to the application when it is subjected to ESD surges up to 15 kV.











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1 Electrical characteristics

Table 1.	Absolute maximum ratings (T _{amb} = 25 °C)

Symbol	Parameter	Value	Unit
P _R	DC power per resistor	70	mW
Тj	Junction temperature	125	°C
T _{op}	Operating temperature range	-40 to + 85	°C
T _{stg}	Storage temperature range	-55 to +150	°C

Table 2.	
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Symbol	Parameters	
V_{BR}	Breakdown voltage	
I _{BM}	Leakage current @ V _{BM}	20

Electrical characteristics (T_{amb} = 25 $^{\circ}$ C)

	V _{BR}	Breakdown voltage				† i		
	I _{RM}	Leakage current @ V _{RM}	010					
	V _{RM}	Stand-off voltage						
	V _{CL}	Clamping voltage		V _{BR}	V _{RM}		\downarrow v	
	R _d	Dynamic impedance				: I _{RM} I _R		
	I _{PP}	Peak pulse current						
	R _{I/O}	Series resistance between input and output		slope : 1 / Rd		I _{PP}	I _{PP}	
	Cline Input capacitance, ei line							
Symbol		O lest conditions	М	in	Тур	Max	Unit	
	V _{BR} i _F = 1 mA		6	6			V	
	I _{R.} V _{RM} = 3 V				0.1	0.5	μA	
C _{line} R ₁ ,R ₂ ,R ₃ ,R ₄ R ₁₀		@ 0 V				20	pF	
		Tolerance ± 5%			47		Ω	
		Tolerance ± 5%			13		kΩ	
05	R ₂₀	Tolerance ± 5%			56		kΩ	
		·	•			•		



Figure 3. S21 (dB) attenuation measurement Figure 4. Cross talk measurement and Aplac simulation





Figure 6. ESD response to NFC 61000-4-2 (-15kV contact cischarge) on one input (Vin) and one output (Vout)







2 Application information









3 Ordering information scheme

Figure 10.	Ordering	information	scheme
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4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, (rand definitions and product status are available at: <u>www.st.com</u>. ECOPACK[®] is an ST trademark.











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Ordering information

Table 3.Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
EMIF04-MMC02F2	FH	Flip Chip	4.5 mg	5000	Tape and reel (7")

Note:

More packing information is available in the applications note: AN1235: "Flip Chip: package description and recommendations for use" AN 1751: "EMI filters: Recomendations and measurements"



6 Revision history

Table 4.Document revision history

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Date	Revision	Changes	
14-Oct-2004	1	First issue	
06-Apr-2005	2	Minor layout update. No content change.	
25-Aug-2005	3	Reformatted to current standard, Aplac model updated in section 2.	
28-Apr-2008	4	Updated ECOPACK statement. Updated <i>Figure 10</i> , <i>Figure 11</i> and <i>Figure 14</i> . Reformatted to current standards.	
27-May-2011	5	Updated Figure 11 for die dimensions. Updated Figure 14 for pocket scale.	
tepr	99ne	Updated ECOPACK statement. Updated <i>Figure 10, Figure 11</i> and <i>Figure 14</i> . Reformatted to current standards. Updated <i>Figure 11</i> for die dimensions. Updated <i>Figure 14</i> for pocket svale.	



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